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PATENT PATENT REEL: 9248 FRAME: 0839 % 5

ASSIGNMENT

(from Inventor to NEC USA, Inc.)

WHEREAS, I, Anand Raghunathan

hereinafter called assignor(s), jointly winvented certain improvements in				
	vith Sujit Dey, Ganes	h Lakshminarayana a	and Niraj K. Jha have	
CONSTRAINED REGISTER SHARING T	TECHNIQUE FOR LOV	V POWER VLSI DESI	GN	
and have executed an application for L; and	etters Patent of the	United States of Am	erica therefor on	
WHEREAS,	NEC USA, Inc. C&C Research Labor 4 Independence Way Princeton, NJ 0854	/		
(assignee) desires to acquire the entire United States of America, and to any l	_			
NOW THEREFORE, for valuable	e consideration, rece	pt whereof is hereby	acknowledged,	
I, the above named assignor(s), its successors and assigns, the entire disclosed therein for the United States Patents to issue any Letters Patent grassignee, its successors and assigns consideration all papers deemed nece application(s) when called upon to do s	e right, title, and inte of America, and I re anted upon the inver s; and I hereby ag essary by the assign	erest in the application equest the United Stantion set forth in the ree that I will exec	on and the invention tes Commissioner of application(s) to the cute without further	
I hereby authorize and request of 2100 Pennsylvania Avenue, NW, (Application Number, filed application when known. Date: 5/7/98	Washington, DC 20	037-3202 to insert filing date and applic	here in parentheses	
of 2100 Pennsylvania Avenue, NW, (Application Number, filed application when known. Date: 5 7 98	Washington, DC 20 the Signature	037-3202 to insert filing date and applic	here in parentheses ation number of said	
of 2100 Pennsylvania Avenue, NW, (Application Number, filed application when known. Date: 5/7/98	Washington, DC 20 the the Signature ANAND	037-3202 to insert filing date and applic	RAGHUNATHAN	
of 2100 Pennsylvania Avenue, NW, (Application Number, filed application when known. Date: 5/7/98	Washington, DC 20 the Signature	037-3202 to insert filing date and applic	here in parentheses ation number of said	

ASSIGNMENT

(from Inventor to NEC USA, Inc.)

WHEREAS, I, Sujit Dey

hereinafter called assignor(s), jointly Jha have invented certain improvement	with Anand Raghunathan, Ganesh Lakshmin ents in	arayana and Niraj K.
CONSTRAINED REGISTER SHARING	TECHNIQUE FOR LOW POWER VLSI DESIG	in
and have executed an application for; and	Letters Patent of the United States of Ame	rica therefor on
WHEREAS,	NEC USA, Inc. C&C Research Laboratories 4 Independence Way Princeton, NJ 08540	
	re right, title, and interest in the application a United States patents to be obtained there	
NOW THEREFORE, for valuab	ple consideration, receipt whereof is hereby	acknowledged,
its successors and assigns, the entitidisclosed therein for the United State Patents to issue any Letters Patent gassignee, its successors and assig consideration all papers deemed negaplication(s) when called upon to do	our attorneys SUGHRUE, MION, ZINN, MAC , Washington, DC 20037-3202 to insert f	n and the invention es Commissioner of application(s) to the ute without further in the United States PEAK & SEAS, PLLC mere in parentheses
NOTARY	S' V J 17 First Name Middle Initial	
State of CA () County of San Diego () ss:		
, to me personally known, as	nd known to me to be the person described is knowledged to me that he executed the same	n and who executed
DEBORAH C. TOBIAS Comm. # 1157625	Notary	

PATENT

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