

07-09-1998

PATENTS ONLY

MRD 6-24-98

P



100757700

d Trademarks:
or copy thereof.

1132822-0001

1. Name of Party(ies) conveying an interest:
Fairchild Semiconductor Corporation

Additional name(s) of conveying party(ies) attached?

☐

Yes

☒

No

2. Name and Address of Party(ies) receiving an interest:

Name: Bankers Trust Company
~~Internal Address~~ (as Collateral Agent)

Street Address: 130 Liberty Street

City: New York

State: NY Zip: 10006

Additional name(s) & addresses attached?

☐

Yes

☒

No

JUN 24 1998

3. Description of the interest conveyed:

☐

Assignment

☐

Change of Name

☒

Other Assignment of

Security Interest

☐

Security Agreement

☐

Merger

Execution Date: December 31, 1997

4. Application number(s) or patent number(s). Additional sheet attached? ☐ Yes ☒ No

If this document is being filed together with a new application, the execution date of the application is:

Date
08/730,591; 08/838,108;
A. Patent Application No.(s) 08/622,098; 08/672,488;
08/948,196 08/672,487 08/851,972
08/056,094 08/649,148 08/838,109
08/959,781 08/727,818 08/842,298

B. Patent No.(s)
5,663,771

5. Name and Address of party to whom correspondence concerning document should be mailed:

Name: White & Case LLP

Internal Address: Paul Alfaks, Esq.

Street Address: 1155 Avenue of the Americas

City: New York

State: NY Zip: 10036

6. Number of applications and patents involved:

14 total

7. Amount of fee enclosed ~~or authorized to be charged~~
checks for 480 \$ total of \$560 enclosed
and 80

8. Deposit account number (Attach duplicate copy of this form if paying by deposit account):

DO NOT USE THIS SPACE

9. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

07/08/1998 DCDATES 00000280 08730591

01 FC:581

560.00 OP

06/22/98

Date

Paul Alfaks

Signature

Paul Alfaks

Name of Person Signing (Sign No)

PATENT

REEL: 9279 FRAME: 0512

**ASSIGNMENT OF SECURITY INTEREST
IN UNITED STATES PATENTS**

FOR GOOD AND VALUABLE CONSIDERATION, receipt and sufficiency of which are hereby acknowledged, Fairchild Semiconductor Corporation of California, a Delaware corporation (the "Assignor") with principal offices at 333 Western Avenue, South Portland, Maine 04106, hereby assigns and grants to Bankers Trust Company, as Collateral Agent, with principal offices at 130 Liberty Street, New York, New York 10006 (the "Assignee"), a security interest in (i) all of the Assignor's right, title and interest in and to the United States patents and pending patent applications (the "Patents") set forth on Schedule A attached hereto, (ii) all Proceeds (as such term is defined in the Security Agreement referred to below) and products of the Patents, and (iii) all causes of action arising prior to or after the date hereof for infringement of any of the Patents or unfair competition regarding the same.

THIS ASSIGNMENT OF SECURITY INTEREST is made to secure the satisfactory performance and payment of all the Obligations of the Assignor, as such term is defined in the Security Agreement among the Assignor, the other assignors from time to time party thereto and the Assignee, dated as of March 11, 1997 (as amended from time to time, the "Security Agreement"). Upon termination of the Security Agreement pursuant to Section 10.8 thereof, the Assignee shall, upon such satisfaction, execute, acknowledge, and deliver to the Assignor an instrument in writing releasing the security interest in the Patents acquired under this Assignment of Security Interest.

0000HLWP.W51

This Assignment of Security Interest has been granted in conjunction with the security interest granted to the Assignee under the Security Agreement. The rights and remedies of the Assignee with respect to the security interest granted herein are without prejudice to, and are in addition to those set forth in the Security Agreement, all terms and provisions of which are incorporated herein by reference. In the event that any provisions of this Assignment of Security Interest are deemed to conflict with the Security Agreement, the provisions of the Security Agreement shall govern.

0000HLWP.W51

PATENT
REEL: 9279 FRAME: 0514

IN WITNESS WHEREOF, the undersigned have executed this
Assignment of Security Interest as of the 31st day of December, 1997.

FAIRCHILD SEMICONDUCTOR
CORPORATION OF CALIFORNIA,
Assignor

By MW Towse
Name: MW TOWSE
Title: TREASURER

BANKERS TRUST COMPANY,
as Collateral Agent, Assignee

By _____
Name:
Title:

0000HLWP.W51

PATENT
REEL: 9279 FRAME: 0515

IN WITNESS WHEREOF, the undersigned have executed this
Assignment of Security Interest as of the 31st day of December, 1997.

FAIRCHILD SEMICONDUCTOR
CORPORATION OF CALIFORNIA,
Assignor

By _____
Name:
Title:

BANKERS TRUST COMPANY,
as Collateral Agent, Assignee

By Mary Kay Coyle
Name: MARY KAY COYLE
Title: Managing Director

0000HLWP.W51

PATENT
REEL: 9279 FRAME: 0516

STATE OF MAINE)
) ss.:
COUNTY OF CUMBERLAND)

On this 31st day of December, 1997, before me personally came
Matthew W. Towse who, being by me duly sworn, did state as follows: that
he is Treasurer of Fairchild Semiconductor Corporation of
California, that he is authorized to execute the foregoing Assignment of Security
Interest on behalf of said corporation and that he did so by authority of the Board of
Directors of said corporation.

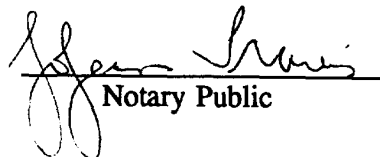
Kleane L. Louyos
Notary Public
My commission expires 8-11-03

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PATENT
REEL: 9279 FRAME: 0517

STATE OF NEW YORK)
) ss.:
COUNTY OF NEW YORK)

On this 31st day of December, 1997, before me personally came
MARY KAT COYLE who, being by me duly sworn, did state as follows: that
she is MANAGING DIRECTOR of Bankers Trust Company, that he is authorized to
execute the foregoing Assignment of Security Interest on behalf of said corporation and
that he did so by authority of the Board of Directors of said corporation.


Notary Public

JOJEAN TRAVIS
Notary Public, State of New York
No. 01TR5013241
Qualified in New York County
Commission Expires July 15, 1999

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PATENT
REEL: 9279 FRAME: 0518

SCHEDULE A

PATENT

PATENT NO.

ISSUE DATE

0000HLWP.W51

PATENT
REEL: 9279 FRAME: 0519

Raytheon Registered Intellectual Property Owned by the CompanyPatent Schedule 3.5 (a) Cases:-

CASE #	TITLE	INVENTOR(S)	COUNTRY	PATENT NO. / APPLN. NO.	FILING DATE	ASSIGNEE RAYTHEON OTHER
36907	Scan Converter	Oakley, D.	US	08/948,196	*	TRW, Inc.
31839	Analog Multiplier	Cate, et. al.	Canada	1,113,160	11/24/89	TRW, Inc.
35955	Multiplier ICS	Muramatsu, et. al.	Canada	1,110,773	ABANDONED	TRW, Inc.
35957	A/D Converter	Miller, et. al.	Canada	1,139,447	1/11/83	TRW, Inc.
36710	Switching Regulator	Wong H	China	97104935	3/26/97	
35955	Multiplier Accumulator	Muramatsu, et. al.	France	7905941	ABANDONED	
35956	Multiplier ICS	Muramatsu, et. al.	France	7916860	*	
35957	A/D Converter	Miller, et. al.	France	8006091	ABANDONED	
35956	Multiplier ICS	Muramatsu, et. al.	Germany	P2945246	*	TRW, Inc.
35955	Multiple Accumulator	Muramatsu, et. al.	Germany	P2911096.1- 53	3/21/79 ABANDONED	
34990	Auto-Zero AMP	Vinn. C.	Japan	161166/90	6/14/90	
35343	Offset Voltage Adj Circuit	Vinn, C.	Japan	UM03336	*	

PATENT

REEL: 9279 FRAME: 0520

CASE #	TITLE	INVENTOR(S)	COUNTRY	PATENT NO. / APPLN. NO.	FILING DATE	ASSIGNEE RAYTHEON OTHER
33287	Off Set Adjust Circuit	Flink, J.	Japan	334644	*	
35343	Low Z Current Comparator	Gill, H.	Japan	347050/91	12/27/91	
33287	Trans Switch with Hyster	Gill, et. al.	Japan	1847069	*	
33610	EGA Process Sequence	Mehrotra, et. al.	Japan	Grant. 1809724	12/10/93	
33884	Logic Gate/Swit Current	Vinn, et. al.	Japan	Grant. 1962890	8/25/95	
33972	Kelvin Zener	Gill, et. al.	Japan	Grant. 1896101	1/23/95	
34268	VF Memory Cell	Shaw, et. al.	Japan	Grant. 2092746	9/18/96	
35956	Multiplier ICS	Muramatsu, et. al.	Sweden	444234	*	
36716	DC-DC Conv Controller	Bryson, et. al.	Taiwan	86108808	6/24/97	
36710	Switching Regulator	Wong H	Taiwan	86/107966	6/10/97	
36710	Switching Regulator	Wong H	United Kingdom	97/05574.3	3/18/97	
36017	Logic Level Compensator	Lee, et. al.	US	08/056,094	*	
36288	Programmable Filters	Ritter, D.	US	08/959,781	10/29/97	
36716	DC-DC Conv Controller	Bryson, et. al.	US	08/672,487 con.	6/26/96	

CASE #	TITLE	INVENTOR(S)	COUNTRY	PATENT NO. / APPLN. NO.	FILING DATE	ASSIGNEE RAYTHEON OTHER
36718	Adaptive Notch Filter	Raby, D.	US	08/649148	5/14/96	
36789	Large Swing Down Converter	Stoichita,	US	08/727,818	9/30/96	
36865	Programmable Step Down...	Bryson, et. al.	US	08/851,972	5/6/97	
*36864	CMOS Rail-to-Rail Input/Out...	Zhang, et. al.	US	08/838,109	4/15/97	
36918	Dual Adj Voltage Regulator	Bryson, S.	US		*	
36919	Programmable Active Filter	Ritter, et. al.	US	08/842,298?	5/5/97	
36717	Comb Filter Architecture	Raby, D.	US	08/664032	6/13/96	
				5,663,771	9/2/97 Grant	
36711	Decoder VRS Signal	Raby, D.	US	Draft Stage	*	
36710	Switching Regulator	Wong H	US	08/622,098	3/29/96	
36748	Popless Amplifier	Vinn, C.	US	08/672,488	6/26/96	
36788	VGA to Video Converter	Sani, et. al.	US	08/730,591	10/15/96	
36863	CMOS Source Coupled	Zhang, et. al.	US	08/838,108	4/15/97	

NOTE: * = Information not available at this time.

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