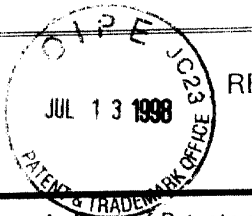


FORM PTO-1595

1-31-92

Docket



07-22-1998



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EET

U.S. DEPARTMENT OF COMMERCE

Patent and Trademark Office

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Yutaka TERADA
Hironori AKAMATSU

Additional name(s) of conveying party(ies) attached? Yes No

7-13-98

2. Name and address of receiving party(ies):

Name: Matsushita Electric Industrial Co., Ltd.
Street Address: 1006, Oaza Kadoma, Kadoma-shi,
City: Osaka State: Japan ZIP: 571-8501

Additional name(s) & address(es) attached?

Yes No

3. Nature of conveyance:

- Assignment Merger
- Security Agreement Change of Name
- Other _____

Execution Date: May 14, 1998

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is:

A. Patent Application No.(s) 09/066,234

B. Patent No.(s)

Additional numbers attached? Yes No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Neil A. DuChes, Esq.
Internal Address: RENNER, OTTO,
BOISSELLE & SKLAR, P.L.L.
Street Address: 1621 Euclid Ave., 19th Floor
City: Cleveland State: Ohio ZIP: 44115

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41):.....\$ 40.00

Enclosed

Authorized to be charged to deposit account

8. Deposit account number: _____

DO NOT USE THIS SPACE

9. Statement and signature

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Neil A. DuChes
Name of Person Signing

Mark D. Saralino
Signature

July 10, 1998
Date

Reg. No. 34,243 Total number of pages including cover sheet, attachments and document: 4

ASSIGNMENT

WHEREAS, The ASSIGNOR, comprising the following named inventor(s):

1. Yutaka TERADA; 1-9-3-406, Nasuzukuri, Hirakata-shi
Osaka Japan
2. Hironori AKAMATSU; 1-32-30-504, Minamikuzuha,
Hirakata-shi, Osaka Japan

has invented and owns a certain invention entitled:

SEMICONDUCTOR INTEGRATED CIRCUIT SYSTEM
AND METHOD FOR REDUCING A SKEW BETWEEN A CLOCK SIGNAL
AND A DATA SIGNAL

for which application for Letters Patent of the United States was filed on April 24, 1998, and assigned Serial No. 09/066,234, and,

WHEREAS, the ASSIGNEE:

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.
1006, Oaza Kadoma, Kadoma-shi,
Osaka 571-8501 Japan

is desirous of acquiring the entire interest in, to and under said invention and the United States Letters Patent to be obtained therefor;

NOW, THEREFORE, TO ALL WHOM IT MAY CONCERN: Be it known that in consideration of the payment by ASSIGNEE to

ASSIGNOR of the sum corresponding to One Dollar (\$1.00), the receipt of which is hereby acknowledged, and for other good and valuable consideration, ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the full and exclusive right, title and interest to said invention and all Letters Patent of the United States to be obtained therefor on said application or any continuation, division, renewal, substitute or reissue thereof for the full term or terms for which the same may be granted.

ASSIGNOR hereby covenants that no assignment sale agreement charge or other encumbrance has been or will be entered into which would conflict with this assignment and sale;

ASSIGNOR further covenants that ASSIGNEE will, upon its request, be provided promptly with all pertinent facts and documents relating to said application, said invention and said Letters Patent as may be known and accessible to ASSIGNOR and will testify as to the same in any interference or litigation related thereto and will promptly execute and deliver to ASSIGNEE or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain and enforce said application, said invention and said Letters Patent which may be necessary or desirable to carry out the purposes hereof.

IN WITNESS WHEREOF, the parties have hereunto set their hands and seal

this 14th day of May, 1998.

1. Yutaka Terada
(Yutaka TERADA)

2. Hironori Akamatsu
(Hironori AKAMATSU)