

07-22-1998

U.S. DEPARTMENT OF COMMERCE  
ET Patent and Trademark Office  
Attorney Docket No: TS2112



100771024

**To the Honorable Assistant Commissioner for Patents: Please record the attached original documents or copy thereof.**

**1. Name of conveying party:**

Kwong-Jr Tsai

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

### 3. Nature of conveyance:

☒ Assignment      ☐ Merger  
☐ Security Agreement      ☐ Change of Name  
☐ Other \_\_\_\_\_

Execution date: June 18, 1998

**2. Name and address of receiving party:**

Name: Vanguard International Semiconductor Corporation  
Address: 123, Park Ave-3rd., Science-Based Industrial Park

City: Hsinchu Country: Taiwan, R.O.C.

Additional name(s) & address(es) attached? ☐ Yes ☒ No

4. Application number(s) or patent number(s):

**A. Patent Application No(s).**

(09/113974

**B. Patent No(s).**

Additional numbers attached? ☐ Yes ☒ No

If this document is being filed together with a new application, the execution date of the application is: June 18, 1998

5. Name and address of party to whom correspondence concerning document should be mailed:

**Bruce E. O'Connor, Esq.**  
**CHRISTENSEN O'CONNOR**  
**JOHNSON & KINDNESS<sup>PLLC</sup>**  
**1420 Fifth Avenue**  
**Suite 2800**  
**Seattle, WA 98101-2347**  
**(206) 682-8100**

6. Total number of applications and patents involved: 1

7. Total fee (37 C.F.R. 3.41):..... **\$ 40.00**

Check No. 093300 in the amount of \$40.00 is enclosed.

8. The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16, 1.17 and 1.18 which may be required during the entire pendency of the application, or credit any overpayment, to Deposit Account No. 03-1740. This authorization also hereby includes a request for any extensions of time of the appropriate length required upon the filing of any reply during the entire prosecution of this application. A copy of this sheet is enclosed.

**DO NOT USE THIS SPACE**

9. Statement and signature:

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

**Bruce E. O'Connor**  
**Registration No. 24,849**  
**Direct Dial (206) 224-0700**

**Signature**

Date \_\_\_\_\_

Total number of pages including cover sheet, attachments and document: 3

OMB No. 0651-0011 (exp. 4/94)

## ASSIGNMENT

This assignment agreement is applicable to an invention entitled (Invention Title) \_\_\_\_\_

METHOD TO MANUFACTURE METAL GATE OF INTEGRATED CIRCUITS

The PATENT RIGHTS referred to in this agreement are:

(check one) ☒ a patent application for this invention, executed by the ASSIGNOR(S)  
concurrently with this assignment.

☐ U.S. patent application Serial No. \_\_\_\_\_, filed \_\_\_\_\_

☐ a U.S. patent application based on PCT International Application  
No. \_\_\_\_\_ filed on (date) \_\_\_\_\_ (U.S. patent application  
Serial No. \_\_\_\_\_, if known).

☐ U.S. patent No. \_\_\_\_\_, issued \_\_\_\_\_

The PATENT RIGHTS also include all divisions reissues, continuations and extensions of  
the patents and patent applications identified above.

The PATENT RIGHTS assigned under this agreement are:

(check one) ☒ U.S. patent rights only.

☐ Worldwide patent rights. In this case, the assignee shall have the right to  
claim the benefit of the filing date of any U.S. or foreign patent application  
for this invention.

The ASSIGNOR(S) referred to in this agreement is (or are) the inventor(s) whose signatures  
appear on page 2 of this Assignment and any Supplemental Sheet(s).

The ASSIGNEE referred to in this agreement is:

(Name of Assignee) VANGUARD INTERNATIONAL SEMICONDUCTOR CORPORATION

(Address) 123, PARK AVE-3RD., SCIENCE-BASED INDUSTRIAL PARK, HSINCHU,  
TAIWAN, R.O.C.

The ASSIGNEE is:

(check one) ☐ An individual.

☐ A Partnership.

☒ A Corporation of TAIWAN, R.O.C. (specify state or country)

☐ (other) \_\_\_\_\_

The ASSIGNOR(S), in consideration of \$10.00 paid by the ASSIGNEE, and other good and  
valuable consideration, receipt of which is acknowledged, hereby assign(s) the following  
rights to the ASSIGNEE, its successors and assigns:

the full and exclusive right to the invention;

the entire right, title and interest in and to the PATENT RIGHTS.

the right to sue and recover for any past infringement; and

the right to claim priority under 35 USC 119, 35 USC 120, or any other applicable  
provisions, based on any earlier patent applications for this invention.

PATENT

REEL: 9315 FRAME: 0861

THIS IS PAGE 2 OF AN ASSIGNMENT FROM THE INVENTOR(S) TO

ASSIGNEE: VANGUARD INTERNATIONAL SEMICONDUCTOR CORPORATION

INVENTION TITLE: METHOD TO MANUFACTURE METAL GATE OF INTEGRATED  
CIRCUITS


As to all U.S. patent applications assigned under this agreement, the ASSIGNOR(S) hereby authorize(s) and request(s) the Commissioner of Patents and Trademark to issue all Letters Patent to the ASSIGNEE as the assignee of the entire right, title and interest, for the sole use and enjoyment of said ASSIGNEE, its successors and assigns.

Further, the ASSIGNOR(S) agree(s) to communicate to said ASSIGNEE, or its representatives, any fact known to the ASSIGNOR(S) with respect to said invention, and testify in any legal proceeding, sign all lawful papers, execute all divisional, continuation, substitute, renewal, reexamination and reissue applications, execute all necessary assignment papers to cause and all Letter Patent to be issued to said ASSIGNEE, make all rightful oaths and generally do everything necessary or desirable to aid said ASSIGNEE, its successors and assigns, to obtain and enforce proper protection for said invention.

The ASSIGNOR(S) authorize(s) the attorneys and agents who have the power of attorney in this application to check any appropriate boxes and to insert the Serial Number and filing date in this document after it has been executed.

Kwong-Jr TSAI

Name of sole or first inventor

Kwong-Jr Tsai 

Signature

6/18/98  
Date

\_\_\_\_\_  
Name of second inventor, if any

\_\_\_\_\_  
Signature

\_\_\_\_\_  
Date

\_\_\_\_\_  
Name of third inventor, if any

\_\_\_\_\_  
Signature

\_\_\_\_\_  
Date

\_\_\_\_\_  
Name of fourth inventor, if any

\_\_\_\_\_  
Signature

\_\_\_\_\_  
Date

\_\_\_\_\_  
Name of fifth inventor, if any

\_\_\_\_\_  
Signature

\_\_\_\_\_  
Date

\_\_\_\_\_  
Name of sixth inventor, if any

\_\_\_\_\_  
Signature

\_\_\_\_\_  
Date

\_\_\_\_\_  
Name of seventh inventor, if any

\_\_\_\_\_  
Signature

\_\_\_\_\_  
Date

\_\_\_\_\_  
Name of eighth inventor, if any

\_\_\_\_\_  
Signature

\_\_\_\_\_  
Date  
PATENT