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Execution Date: July 29, 1998		
1. Application number(s) or patent number(s):		
	ication, the execution date of the application is: 07/29/98	
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Assignment

Whereas, I/We,

Shuji TAKAHASHI Tokyo, Japan of

hereinafter called assignor(s), have invented certain improvements in

SEMICONDUCTOR INTEGRATED CIRCUIT DESIGNING SYSTEM TAKING INSERTION OF REPEATERS INTO CONSIDERATION AND DESIGNING METHOD THEREOF and executed an application for Letters Patent of the United States of America therefor on July 19 1998; and

Whereas,

NEC Corporation

7-1, Shiba 5-chone, Minato-ku, Tokyo, Japan

(assignee), desires to acquire the entire right, title, and interest in the application and invention, and to any United States patents to be obtained therefor;

Now therefore, for valuable consideration, receipt whereof is hereby acknowledged,

I/We, the above named assignor(s), hereby sell, assign and transfer to the above named assignee, its successors and assigns, the entire right, little and interest in the application and the invention disclosed therein for the United States of America, including the right to claim priority under 35 U.S.C. §119, and I/We request the Commissioner of Patents to issue any Letters Patent granted upon the invention set forth in the application to the assignee, its successors and assigns; and I/We will execute without further consideration all papers deemed necessary by the assignee in connection with the United States application when called upon to do so by the assignee.

ON, ZINN,	MACPEAK	& SEAS of
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of execution	under 35 U	J.S.C. §261)
	the filing	the filing date and the fi

PATENT

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