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DOCKET NO. P-3584

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Sheldon Aronowitz, Helmut Puchner, Ravindra A. Kapre, and James P. Kimball

Assignee: LSI Logic Corporation

Title: FORMATION OF INTEGRATED CIRCUIT STRUCTURE USING ONE OR MORE SILICON LAYERS FOR IMPLANTATION AND OUT-DIFFUSION IN FORMATION OF DEFECT-FREE SOURCE/DRAIN REGIONS AND ALSO FOR SUBSEQUENT FORMATION OF SILICON NITRIDE SPACERS

Serial No.: Unknown

MRD 5-11-98

Filed: Herewith

Group Art Unit: Unknown

Attorney Docket No. P-3584

Examiner: Unknown

09/076399

May 11, 1998

RECORDATION FORM COVER SHEET PATENTS ONLY

To the Honorable Commissioner  
of Patents and Trademarks  
Washington, DC 20231

Sir:

Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Sheldon Aronowitz  
Helmut Puchner  
Ravindra A. Kapre  
James P. Kimball

Date of Execution: May 4, 1998  
Date of Execution: May 5, 1998  
Date of Execution: May 5, 1998  
Date of Execution: May 6, 1998

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NO SPEC  
Fee

2. Name and address of receiving party(ies):

LSI Logic Corporation  
1551 McCarthy Boulevard  
Milpitas, California 95035  
U.S.A.

3. Nature of conveyance: Assignment

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: May 4, 1998, May 5, 1998, May 5, 1998, and May 6, 1998.

A. Patent Application Serial Number: \_\_\_\_\_

B. Patent Number: \_\_\_\_\_

5. Name and address of party to whom correspondence concerning document should be mailed:

John P. Taylor  
Post Office Box 1598  
Temecula, California 92593-1598  
U.S.A.

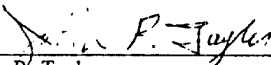
6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41): \$40.00

8. Attached is a check in the amount of \$40.00.

Charge Deposit Account No. \_\_\_\_\_ in the amount of \$ \_\_\_\_\_  
(A duplicate copy of this document is enclosed.)

9. Statement and Signature: To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

  
\_\_\_\_\_  
John P. Taylor Reg. No. 22,369

10. Total number of pages including cover sheet, attachments and document: 3

Docket No.: P-3584

Patent

ASSIGNMENT

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I/WE, the undersigned, Sheldon Aronowitz, Helmut Puchner, Ravindra A. Kapre, and James P. Kimball, do hereby sell, assign, and transfer to: LSI LOGIC CORPORATION, a corporation of Delaware, having a place of business at 1551 McCarthy Boulevard, Milpitas, California 95035, ("Assignee"), its successors, assigns, and legal representatives, the entire right, title and interest for the United States and all foreign countries, in and to any and all inventions and improvements which are disclosed in the application for United States Letters Patent,

( X ) which has been executed by the undersigned concurrently herewith,  
( ) which was filed on \_\_\_\_\_ 19\_\_\_\_ and assigned Serial No. \_\_\_\_\_.

and is entitled: "FORMATION OF INTEGRATED CIRCUIT STRUCTURE USING ONE OR MORE SILICON LAYERS FOR IMPLANTATION AND OUT-DIFFUSION IN FORMATION OF DEFECT-FREE SOURCE/DRAIN REGIONS AND ALSO FOR SUBSEQUENT FORMATION OF SILICON NITRIDE SPACERS" and in and to said application and all divisional, continuing, substitute, renewal, reissue, and all other applications for Letters Patent which have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original and reissued patents which have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application; and

Agree that said Assignee may apply for and receive Letters Patent for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney and other papers; communicate to said Assignee, its successors, assigns, and representatives, all facts known to the undersigned relating to said improvements and the history thereof; and generally do everything possible which said Assignee, its successors, assigns or representatives shall consider desirable for aiding in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements and all applications to patents and all patents on said improvements, in said Assignee, its successors, assigns and legal representatives; and

Covenant with said Assignee, its successors, assigns and legal representatives that no assignment, grant mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Name: <u>[Signature]</u> Sheldon Aronowitz	Date: <u>[Signature]</u> , 1998	Witness: <u>[Signature]</u> Witness: <u>Leticia Rosenthal</u> Printed
Name: <u>[Signature]</u> Helmut Puchner	Date: <u>May 5</u> , 1998	Witness: <u>[Signature]</u> Witness: <u>Leticia Rosenthal</u> Printed
Name: <u>[Signature]</u> Ravindra A. Kapre	Date: <u>May 5</u> , 1998	Witness: <u>[Signature]</u> Witness: <u>Leticia Rosenthal</u> Printed
Name: <u>[Signature]</u> James P. Kimball	Date: <u>May 6</u> , 1998	Witness: <u>[Signature]</u> Witness: <u>WALTER E. [Signature]</u> Printed

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REEL: 9390 FRAME: 0262

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