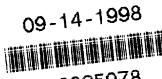
## IN THE UNI



EMARK OFFICE

Docket No.: VLSI-2775

SEP

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the U.S. Patent and Trademark Office, Assignment Branch, North Tower Building, Suite 10C35, Washington, D.C., 20231, on the below date of deposit.

Date of 08/28/98 Deposit:

Name of Person Making the Deposit:

ANDREA D. HUSSEY

Signature of the Person Making the Deposit:

The Assistant Commissioner for Patents Washington, D.C. 20231

Please record the attached original document(s) or copy(ies) thereof:

Transmittal of an Assignment

1. Name of conveying Patrick Feyfant, Laurent Winckel and Varenka Martin

party(ies):

Filed:

Serial No.:

09/058,611

04/10/98

Group Art Unit:

2744

Examiner:

2. Name and Address of VLSI Technology, Inc.

Receiving party(ies)

1109 McKay Drive, San Jose, California 95131

3. Nature of Conveyance: Assignment

Execution Date: 6/12/98, 8/5/98 and 8/5/98

4. New Patent Application entitled:

DIRECT DIGITAL DOWN CONVERSION OF A 10.8 MHz

INTERMEDIATE FREQUENCY SIGNAL IN THE PERSONAL

HANDY PHONE SYSTEM

5. Name and address of party to whom correspondence concerning document should be mailed:

Name:

Anthony C. Murabito

**Address** 

Wagner, Murabito & Hao

Two North Market Street, Third Floor, San Jose, CA 95113

Phone

(408) 938-9060

6. Total Number of applications and patents involved: ONE

7. Fee Calculation (for other than a small entity)

Assignment Recordation Fee, per property

Total Fees (37 CFR 3.41) The amount of \$ 40.00

is enclosed

1 **X** \$40.00

\$40.00

- 8. At any time during the pendency of this application, please charge any additional fees required or credit any overpayments to Deposit Account 23-0085. A duplicate copy of this transmittal is enclosed.
- 9. Statement and signature:

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Name Anthony C. Murabito

Reg. No. 35,295

Date:

August 28, 1998

09/08/1998 TTUN11

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Page 1 of 1

rev. 6/97 jpw

REEL: 9435 FRAME: 0352

Docket No.: VLSI-2	775
In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we  Patrick Feyfant, Laurent Winckel and Varenka Martin  do hereby sell, assign and transfer unto VLSI Technology, Inc. (hereinafter called VLSI), a California Corp having its principal place of business at 1109 McKay Drive, San Jose, California 95131, and its successor assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any improvements, including the right of priority in, to, and under, the application for the United States patent e	ors and all
DIRECT DIGITAL DOWN CONVERSION OF A 10.8 MHz INTERMEDIATE FREQUENCY SIGNAL IN	I THE
filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefore; or	
x Serial No.: 09/058/611 filed on 04/10/98 and described therein, and any and all Letters Patent of the United States and of countries foreign the which may be granted thereon or therefore;	forth nereto
Further, I/we have agreed to assign to VLSI all inventions (except as otherwise limited by law) which relate to business and which were first conceived or actually reduced to practice during my/our employment by VLSI	
And for the above consideration, I/we agree promptly upon request of VLSI, its successors or assigns, to each deliver without further compensation any power of attorney, assignment, application, whether continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to V successors and assigns, the inventions described in said application and all patent rights therein, in the States and in any country foreign thereto, and to cooperate and assist in the prosecution of interforceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provide expenses which may be incurred by me/us in lending such cooperation and assistance are paid by VLSI;	original, 'LSI, its United ference
I/we further covenant with VLSI, its successors, assigns, and legal representatives that no assignment mortgage, license, or other agreement affecting the rights and property herein conveyed has been nothers by the undersigned, and that full right to convey the same as herein expressed is possessed undersigned;	nade to
In witness whereof, I/we hereunto set my/our hand(s) and seal.	
Inventor's Signature: Date: 12 June 19	9 <i>8</i>

Inventor's Signature: Date: Inventor's Signature: Date:

Date:

\_\_\_\_\_ Date: \_\_\_\_

Date:

Date:

Date:

Date:

Page 1 of 1 rev: 11/97 epn

> **PATENT REEL: 9435 FRAME: 0353**

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Inventor's Signature:

Inventor's Signature:

Inventor's Signature:

Inventor's Signature:

Inventor's Signature:

Inventor's Signature:

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