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01-11-1999

PATENT AND TRADEMARK OFFICE

Attorney Docket No. 7268-000146

CO/

DOCUMENT (PATENT)

100874733

Hon. Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Sir.

Pursuant to 37 C.F.R. 3.31, enclosed herewith is a document for recordal in this case.  
The following information is provided:

(1) **Name Of Party Conveying The Interest:**

Shye-Lin Wu

(2) **Name And Address Of Party Receiving The Interest:**

Acer Semiconductor Manufacturing Inc.  
No. 6, Creation Rd. 2, Science-Based Industrial Park  
Hsinchu, TAIWAN, R.O.C.

(3) **Description Of The Transaction To Be Recorded:**

☒ Assignment      ☐ License  
☐ Change of Name   ☐ Other \_\_\_\_\_

(4) **Application(s) and/or Patent(s) Against Which Enclosure Is To Be Recorded:**

☐ Serial Number 09 109348, filed \_\_\_\_\_  
☒ Application being filed concurrently herewith.  
☐ Patent Number \_\_\_\_\_, issued \_\_\_\_\_

(5) **Name And Address Of The Party To Whom Correspondence Concerning The Request To Record Should Be Mailed:**

Thomas T. Moga  
HARNESS, DICKEY & PIERCE, P.L.C.  
P.O. Box 828  
Bloomfield Hills, MI 48303

*Free OK*

**(6) Number Of Applications and/or Patents Identified In The Cover Sheet And Total Recordal Fee:**

Number of Applications/Patents: . 1

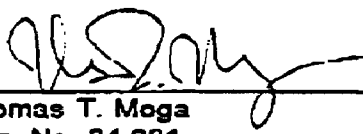
Total Recordal Fee Enclosed: . . . \$40.00

**(7) Date(s) The Document Was Executed: May 6, 1998**

To the best of my knowledge and belief, the foregoing information is true and correct, and if the attached is not an original document, the undersigned verifies that it is a true copy of the original.

If, for some reason, Applicant has not paid a sufficient fee, please charge our Deposit Account No. 08-0750 for any further fees which may be due. A duplicate copy of this document is enclosed.

Respectfully submitted,

By:   
Thomas T. Moga  
Reg. No. 34,881  
Attorney for Applicant

Date: June 30, 1998

# ASSIGNMENT

(A)

This assignment agreement is applicable to an invention entitled (Invention Title) HIGH DENSITY BURIED BIT LINE FLASH EEPROM MEMORY CELL WITH A SHALLOW TRENCH FLOATING GATE

The PATENT RIGHTS referred to in this agreement are:

(check one) ☒ a patent application for this invention, executed by the ASSIGNOR(S) concurrently with this assignment.

☐ U.S. patent application Serial No. \_\_\_\_\_, filed \_\_\_\_\_

☐ a U.S. patent application based on PCT International Application No. \_\_\_\_\_ filed on (date) \_\_\_\_\_ (U.S. patent application Serial No. \_\_\_\_\_, if known).

☐ U.S. patent No. \_\_\_\_\_, issued \_\_\_\_\_

The PATENT RIGHTS also include all divisions reissues, continuations and extensions of the patents and patent applications identified above.

The PATENT RIGHTS assigned under this agreement are:

(check one) ☒ U.S. patent rights only.

☐ Worldwide patent rights. In this case, the assignee shall have the right to claim the benefit of the filing date of any U.S. or foreign patent application for this invention.

The ASSIGNOR(S) referred to in this agreement is (or are) the inventor(s) whose signatures appear on page 2 of this Assignment and any Supplemental Sheet(s).

The ASSIGNEE referred to in this agreement is:

(Name of Assignee) Acer Semiconductor Manufacturing Inc.

(Address) NO.6, CREATION RD.2, SCIENCE-BASED INDUSTRIAL PARK, HSINCHU, TAIWAN, R.O.C.

The ASSIGNEE is:

(check one) ☐ An individual.

☐ A Partnership.

☒ A Corporation of TAIWAN, R.O.C. (specify state or country)

☐ (other) \_\_\_\_\_

The ASSIGNOR(S), in consideration of \$10.00 paid by the ASSIGNEE, and other good and valuable consideration, receipt of which is acknowledged, hereby assign(s) the following rights to the ASSIGNEE, its successors and assigns:

the full and exclusive right to the invention;

the entire right, title and interest in and to the PATENT RIGHTS;

the right to sue and recover for any past infringement; and

the right to claim priority under 35 USC 119, 35 USC 120, 35 USC 121, 35 USC 122, 35 USC 123, 35 USC 124, 35 USC 125, 35 USC 126, 35 USC 127, 35 USC 128, 35 USC 129, 35 USC 130, 35 USC 131, 35 USC 132, 35 USC 133, 35 USC 134, 35 USC 135, 35 USC 136, 35 USC 137, 35 USC 138, 35 USC 139, 35 USC 140, 35 USC 141, 35 USC 142, 35 USC 143, 35 USC 144, 35 USC 145, 35 USC 146, 35 USC 147, 35 USC 148, 35 USC 149, 35 USC 150, 35 USC 151, 35 USC 152, 35 USC 153, 35 USC 154, 35 USC 155, 35 USC 156, 35 USC 157, 35 USC 158, 35 USC 159, 35 USC 160, 35 USC 161, 35 USC 162, 35 USC 163, 35 USC 164, 35 USC 165, 35 USC 166, 35 USC 167, 35 USC 168, 35 USC 169, 35 USC 170, 35 USC 171, 35 USC 172, 35 USC 173, 35 USC 174, 35 USC 175, 35 USC 176, 35 USC 177, 35 USC 178, 35 USC 179, 35 USC 180, 35 USC 181, 35 USC 182, 35 USC 183, 35 USC 184, 35 USC 185, 35 USC 186, 35 USC 187, 35 USC 188, 35 USC 189, 35 USC 190, 35 USC 191, 35 USC 192, 35 USC 193, 35 USC 194, 35 USC 195, 35 USC 196, 35 USC 197, 35 USC 198, 35 USC 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PATENT

REEL: 9448 FRAME: 0864

THIS IS PAGE 2 OF AN ASSIGNMENT FROM THE INVENTOR(S) TO

ASSIGNEE: Acer Semiconductor Manufacturing Inc.

INVENTION TITLE: HIGH DENSITY BURIED BIT LINE FLASH EEPROM MEMORY  
CELL WITH A SHALLOW TRENCH FLOATING GATE

As to all U.S. patent applications assigned under this agreement, the ASSIGNOR(S) hereby authorize(s) and request(s) the Commissioner of Patents and Trademark to issue all Letters Patent to the ASSIGNEE as the assignee of the entire right, title and interest, for the sole use and enjoyment of said ASSIGNEE, its successors and assigns.

Further, the ASSIGNOR(S) agree(s) to communicate to said ASSIGNEE, or its representatives, any fact known to the ASSIGNOR(S) with respect to said invention, and testify in any legal proceeding, sign all lawful papers, execute all divisional, continuation, substitute, renewal, reexamination and reissue applications, execute all necessary assignment papers to cause and all Letter Patent to be issued to said ASSIGNEE, make all rightful oaths and generally do everything necessary or desirable to aid said ASSIGNEE, its successors and assigns, to obtain and enforce proper protection for said invention.

The ASSIGNOR(S) authorize(s) the attorneys and agents who have the power of attorney in this application to check any appropriate boxes and to insert the Serial Number and filing date in this document after it has been executed.

Shey-Lin WU

Name of sole or first inventor

Shey-Lin Wu  
Signature

5/6/78  
Date

Name of second inventor, if any

Signature

Date

Name of third inventor, if any

Signature

Date

Name of fourth inventor, if any

Signature

Date

Name of fifth inventor, if any

Signature

Date

Name of sixth inventor, if any

Signature

Date

Name of seventh inventor, if any

Signature

Date

Name of eighth inventor, if any

Signature

Date

P.4/10

ALP HARNES, DICKY & PIERCE

PATENT

RECORDED: 06/30/1998

REEL: 9448 FRAME: 0865