

10-16-1998

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To the Honorable Commission:

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ached original documents or copy thereof.

1. Name of conveying party(ies):

HUN-JAN TAO AND YUAN-CHANG HUANG

2. Name and address of receiving party(ies):

Name: Taiwan Semiconductor Manufacturing Company

Internal Address:

Street Address:

121 Park Ave. 3  
Science-Based Industrial Park  
City: Hsin-Chu Zip:  
Country: Taiwan, R.O.C.Additional name(s) or conveying party(ies) attached? ☐ Yes ☒ No

3. Nature of conveyance

☒ Assignment ☐ Merger  
☐ Security Agreement ☐ Change of Name  
☐ other \_\_\_\_\_Execution Date: 8/18/98Additional name(s) & address(es) attached? Yes ☐ No ☒

4. Application number(s) or patent number(s):

MAD 10-01-98

If this document is being filed together with a new application, the execution date of the application is: 8/18/98A. Patent Application No(s)B. Patent No(s)Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: GEORGE O. SAILE

Internal Address: \_\_\_\_\_

Street Address: 20 MCINTOSH DRIVECity: POUGHKEEPSIE State: NY ZIP: 126036. Total no. of applications and patents involved: 17. Total fee (37 CFR 3.41): \$ 40.00☐ Enclosed☒ Authorized to be charged to deposit acct.8. Deposit account number: 19-0033

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### ASSIGNMENT OF INVENTION

In consideration of the payment by ASSIGNEE to ASSIGNOR of the sum of One Dollar (\$1.00), the receipt of which is hereby acknowledged, and for other good and valuable consideration,

#### ASSIGNOR:

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#### ASSIGNEE:

**Taiwan Semiconductor Manufacturing Co. Ltd.**

**121 Park Ave. 3**

**Science-Based Industrial Park**

**Hsin-Chu, Taiwan, R.O.C.**

**Taiwan, R.O.C.**

Nationality

and the successors, assigns and legal representatives of the ASSIGNEE

(complete one of the following)

☒ the entire right, title and interest

☐ an undivided \_\_\_\_\_ percent (\_\_\_%) interest for the United States and its territorial possessions  
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in and to any and all improvements which are disclosed in the invention entitled:

**Method For Patterning A Polysilicon Gate In Deep Submicron Technology**

and which is found in

- (a) ☒ U.S. patent application executed on even date herewith
- (b) ☐ U.S. patent application executed on \_\_\_\_\_
- (c) ☐ U.S. application serial no. \_\_\_\_\_ filed on \_\_\_\_\_
- (d) ☐ U.S. patent no. \_\_\_\_\_ issued \_\_\_\_\_  
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and, in and to, all Letters Patent to be obtained for said invention by the above application or any continuation, division, renewal, or substitute, thereof, and as to letters patent any re-issue or re-examination thereof.


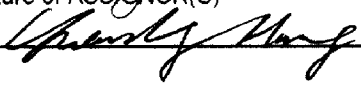
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IN WITNESS WHEREOF, I / We have signed this

8/18/1998 (Date of signing)

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Signature of ASSIGNOR(S)  
  
\_\_\_\_\_  
\_\_\_\_\_

If ASSIGNOR is a legal entity complete the following information

\_\_\_\_\_  
Type or print the name of the above person authorized to sign on  
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\_\_\_\_\_  
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