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COVER SHEET  
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1. Name of Conveying Part(ies):

Patrick Arnould  
Frederic Hayem

10.14.98

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

2. Name and address of receiving party(ies):

Name: VLSI Technology, Inc.

Internal Address:

Street Address: 1109 McKay Drive

City: San Jose State: CA Zip: 95131

Additional names(s) & address(es) attached: ☐ Yes ☒ No

U.S. PTO  
09/17/98  
10/14/98

3. Nature of conveyance:

- ☒ Assignment ☐ Security Agreement  
☐ Merger ☐ Change of Name  
☐ Other

Execution Date: October 12, 1998

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: October 12, 1998

A. Patent Application No.(s):

89/173441

Additional numbers attached: ☐ Yes ☒ No

B. Patent No.(s)

Additional numbers attached: ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: James D. Shaurette

Internal Address: Wells, St. John, Roberts,

Gregory & Matkin P.S.

Street Address: 601 W. First Avenue, Ste. 1300

City: Spokane State: WA Zip: 99201-3817

6. Total number of applications and patents involved. 1

7. Total fee (37 CFR 3.41). \$ 40  
☒ Enclosed  
☐ Authorized to be charged to deposit account

8. Deposit account number

23-0925

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Statement and Signature.  
To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

James D. Shaurette

Name of Person Signing

[Signature]

Signature

October 14, 1998

Date

TOTAL NUMBER OF PAGES INCLUDING COVER SHEET, ATTACHMENTS AND DOCUMENT: 4

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**ASSIGNMENT****PARTIES TO THE ASSIGNMENT:****INVENTORS:**

Patrick Arnould

Frederic Hayem

**ASSIGNEE:**

VLSI Technology, Inc.  
Corporation of the State of Delaware  
1109 McKay Drive  
San Jose, CA 95131

**BACKGROUND OF THIS ASSIGNMENT:**

Inventors have conceived certain new and useful inventions disclosed in a United States patent application titled Circuit Assembly and Method of Synchronizing Plural Circuits.

VLSI TECHNOLOGY, INC. desires to acquire the entire right, title and interest in said inventions and with respect to any Letters Patent that may be granted with respect to the inventions in both the United States and in all foreign countries.

**THE PARTIES AGREE AS FOLLOWS:**

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, INVENTORS have sold, assigned and transferred to VLSI TECHNOLOGY, INC. the full and exclusive right to said inventions in the United States and in all foreign countries.

INVENTORS do hereby sell, assign and transfer to VLSI TECHNOLOGY, INC. the entire right, title and interest in the

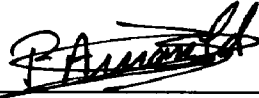
1 above-identified application executed concurrently with this assignment  
2 and to any reissues, divisions or continuations thereof, and hereby  
3 authorizes the Commissioner of Patents and Trademarks to issue such  
4 Letters Patent to VLSI TECHNOLOGY, INC. for the sole use of VLSI  
5 TECHNOLOGY, INC., its successors or assigns.

6 INVENTORS further agree to execute, at the request and expense  
7 of VLSI TECHNOLOGY, INC. such other formal documents as may be  
8 required to fully convey the interest transferred herein and will similarly  
9 execute any applications papers required for the filing of any division,  
10 continuation or reissue of the patent application or resulting Letters  
11 Patent; and will generally do everything necessary or desirable to obtain  
12 and enforce proper protection for the inventions assigned hereby.

13 INVENTORS further assign to VLSI TECHNOLOGY, INC. the  
14 whole right, title and interest in the inventions disclosed in the  
15 application throughout all countries foreign to the United States. VLSI  
16 TECHNOLOGY, INC. is hereby authorized to apply for patents relating  
17 to the inventions in its own name in countries where such procedure is  
18 proper; to claim the benefit of the International Convention; to file and  
19 prosecute International Applications relating to the inventions under the  
20 Patent Cooperation Treaty; and to file and prosecute applications  
21 relating to the inventions under the European Patent Convention.  
22 INVENTORS agree to execute applications relating to the inventions in  
23 those countries and under those conventions where it is necessary that  
24 the same be executed by the inventor, and to execute assignments of

1 such applications and the resulting Letters Patent to VLSI  
2 TECHNOLOGY, INC. as well as all other necessary papers in relation  
3 to such applications and Letters Patent.  
4

5 Signed at Antibes on October, 6, 1998.

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7 PATRICK ARNOULD

8 Signed at San Diego on October 12, 1998.

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10 FREDERIC HAYEM  
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