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Execution Date: October 1, 1998	
4. Application number(s) or patent number(s):	
If this document is being filed together with a new application, the execution date of the application is: October 1, 1998	
A. Patent Application No.(s) 09/173/63	B. Patent No.(s)
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**PATENT REEL: 9530 FRAME: 0649** 

## Assignment

Whereas, I/We,

TADASHI IWASAKI Tokyo, Janen

hereinafter called assignor(s), have invented certain improvements in

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING PROTECTIVE TRANSISTORS WITH P-N JUNCTION BROKEN DOWN EARLIER THAN BREAKDOWN OF GATE INSULATOR OF COMPONENT TRANSISTORS

executed an application for Letters Patent of the United States of America therefor on 19 98 ; and October 1,

Whereas,

NEC Corporation 7-1, Shiba 5-chome, Minato-ku, Tokyo, Japan

(assignee), desires to acquire the entire right, title, and interest in the application and invention, and to any United States patents to be obtained therefor;

Now therefore, for valuable consideration, receipt where of is hereby acknowledged,

I/We, the above named assignor(s), hereby sell, assign and transfer to the above named assignee, its successors and assigns, the entire right, title and interest in the application and the invention disclosed therein for the United States of America, including the right to claim priority under 35 U.S. Code 119, and I/weggequest the Commissioner of Patents to Issue any Letters Patent granted upon the Invention set forth in the application to the assignee, its successors and assigns; and i/we will execute without further consideration all papers deemed necessary by the assignee in connection with the United States application when called upon to do so by the assignee.

Signed at

Tokyo, Japan

October 1,

Tadashi dwasa

Witness

Date

AKIO MURANO

AKIRA MUTO

(Legalization not required for recording but is prima facle evidence of execution under公市中心下C. 261)

RECORDED: 10/15/1998 applicants from Convention Countries