FORM FIO-1393		U.S. Department of Commerce Patent and Trademark Office	
To the Honorable Commissioner of Patents and Trademark	ks. Please record the attached original documents or copy thereof.	TO	
1. Name of conveying party(ies):	2. Name and Address of receiving party(ies):		
1) Shunichi Sukegawa	Name: TEXAS INSTRUMENTS INCORPORATED	2.0	
 Shinji Bessho tadashi Tachibana 	Address: P.O. Box 655474, MS 3999	540	
4) Hiroyuki Yoshida	City: Dallas	Ŭ N	
	State: TX Zip: 75265		
	Additional name(s) & address(es) attached? Yes _X_ N	o	
3. Nature of Conveyance:			
X Assignment Merger			
Security Agreement Change of Name			
Other			
Execution Date: Oct. 30, 1997, Oct. 31, 1997, Nov. 13, 19	97, Oct. 31, 1997		
4. Application number(s) or patent number(s).			
CIRCUIT	MAVING A BACK GATE VOLTAGE CONTROLLED D	ELA	
Execution date of the application: November 24, 1998 Title: SEMICONDUCTOR MEMORY DEVICE I CIRCUIT Docket No: TI-20601 A. Patent Application No.(s)		ELA	
Execution date of the application: November 24, 1998 Title: SEMICONDUCTOR MEMORY DEVICE I CIRCUIT Docket No: TI-20601	HAVING A BACK GATE VOLTAGE CONTROLLED D		
Execution date of the application: November 24, 1998 Title: SEMICONDUCTOR MEMORY DEVICE H CIRCUIT Docket No: TI-20601 A. Patent Application No.(s) S.N.	HAVING A BACK GATE VOLTAGE CONTROLLED D B. Patent No.(s) Additional numbers attached? YesX No		
Execution date of the application: November 24, 1998 Title: SEMICONDUCTOR MEMORY DEVICE H CIRCUIT Docket No: TI-20601 A. Patent Application No.(s) S.N. Additional numbers attached?YesXNo 5. Name and address of party to whom correspondence conce	HAVING A BACK GATE VOLTAGE CONTROLLED D B. Patent No.(s) Additional numbers attached? YesX No		
Execution date of the application: November 24, 1998 Title: SEMICONDUCTOR MEMORY DEVICE H CIRCUIT Docket No: TI-20601 A. Patent Application No.(s) S.N. Additional numbers attached?YesXNo 5. Name and address of party to whom correspondence concer document should be mailed: Name: Robby T. Holland Texas Instruments Incorporated Address: P.O. Box 655474, MS 3999 City: Dallas State: TX Zip: 75265	HAVING A BACK GATE VOLTAGE CONTROLLED D B. Patent No.(s) Additional numbers attached? YesX No		
Execution date of the application: November 24, 1998 Title: SEMICONDUCTOR MEMORY DEVICE H CIRCUIT Docket No: TI-20601 A. Patent Application No.(s) S.N. Additional numbers attached?YesXNo 5. Name and address of party to whom correspondence conce document should be mailed: Name: Robby T. Holland Texas Instruments Incorporated Address: P.O. Box 655474, MS 3999 City: Dallas State: TX Zip: 75265	HAVING A BACK GATE VOLTAGE CONTROLLED D B. Patent No.(s) Additional numbers attached? Yes Additional numbers attached? Yes Yes (1)		
Execution date of the application: November 24, 1998 Title: SEMICONDUCTOR MEMORY DEVICE H CIRCUIT Docket No: TI-20601 A. Patent Application No.(s) S.N. Additional numbers attached? Yes X No 5. Name and address of party to whom correspondence concer document should be mailed: Name: Robby T. Holland Texas Instruments Incorporated Address: P.O. Box 655474, MS 3999 City: Dallas State: TX Zip: 75265 SBURNS 0000036 200668 09198816 40.00 CH	HAVING A BACK GATE VOLTAGE CONTROLLED D B. Patent No.(s) Additional numbers attached? YesX No erning 6. Number of applications and patents involved:(1) 7. Amount of fee enclosed or authorized to be charged: \$40		

PATENT REEL: 9609 FRAME: 0765

Attorney Docket No. TI-20621 Page 1 of 4

ASSIGNMENT

WHEREAS, TEXAS INSTRUMENTS JAPAN, LTD. (hereinafter TIJ), a Japanese company having its headquarters at Aoyama Fuji Bldg., 6-12 Kiota-Aoyama 3-Chome, Minato-ku, Tokyo, Japan, under the laws of Japan made application, in its own name, for Letters Patent in Japan with respect of an invention as set forth below;

WHEREAS, the or each below named inventor (hereinafter INVENTOR) is the or an INVENTOR of the said invention;

WHEREAS, TEXAS INSTRUMENTS INCORPORATED, a corporation organized and existing under the laws of the State of Delaware, with its principal office at 13510 North Central Expressway, Dallas, Texas 75243, is desirous of acquiring our entire right, title and interest in and to the said invention outside of Japan, and in and to the U.S. patent application as identified hereinafter and any Letters Patent that may issue thereon;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, by this Assignment,

TIJ and INVENTOR sell and assign to the said TEXAS INSTRUMENTS INCORPORATED, its successors and assigns, the entire right, title and interest in and to the said invention in the U.S.A. and all other countries outside Japan; in and to the U.S. patent application executed as identified below and all patents which may be granted therefor, and all divisions, reissues, substitutions, continuations, and extensions thereof; and the right to file applications and obtain patents in its own name in any said other country or group of countries, and to claim priority under the terms of the International Convention for the Protection of Industrial Property or any other relevant convention;

TIJ and INVENTOR hereby authorize and request the Commissioner of Patents to issue U.S. patents for said invention, or U.S. patents resulting therefrom, insofar as the interest of TIJ and INVENTOR is concerned to the said TEXAS **INSTRUMENTS INCORPORATED**, as assignee of the entire right, title and interest.

TIJ and INVENTOR further agree to communicate to TEXAS INSTRUMENTS INCORPORATED or to its successors, assigns and legal representatives, any facts know to TIJ and INVENTOR respecting the said invention, and at the expense of TEXAS INSTRUMENTS INCORPORATED, sign any and all patent applications, assignments, affidavits and other lawful papers, execute all divisional, continuation, reissue and substitute applications, make all lawful oaths, testify in any legal or quasi-legal proceedings and generally to do everything possible to aid said TEXAS INSTRUMENTS INCORPORATED, its successors, assigns and nominees to obtain and enforce proper patent protection for said invention in all countries outside of Japan.

Title of Invention:	SEMICONDUCTOR MEMORY DEVICE HAVING A BACK GATE VOLTAGE CONTROLLED DELAY CIRCUIT
Inventors:	Shunichi Sukegawa Shinji Bessho Tadashi Tachibana Hiroyuki Yoshida

U.S. Application executed on:

Attorney Docket No. TI-20621 Page 3 of 4

IN WITNESS WHEN set forth below;	REOF , this Agreement has be	een executed as
Signed at by TEXAS INSTRUMENTS	On 5 JAPAN, LTD.	
Name:		
Title:		
Date:		
and by the INVENTOR	(S):	
Name of Inventor:	Stumicli Supequer Shunichi Sukegava	
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Name of Inventor:	Shinji Bessho	
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Attorney Docket No. TI-20621 Page 4 of 4

Name of Inventor:

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AUS01:120040.1

PATENT REEL: 9609 FRAME: 0769

RECORDED: 11/24/1998