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Case Docket No. 981217

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To the Assistant Commissioner for Patents
Please record the attached original documents or copy thereof.

1. Name of conveying party:

Naoki YAMADA

Additional name(s) of conveying party attached? Yes ___ No XX

2. Name and address of receiving party:

Name: FUJITSU LIMITED

Street Address: 1-1 Kamikodanaka 4-chome, Nakahara-ku,
Kawasaki-shi, Kanagawa 211 Japan

Additional name(s) & address(es) attached? Yes ___ No XX

3. Nature of conveyance: Assignment

Execution Date: November 16, 1998

4. Title: SEMICONDUCTOR SUBSTRATE PROCESSING METHOD

5. Application number(s) or patent number(s):

A. Patent Application No.(s)
09/163,011

B. Patent No.(s)

If this document is being filed together with a new application, the execution date of the application is: _____

Additional numbers attached? Yes ___ No XX

40.00 CP

12/04/1998 INVENTEN 00000146 09163011

01 FC:501

PATENT

REEL: 9615 FRAME: 0784

6. Name and address of party to whom correspondence concerning document should be mailed:

Name: ARMSTRONG, WESTERMAN, HATTORI,
MCLELAND & NAUGHTON
1725 K Street, N.W., Suite 1000
Washington, D.C. 20006

7. Total number of applications and patents involved: 1

8. Total fee (37 CFR 3.41). \$40.00

XX Enclosed

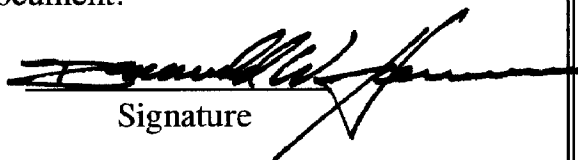
 Authorized to be charged to Deposit Account

9. Deposit Account No.: 01-2340

10. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Donald W. Hanson
Name of Person Signing


Signature

December 1, 1998
Date

27,133
Registration No.

Total number of pages including cover sheet, attachments and document: 3

Rev. 2/97

DWH/llf

U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter ASSIGNOR) by

(Insert ASSIGNEE's
Name(s) Address(es))

FUJITSU LIMITED1-1 Kamikodanaka 4-chome, Nakahara-ku, Kawasaki-shi, Kanagawa 211 Japan

(hereinafter ASSIGNEE), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled

(Title of Invention)

SEMICONDUCTOR SUBSTRATE PROCESSING METHOD

(*If the assignment is
being filed after the filing
of the application, this
section must be
completed)

for which application for Letters Patent of the United States was executed on even date herewith unless otherwise indicated below:

*filed on September 30, 1998, Serial No. 09/163,011.
(Armstrong, Westerman, Hattori, McLeland & Naughton is hereby authorized to insert the series code, serial number and/or filing date hereon, when known)

and all Letters Patent of the United States to be obtained therefor on said application or any continuation, divisional, substitute, reissue or reexamination thereof for the full term or terms for which the same may be granted.

The ASSIGNOR agrees to execute all papers necessary in connection with the application and any continuation, divisional, reissue or reexamination applications thereof and also to execute separate assignments in connection with such applications as the ASSIGNEE may deem necessary or expedient.

The ASSIGNOR agrees to execute all papers necessary in connection with any interference, litigation, or other legal proceeding which may be declared concerning this application or any continuation, divisional, reissue or reexamination thereof or Letters Patent or reissue patent issued thereon and to cooperate with the ASSIGNEE in every way possible in obtaining and producing evidence and proceeding with such interference, litigation, or other legal proceeding.

IN WITNESS WHEREOF, the undersigned inventor(s) has (have) affixed his/her/their signature(s).

(Signatures)

Naoki Yamada
(SIGNATURE)

Naoki YAMADA
(TYPE NAME)

November 16, 1998
(DATE)

(SIGNATURE)

(TYPE NAME)

(DATE)

(SIGNATURE)

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NO LEGALIZATION REQUIRED