

REC

01-19-1999



HEET

U.S. DEPARTMENT OF COMMERCE
Patent and Trademark Office

Tab sellings

100945196

To the Honorable Commissioner of Patents and Trademarks: Please return attached original documents or copy thereof.

1. Name of conveying party(ies):

Sigma Designs, Inc.

Additional name(s) of conveying party(ies) attached? Yes No

2. Name and address of receiving party(ies)

Name: SILICON VALLEY BANK

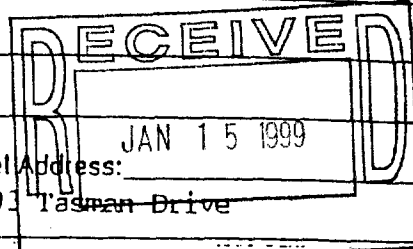
Internal Address: Attn: Loan Documentation Group

Street Address:

3003 Tasman Drive

City: Santa Clara State: CA ZIP: 95054

Additional name(s) & address(es) attached? Yes No



3. Nature of conveyance: MPD 1-15-99

Assignment

Merger

Security Agreement

Change of Name

Other

Execution Date: 10-26-98

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: _____

A. Patent Application No.(s)

See attached.

B. Patent No.(s)

See Attached

Additional numbers attached? Yes No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: SILICON VALLEY BANK

Internal Address:

Attn: Loan Documentation Group

Street Address:

3003 Tasman Drive

City: Santa Clara State: CA ZIP: 95054

6. Total number of applications and patents involved: 13

7. Total fee (37 CFR 3.41).....\$ 520 -

Enclosed CHECK NO. _____

Authorized to be charged to deposit account

8. Deposit account number: _____

(Attach duplicate copy of this page if paying by deposit account)

01/19/1999 DNGUYEN 00000009 5615401

FC:581

520.00 DP

DO NOT USE THIS SPACE

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Maggie Garcia

Name of Person Signing

Maggie Garcia

Signature

1-5-99

Date

ECC/114

Total number of pages including cover sheet, attachments, and documents: _____



REEL: 9693 FRAME: 0387

Sigma Designs, Inc.
Patents

Patent Application No. (s):

08/765,464
08/913,569
08/731,614
08/782,977
09/135,151
08/943,772
09/024,414

Patent No. (s):

5,615,401
5,790,881
5,719,511
5,821,947
5,818,468
5,797,029

ADDENDUM TO INTELLECTUAL PROPERTY SECURITY AGREEMENT

This Addendum to Intellectual Property Security Agreement is executed pursuant to, and is an addendum to, a Intellectual Property Security Agreement, dated October 1, 1996, filed with the United States Patent and Trademark office on October 28, 1996, as file number 100297192A for Patents, and file number 100297191A for Trademarks. This Addendum to Intellectual Property Security Agreement is presented for recordation as constructive notice that Sigma Designs, Inc. ("Assignor"), with its principal office at 46501 Landing Parkway, Fremont, CA 94538, the owner of the intellectual property identified in the exhibit attached hereto, has granted to SILICON VALLEY BANK ("Assignee"), with its principal office at 3003 Tasman Drive, Santa Clara, CA 95054, a security interest in the intellectual property described on **Exhibit A** hereto, and the exclusive rights comprised in the intellectual property, to secure payment of a debt.

IN WITNESS WHEREOF, Assignor has executed this Addendum to Intellectual Property Security Agreement as of October 26, 1998.

Borrower

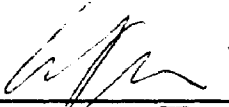
By: 
Name: KT Kim
Title: CFO

Exhibit "A" attached to that certain Addendum to Collateral Assignment, Patent Mortgage and Security Agreement dated October 26, 1998.

EXHIBIT "A"
COPYRIGHTS

SCHEDULE A - ISSUED COPYRIGHTS

<u>COPYRIGHT DESCRIPTION</u>	<u>REGISTRATION NUMBER</u>	<u>DATE OF ISSUANCE</u>
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SCHEDULE B - PENDING COPYRIGHT APPLICATIONS

<u>COPYRIGHT DESCRIPTION</u>	<u>APPLICATION NUMBER</u>	<u>DATE OF FILING</u>	<u>DATE OF CREATION</u>	<u>FIRST DATE OF PUBLIC DISTRIBUTION</u>
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SCHEDULE C - UNREGISTERED COPYRIGHTS (Where No Copyright Application is Pending)

<u>DESCRIPTION</u>	<u>CREATION</u>	<u>DISTRIBUTION FROM ASSIGNOR</u>	<u>ASSIGNOR</u>
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Exhibit "B" attached to that certain Addendum to Collateral Assignment, Patent Mortgage and Security Agreement dated October 26, 1998.

EXHIBIT "B"
PATENTS

Exhibit "C" attached to that certain Addendum to Collateral Assignment, Patent Mortgage and Security Agreement dated October 26, 1998.

EXHIBIT "C"
TRADEMARKS

<u>TRADEMARK</u> <u>DESCRIPTION</u>	<u>COUNTRY</u>	<u>SERIAL NO.</u>	<u>REG. NO</u>	<u>STATUS</u>
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Sigma Designs, Inc.

Patent/Application Product

Docket No.	Ser/Pat No.	File/Issue Date	Title
<u>SIGMA NGU-001</u>	5,404,437	4/4/95	Mixing of Computer Graphics and Animation Sequences
<u>SIGMA-002</u>	5,515,107	5/7/96	Method of Encoding A Stream of Motion Picture Data
<u>SIGMA-003</u>	5,598,576	1/28/97	Audio Output Device Having Digital Signal Processor for Responding to Commands Issued by Processor by Emulating Designated Functions According to Common Command Interface
<u>SIGMA-004</u>	5,615,401	3/25/97	Video and Audio Data Presentation Interface
<u>SIGMA-005</u>	5,528,309	8/18/96	Analog Video Chromakey Mixer
<u>SIGMA-005PCT EP</u>		3/11/96	Analog Video Chromakey Mixer
<u>SIGMA-005PCT US</u>	08/765,464	12/20/96	Analog Video Chromakey Mixer
<u>SIGMA-006</u>	5,790,881	8/4/98	Computer System Including Coprocessor Devices Simulating Memory Interfaces
<u>SIGMA-007</u>	08/408,588	3/22/95	Mixing of Computer Graphics and Animation Sequences
<u>SIGMA-009</u>	08/538,699	10/3/95	Methods of Encoding a Stream of Motion Picture Video Data
<u>SIGMA-010</u>	5,719,511	2/17/96	Circuit for Generating an Output Signal Synchronized to an Input Signal
<u>SIGMA-011</u>	5,818,468	10/6/98	Decoding Video Signals at High Speed Using a Memory Buffer
<u>SIGMA-013</u>	08/657,555	6/4/96	Improved Digital Signal Processor

Docket No.	Ser/Pat No.	File/Issue Date	Title
SIGMA-014 (O)			Brook Tree - Validity Search
SIGMA-018 (O)			Auravision Corporation
SIGMA-020			Pixel Data Compression
SIGMA-021	08/913,569	9/30/96	Method and Apparatus for Automatic Calibration of Analog Video Chromakey Mixer
SIGMA-023	08/731,614	10/15/96	Method and Apparatus for Automatic Calibration of Analog Video Chromakey Mixer
SIGMA-024	5,821,947	10/13/98	Mixing of Computer Graphics and Animation Sequences
SIGMA-025	08/782,977	1/14/97	Method of Encoding a Stream of Motion Picture Data
SIGMA-026	5,797,029	8/18/98	Sound Board Emulation Using Digital Signal Processor Using Data Word to Determine Which Operation to Perform and Writing the Result into Read Communication Area
SIGMA-026b	09/135,151	8/17/98	Sound Board Emulation Using Digital Signal Processor Using Data Word to Determine Which Operation to Perform and Writing the Result into Read Communication Area
SIGMA-029	08/943,772	10/6/97	Multifunction USB Video Capture Chip Using Bufferless Data Compression
SIGMA-031	09/024,414	2/17/98	Synchronizing and Clock Generating Circuits
SIGMA-032 (O)			Nissim Corporation
SIGMA-033 (O)			Columbia Innovation Enterprises/Columbia University
SIGMA-034 (O)			Winbond