

02-02-1999



To the Honorable Commissioner of Patents and Trade.

100955945

or copy thereof.

## 1. Name of conveying party(ies):

David B. Witt

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

## 2. Name and address of receiving party(ies):

Name: Advanced Micro Devices, Inc.

Internal Address: \_\_\_\_\_

Street Address: One AMD PlaceCity Sunnyvale State CA ZIP 94088Additional name(s) & address(es) attached? ☐ Yes ☒ NoJc503 U.S. PTO  
09/233259  
01/19/99

## 3. Nature of Conveyance:

☒ Assignment ☐ Merger☐ Security Agreement ☐ Change of Name☐ Other \_\_\_\_\_Execution Date: January 18, 1999

## 4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of this application is: January 19, 1999

A. Patent Application No.(s)

B. Patent No.(s)

Additional numbers attached? ☐ Yes ☒ No

## 5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Lawrence J. MerkelInternal Address: Conley, Rose & Tayon, P.C.Street Address: P.O. Box 398City Austin State TX ZIP 78767-0398

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6. Total number of applications and patents involved: 17. Total fee (37 CFR 3.41): .....\$ 40.00☒ Enclosed☐ Authorized to be charged to deposit account

## 8. Deposit account number:

(Attach a duplicate copy of this page if paying by deposit account)

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## 9. Statement and Signature.

*To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.*Lawrence J. MerkelName of Person Signing  
Reg. No. 41,191Lawrence J. Merkel  
SignatureJanuary 19, 1999  
DateTotal number of pages: 3

## ASSIGNMENT

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency and adequacy of which are hereby acknowledged, the undersigned does hereby:

SELL, ASSIGN AND TRANSFER to ADVANCED MICRO DEVICES, INC. (the "Assignee"), a corporation organized and existing under and by virtue of the laws of the State of Delaware, and having its principal place of business at One AMD Place, P.O. Box 3453, Sunnyvale, CA 94088, the entire right, title and interest for the United States and all foreign countries, in and to any and all improvements which are disclosed in the application for United States Letters Patent, which has been executed by the undersigned concurrently herewith, and is entitled "APPARATUS AND METHOD SPECULATIVELY FETCHING INSTRUCTIONS FOLLOWING A BRANCH INSTRUCTION IN A MICROPROCESSOR", such application and all divisional, continuing, substitute, renewal, reissue and all other applications for patent which have been or shall be filed in the United States and all foreign countries on any of such improvements; all original and reissued patents which have been or shall be issued in the United States and all foreign countries on such improvements; and specifically including the right to file foreign applications under the provisions of any convention or treaty and claim priority based on such application in the United States;

AUTHORIZE AND REQUEST the issuing authority to issue any and all United States and foreign patents granted on such improvements to the Assignee;

WARRANT AND COVENANT that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been or will be made to others by the undersigned, and that the full right to convey the same as herein expressed is possessed by the undersigned;

COVENANT that, when requested and at the expense of the Assignee, to carry out in good faith the intent and purpose of this assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all such improvements; execute all rightful oaths, declarations, assignments, powers of attorney and other papers; communicate to the Assignee all facts known to the undersigned relating to such improvements and the history thereof; and generally do everything possible which the Assignee shall consider desirable for securing, maintaining and enforcing proper patent protection for such improvements and for vesting title to such improvements in the Assignee;

