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To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy the \_\_\_\_\_ of \_\_\_\_\_

MCD 3-19-99

JCS14 U.S. 09/27/07 03/19/99

1. Name of Conveying Part(ies):  
David Ziger  
Additional name(s) of conveying party(ies) attached?  Yes  No

2. Name and address of receiving party(ies):  
Name: VLSI Technology, Inc.  
Internal Address: \_\_\_\_\_  
Street Address: 1109 McKay Drive  
City: San Jose State: CA Zip: 95131  
Additional names(s) & address(es) attached:  Yes  No

3. Nature of conveyance:  
 Assignment  Security Agreement  
 Merger  Change of Name  
 Other \_\_\_\_\_  
Execution Date: 03/05/99

4. Application number(s) or patent number(s):  
If this document is being filed together with a new application, the execution date of the application is: 03/05/99  
A. Patent Application No.(s):  
03/05/1999 PATTEN 00000002 09273077  
03 PDI001  
Additional numbers attached:  Yes  No (40.00 DP)

B. Patent No.(s)  
09273077  
Additional numbers attached:  Yes  No

5. Name and address of party to whom correspondence concerning document should be mailed:  
Name: Lance R. Sadler  
Internal Address: Wells, St. John, Roberts,  
Gregory & Matkin P.S.  
Street Address: 601 W. First Avenue, Ste. 1300  
City: Spokane State: WA Zip: 99201-3828

6. Total number of applications and patents involved. 1  
7. Total fee (37 CFR 3.41). \$ 40  
 Enclosed  
 Authorized to be charged to deposit account  
8. Deposit account number 23-0925  
(Attach duplicate copy of this page if paying by deposit account)

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9. Statement and Signature.  
*To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.*  
Lance R. Sadler  
Name of Person Signing  
[Signature]  
Signature  
3/19/99  
Date  
TOTAL NUMBER OF PAGES INCLUDING COVER SHEET, ATTACHMENTS AND DOCUMENT: 4

OMB No. 0651-0011 (exp. 9/94)

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BOX ASSIGNMENTS  
WASHINGTON, D.C. 20231

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1 ASSIGNMENT

2 PARTIES TO THE ASSIGNMENT:

3 INVENTORS:

4 David Ziger

5  
6 ASSIGNEE:

7 VLSI Technology, Inc.  
8 Corporation of the State of Delaware  
9 1109 McKay Drive  
10 San Jose, CA 95131

11 BACKGROUND OF THIS ASSIGNMENT:

12 Inventors have conceived certain new and useful inventions  
13 disclosed in a United States patent application titled Methods of  
14 Inspecting for Mask-Defined, Feature Dimensional Conformity Between  
15 Multiple Masks.

16 VLSI TECHNOLOGY, INC. desires to acquire the entire right,  
17 title and interest in said inventions and with respect to any Letters  
18 Patent that may be granted with respect to the inventions in both the  
19 United States and in all foreign countries.

20 THE PARTIES AGREE AS FOLLOWS:

21 In consideration of good and valuable consideration, the receipt of  
22 which is hereby acknowledged, INVENTORS have sold, assigned and  
23 transferred to VLSI TECHNOLOGY, INC. the full and exclusive right  
24 to said inventions in the United States and in all foreign countries.

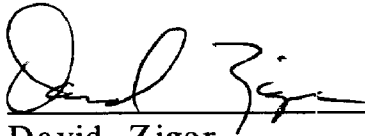
1 INVENTORS do hereby sell, assign and transfer to VLSI  
2 TECHNOLOGY, INC. the entire right, title and interest in the  
3 above-identified application executed concurrently with this assignment  
4 and to any reissues, divisions or continuations thereof, and hereby  
5 authorizes the Commissioner of Patents and Trademarks to issue such  
6 Letters Patent to VLSI TECHNOLOGY, INC. for the sole use of VLSI  
7 TECHNOLOGY, INC. its successors or assigns.

8 INVENTORS further agree to execute, at the request and expense  
9 of VLSI TECHNOLOGY, INC. such other formal documents as may be  
10 required to fully convey the interest transferred herein and will similarly  
11 execute any applications papers required for the filing of any division,  
12 continuation or reissue of the patent application or resulting Letters  
13 Patent; and will generally do everything necessary or desirable to obtain  
14 and enforce proper protection for the inventions assigned hereby.

15 INVENTORS further assign to VLSI TECHNOLOGY, INC. the  
16 whole right, title and interest in the inventions disclosed in the  
17 application throughout all countries foreign to the United States. VLSI  
18 TECHNOLOGY, INC. is hereby authorized to apply for patents relating  
19 to the inventions in its own name in countries where such procedure is  
20 proper; to claim the benefit of the International Convention; to file and  
21 prosecute International Applications relating to the inventions under the  
22 Patent Cooperation Treaty; and to file and prosecute applications  
23 relating to the inventions under the European Patent Convention.  
24 INVENTORS agree to execute applications relating to the inventions in

1 those countries and under those conventions where it is necessary that  
2 the same be executed by the inventor, and to execute assignments of  
3 such applications and the resulting Letters Patent to VLSI  
4 TECHNOLOGY as well as all other necessary papers in relation to such  
5 applications and Letters Patent.

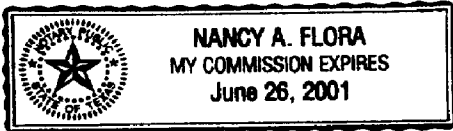
6  
7 Signed at VLSI (San Antonio) on March 5, 1999.

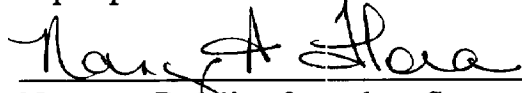
8   
9 \_\_\_\_\_  
10 David Ziger

11 State of Texas

12 County of Bexar

13 On March 5, before me, a Notary Public, personally  
14 appeared David Ziger who is known to me to be the person described  
15 in the foregoing assignment, who executed it and acknowledged such  
16 execution to be voluntary and for the purposes set forth therein.



  
\_\_\_\_\_  
Notary Public for the State of  
Texas residing at  
9651 Westover Hills  
San Antonio TX 78251