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PATENT REEL: 9867 FRAME: 0481

ASSIGNMENT

	WHEREAS,	HIROSHI_K	<u>OGA</u>		
		of Tokyo,	Japan		
	LOGIC CIRCUIT H	AVING REDUCED POWER C	ONSUMPTION		
for w	whichI_am_abou	t_to_make (I/we have made—or am/are	e about to make)	application for Letter	
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			Wiroshi'	Maya ntor's full Signature) HIROSHI KOGA	
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