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2.	Name and address of ra SHINKO ELECT			1 70			
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З.	Nature of conveyance:						
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	Execution Date(s): Dec		16	· · · · · · · · · · · · · · · · · · ·		, .	
4.	Application number(s) (r patent num	ber(s):				
		-		new application:			:
	This document is being filed together with a new application: (a) The execution date(s) of the application is/are: <u>December 2, 1996</u> (b) The title is: <u>METHOD FOR PRODUCING CIRCUIT BOARD, FOR SEMICONDUCTOR PACKAG</u> <u>HAVING CAVITY FOR ACCOMMODATING SEMICONDUCTOR ELEMENT</u> ** OR ***						
	This document is being	filed after filir	ng of the	application:		-	
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5.	Name and address of p	arty to whom	corresp	ondence concerni	ing document she	uld be mailed:	
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PATENT REEL: 9913 FRAME: 0295 STAAS & HALSEY

U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable monsideration peld to the undersigned inventor(s) (hereinsiter "ASSIGNOR") by

SHINKO ELECTRIC INDUSTRIES CO., LTD.

(Insert ASSIGNEE'S Name(s) Address(es))

(Title of Izvention)

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711, Aza Shariden, Caza Kurita, Nagano-shi, Nagano 380, Japan

(hereinsites "ASSIGNEE"), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby soils, easigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled

METHOD FOR PRODUCING CIRCUIT BOARD, FOR SEMICONDUCTOR PACKAGE,

HAVING CAVITY FOR ACCOMPODATING SEMICONDUCTOR ELEMENT

relating to International Petent Application PCT/IP and/or for which application for Letters Patent of the United States was an

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ation,	(b) filed on Serial No. /) is hereby suthorized to insert in (b) the specified			
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and to said application and all Letters Patent(s) of the United States granted on said application and any continuation, division, renoval, substitute, reissue or reasonination application based thereon, for the full term or terms for which the said Lorrers Patent(s) may be granted and including any extensions thereof (collastively, hereisafter, "said application(s) and Latters Patent(s)*).

The ASSIGNOR agree(s), when requested by told ASSIGNEE and without charge to but at the expense of said ASSIGNEE, to do all acts which the ASSIGNEE may deem necessary, desirable or expedient, for securing, maintaining and enforcing protection for said invention, including in the preparation and protection of said application(s) and the issuance of said Letters Patent(s), in any interference, reissue, reexamination, or public use proceeding and in any litigation or other legal proceeding which may arise or be declared in relation to same, such acts to include but not be limited to executing all papers. including separate assignments and declarations, taking all rightful oaths, providing soom testimony, and obtaining and producing evidence.

IN WITNESS WHEREOF, the undersigned inventor(s) has (have) affired his/her/their signature(s).

1.1.102}	" dirock: Sato	Hiroaki Sato	December 2, 1997
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	" direction Sato Bannas Masayestre Eta	Masayoshi Ebe	December 2, 1997
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