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05-26-1999



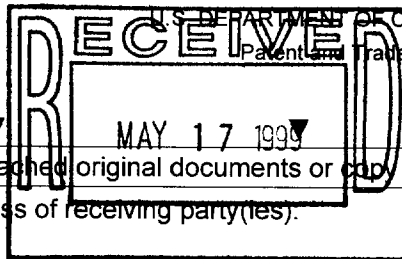
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To the Honorable Commissioner of Patents and Trademarks

COVER SHEET

ONLY

U.S. DEPARTMENT OF COMMERCE
Patent and Trademark Office

Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):
SEE ATTACHED SCHEDULE

2. Name and address of receiving party(ies):

Name: **FOOTHILL CAPITAL CORPORATION, As Agent**

Internal Address: _____

Additional names(s) of conveying party(ies) ☒ Yes ☐ No

3. Nature of conveyance:

☐ Assignment☐ Merger☒ Security Agreement☐ Change of Name☐ Other _____Street Address: **11111 SANTA MONICA BOULEVARD**City: **LOS ANGELES** State: **CA** ZIP: **90025**Execution Date: **05141999**Additional name(s) & address(es) attached? ☐ Yes ☒ No

4. Application number(s) or registration numbers(s):

If this document is being filed together with a new application, the execution date of the application is: _____

A. Patent Application No.(s)

B. Patent No.(s)

SEE
ATTACHED
SCHEDULE

SEE
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SCHEDULE

Additional numbers attached? ☒ Yes ☐ No

5. Name and address of party to whom correspondence concerning document should be mailed:

6. Total number of applications and patents involved: **82**Name: **JOSHUA R. BRESSLER, ESQ.**7. Total fee (37 CFR 3.41):.....\$ **3,280.00**Internal Address: **SCHULTE ROTH & ZABEL LLP**☐ Enclosed - Any excess or insufficiency should be credited or debited to deposit account☒ Authorized to be charged to deposit accountStreet Address: **900 THIRD AVENUE**

8. Deposit account number:

05/24/1999 MTHAI1 00000197 500675 5864555**SCHULTE ROTH & ZABEL LLP - 500675****01 FC:581 3280.00 CH**City: **NEW YORK** State: **NY** ZIP: **10022**

DO NOT USE THIS SPACE

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

JOSHUA R. BRESSLER**MAY 14, 1999**

Name of Person Signing

Signature

Date

Total number of pages including cover sheet, attachments, and document:

38

PATENT

REEL: 9935 FRAME: 0139

Names and Addresses of Conveying Parties:

**1. Name : GENERAL DATACOMM, INC.
(DELAWARE CORPORATION)**

**Address : 1579 STRAITS TURNPIKE
MIDDLEBURY, CONNECTICUT 06762**

**2. Name : GENERAL DATACOMM INDUSTRIES, INC.
(DELAWARE CORPORATION)**

**Address : 1579 STRAITS TURNPIKE
MIDDLEBURY, CONNECTICUT 06762**

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May 17, 1999

BY HAND

Commissioner of Patents and Trademarks
Box Assignments
Washington, DC 20231

Re: Recordation of
Patent Security Agreement
Our ref.: General Datacomm 025983/0025

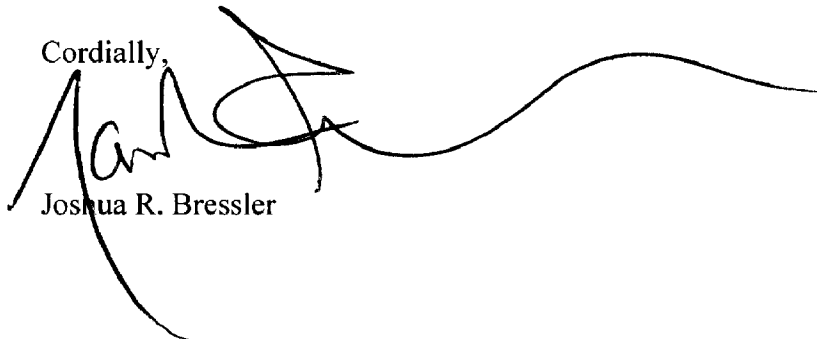
Dear Sir or Madam:

Please record the above-referenced document against the U.S. patent filings identified in the accompanying Document Cover Sheet.

We authorize the deduction from Schulte Roth & Zabel LLP's Deposit Account No. 500675 of \$3,280.00, plus any deficiencies therein, to cover recordation fees, and we enclose an extra copy of the Document Cover Sheet for this purpose.

We also enclose an additional copy of this letter, which we request that you date stamp and return to the person submitting the document so that we may have a record of your receipt of this filing.

Cordially,


Joshua R. Bressler

Enclosures

SRZNY\563421v1

PATENT
REEL: 9935 FRAME: 0141

SCHEDULE 5.16**GENERAL DATACOMM U.S. PATENTS**

TITLE	PATENT NO. (ISSUED)	CURRENT OWNER OF RECORD
Method and Apparatus for Generating a Proxy Connection Endpoint for Operation Administration and Management (OAM) Asynchronous Transfer Mode (ATM) Cells	5,864,555 (1/26/99)	General DataComm, Inc.
Methods, Apparatus, and Systems for Transporting Multimedia Conference Data Streams Through a Transport Network	5,844,600 (12/1/98)	General DataComm, Inc.
ATM Network Switch with Congestion Level Signalling for Controlling Cell Buffers	5,841,773 (11/24/98)	General DataComm, Inc.
High Speed Synchronous Digital Data Bus System Having Unterminated Data and Clock Buses	5,818,884 (10/30/98)	General DataComm, Inc.
ATM Cell Switch	5,796,956 (8/18/98)	General DataComm Advanced Research Centre Ltd.
Systems and Methods for Routing ATM Switched Virtual Circuit Calls	5,781,529 (7/14/98)	General DataComm, Inc.
TMS 3000 Software Fallback	5,754,785 (5/19/98)	General DataComm, Inc.
Apparatus and Method for Transferring Operation, Administration and Management Cells Across and ATM Data Frame User Network Interface	5,712,853 (1/27/98)	General DataComm, Inc.
Multimedia Multipoint Telecommunications Reservation Systems	5,680,392 (10/21/97)	General DataComm, Inc.
Apparatus and Method for Detection of Operation Administration and Management (OAM) Cell Loopback and Physical Loopbacks in an Asynchronous Transfer Mode (ATM) Network	5,659,540 (8/19/97)	General DataComm, Inc.
Isolated High Impedance, DC Current Sources for Telecommunications Applications	5,642,412 (6/24/97)	General DataComm, Inc.

TITLE	PATENT NO. (ISSUED)	CURRENT OWNER OF RECORD
Time Division Multiplexed Backplane with Packet Mode Capability	5,615,211 (3/25/97)	General DataComm, Inc.
Multiport Multidrop Digital System	5,524,107 (6/4/96)	General DataComm, Inc.
Standing Sine Wave Clock Bus for Clock Distribution Systems	5,517,532 (5/14/96)	General DataComm, Inc.
Output Characteristics Stabilization of CMOS Devices	5,510,729 (4/23/96)	General DataComm, Inc.
Diagnostic Channel Wakeup Sequence for Network Managed Fractional T1 or E1 Digital Service Units	5,495,516 (2/27/96)	General DataComm, Inc.
Distributed Frame Processing for Time Division Multiplexing	5,469,434 (11/21/95)	General DataComm, Inc.
Packet Data Network Switch Having Internal Fault Detection and Correction	5,461,609 (10/24/95)	General DataComm Advanced Research Centre Ltd.
ATM Cell Switch Suitable For Multicast Switching	5,436,893 (7/25/95)	General DataComm Advanced Research Centre Ltd.
Transformed Current Sensing Replay for Use in Switched Network Modems and Circuit Incorporating Same	5,426,698 (6/20/95)	General DataComm, Inc.
Connector System Having Switching and Testing Functions Using Tapered Spring Contact Elements and Actuators Therefor	5,425,649 (6/20/95)	General DataComm, Inc.
Spring Biased Tapered Contact Elements for Electrical Connectors and Integrated Circuit Packages	5,366,380 (11/22/94)	General DataComm, Inc.
Grounding Spring Clip for Modular Jacks	5,348,484 (9/20/94)	General DataComm, Inc.
Automatic Detector and Selector of RS-232 or V.35 Interface	5,331,672 (7/19/94)	General DataComm, Inc.

TITLE	PATENT NO. (ISSUED)	CURRENT OWNER OF RECORD
Electrical Connectors for Direct Connection to Plated Through Holes in Circuit Board	5,256,073 (10/26/93)	General DataComm, Inc.
Electrical Connectors Having Tapered Spring Contact Elements for Direct Mating to Holes	5,215,471 (6/1/93)	General DataComm, Inc.
Integrated Circuit Packages Using Tapered Spring Contact Leads for Direct Mounting to Circuit Boards	5,160,270 (11/3/92)	General DataComm, Inc.
Apparatus and Method for Determining Line Rates	5,157,651 (10/20/92)	General DataComm, Inc.
Electrical Connector for Direct Connection to Plated Through Holes in Circuit Board	4,966,556 (10/30/90)	General DataComm, Inc.
System and Apparatus for Providing Three Dimensions of Input into a Host Processor	4,961,138 (10/2/90)	General DataComm, Inc.
Multiplexer Frame Synchronization Technique	4,930,125 (5/29/90)	General DataComm, Inc.
Digital Signal Processor Architecture with an ALU and a Serial Processing Section Operating in Parallel	4,926,355 (5/15/90)	General DataComm, Inc.
Microinstruction Sequencer for Instructing Arithmetic, Logical and Data Move Operations in a Conditional Manner	4,891,754 (1/2/90)	General DataComm, Inc.
Algorithm for Selecting Channels for Multiplexer Frame	4,888,770 (12/19/89)	General DataComm, Inc.
Parallel Arithmetic-Logic Unit for as an Element of Digital Signal Processor	4,888,722 (12/19/89)	General DataComm, Inc.
Algorithm for Minimizing Excursion in Placement of Channel Selects into Multiplexer Frame	4,885,745 (12/5/89)	General DataComm, Inc.
Framing Algorithm for BIT Interleaved Time Division Multiplexer	4,881,224 (11/14/89)	General DataComm, Inc.
Captive Screw and Assembly	4,877,364 (10/31/89)	General DataComm, Inc.

TITLE	PATENT NO. (ISSUED)	CURRENT OWNER OF RECORD
Serial Arithmetic Processor	4,858,163 (8/15/89)	General DataComm, Inc.
Active Transconductance Filter Device	4,839,542 (6/13/89)	General DataComm Industries, Inc.
High Speed Bit Interleaved Time Division Multiplexer for Multinode Communication System	4,815,074 (3/21/89)	General DataComm, Inc.
Circular First-In, First Out Buffer System	4,803,654 (2/7/89)	General DataComm Industries, Inc.
Slide Lock Mechanism	4,783,034 (11/8/98)	General DataComm, Inc.
Multi Circuit Board Card Enclosure	4,717,216 (1/5/88)	General DataComm, Inc.
Precision Current Rectifier for Rectifying Input Current	4,745,395 (5/17/88)	General DataComm, Inc.
Method for Establishing and Maintaining Synchronization Between Communicating Multiplexers Using Checksum Values	4,729,123 (3/1/88)	General DataComm, Inc.
Variable Control and Data Rates in Highly Efficient Multiplexer	4,727,536 (2/23/88)	General DataComm, Inc.
BIT Interleaved Multiplexer System Providing BYTE Synchronization for Communicating Apparatuses	4,710,920 (12/1/87)	General DataComm, Inc.
Regenerative Strobe Circuit for CMOS Programmable Logic Array	4,692,639 (9/8/87)	General DataComm, Inc.
Analog-digital Converter	4,691,190 (9/1/87)	General DataComm, Inc.
Bidirectional Transconductance Amplifier	4,689,607 (8/25/87)	General DataComm, Inc.
Continuous Time Domain Analog-Digital Converter	4,667,180 (5/19/87)	General DataComm, Inc.

TITLE	PATENT NO. (ISSUED)	CURRENT OWNER OF RECORD
Automatic Framing in Time Division Multiplexer	4,460,993 (7/17/84)	General DataComm Industries, Inc.
Standard Independent Multimedia Multipoint Server	08/942,675 (10/22/97)	General DataComm, Inc.
Multicast Delivery Tree Switching	08/891,176 (7/10/97)	General DataComm, Inc.
TFTP Architecture to Transfer Non-file SNMP's MIB for ATM Billing System	08/796,079 (2/5/97)	General DataComm, Inc.
Inband IP Packet Processing	08/658,842 (5/31/96)	General DataComm, Inc.
Call Correlation Tags for Billing/Statistics	08/622,897 (3/29/96)	General DataComm, Inc.
Data Network Switch with Fault Tolerance	08/930,973 (10/7/97)	General DataComm, Inc.
A Traffic Shaping ATM Network Switch	08/913,815 (9/15/97)	General DataComm, Inc.
ATM Network Switch Congestion Control	08/973,441 (12/4/97)	General DataComm, Inc.
Controlling the Flow of ATM Cells in an ATM Network	08/973,442 (12/4/97)	General DataComm, Inc.
Method & Apparatus for Connection Admission Control of Variable Bit Rate Traffic in an ATM Switch	08/942,021 (10/4/96)	General DataComm, Inc.
ATM Network Switch Having Enhanced Call Setup Capability	09/029,585 (2/27/98)	General DataComm, Inc.
ATM Switch with VC Priority Buffers	09/029,295 (3/5/98)	General DataComm, Inc.
Controlled Available Bit Rate Service in an ATM Switch	09/077,219 (5/19/98)	General DataComm, Inc.
Panel Mount Feed Through Direct Connecting Power Block	08/796,080 (2/5/97)	General DataComm, Inc.

TITLE	PATENT NO. (ISSUED)	CURRENT OWNER OF RECORD
MMS Call Setup Circuit Caching	08/915,496 (8/20/97)	General DataComm, Inc.
Conference Reservation Acceptance Algorithm	08/877,463 (6/16/97)	General DataComm, Inc.
Audio Automatic Gain Control Algorithm	08/888,571 (7/7/97)	General DataComm, Inc.
Enhanced Etched PCB Inductor	08/853,813 (5/9/97)	General DataComm, Inc.
Voice Server Module for ATM Switch	09/015,303 (1/29/98)	General DataComm, Inc.
ATM Switch Voice Server Module Having DSP Array	09/015,302 (1/29/98)	General DataComm, Inc.
ATM Switch Voice Server Module Utilizing Flag Signalling	09/015,301 (1/29/98)	General DataComm, Inc.
Precedence and Preemption in ATM Call Admission Control	08/853,654 (5/9/97)	General DataComm, Inc.
Hot Insertable UTOPIA Interface	09/172,545 (10/14/98)	General DataComm, Inc.
Automatic Backup and Restoral for Single Point T1 Failures	09/162,470 (9/28/98)	General DataComm, Inc.
Card Panel with Integral EMI Spring Track	09/099,790 (6/18/98)	General DataComm, Inc.
An Explicit Rate ABR Algorithm for New- Generation of ATM Switches	60/093,826 (7/22/98)	General DataComm, Inc.
A Dynamic Buffer Management for ATM Switches	60/093,681 (7/22/98)	General DataComm, Inc.
Method for Mapping Narrowband DSO Circuits into AAL Type 2 SVCs	09/289,463 (4/9/99)	General DataComm, Inc.
Derivation of AAL Type 2 SVC Characteristics Based Upon Signalled Narrowband Parameter	09/289,464 (4/9/99)	General DataComm, Inc.

PATENT SECURITY AGREEMENT

THIS PATENT SECURITY AGREEMENT (this "Agreement"), dated as of May 14, 1999 is made by **GENERAL DATACOMM INDUSTRIES, INC.**, a Delaware corporation, and **GENERAL DATACOMM, INC.**, a Delaware corporation (collectively, "Debtors"), on the one hand, and, on the other hand, **FOOTHILL CAPITAL CORPORATION**, a California corporation, as agent for the "Lenders" from time to time party to the Loan Agreement (in such capacity, "Secured Party").

RECITALS

A. Debtors, DataComm Leasing Corporation, Vital Network Services, L.L.C., GDC Naugatuck, Inc. and GDC Federal Systems, Inc. (collectively, the "Borrowers"), Lenders and Secured Party have entered into that certain Loan and Security Agreement, of even date herewith (as amended, restated, modified, supplemented, refinanced, renewed, or extended from time to time, the "Loan Agreement"), pursuant to which Lenders have agreed to make certain loans and other extensions of credit to or for the benefit of the Borrowers.

B. The Borrowers have granted to Secured Party, for the benefit of the Lenders, a continuing first priority security interest in (among other things) all general intangibles of the Borrowers in order to secure the Borrowers' obligations under each of the Loan Documents to which the Borrowers are party.

C. Pursuant to the Loan Agreement and as one of the conditions precedent to the obligations of Lenders under the Loan Agreement, Debtors have agreed to execute and deliver this Agreement to Secured Party for filing with the United States Patent and Trademark Office and with any other relevant recording systems in any domestic or foreign jurisdiction, and as further evidence of and to effectuate the existing security interests of Secured Party (for the benefit of the Lenders) in the patents and other general intangibles described herein.

ASSIGNMENT

NOW, THEREFORE, for valuable consideration, the receipt and adequacy of which are hereby acknowledged, Debtors hereby agree in favor of Secured Party as follows:

1. Definitions: Interpretation.

(a) Certain Defined Terms. As used in this Agreement, the following terms shall have the following meanings:

"Patent Collateral" has the meaning set forth in Section 2.

"Patents" has the meaning set forth in Section 2.

"Proceeds" means whatever is receivable or received from or upon the sale, lease, license, collection, use, exchange or other disposition, whether voluntary or

involuntary, of any Patent Collateral, including "proceeds" as defined at UCC Section 9-306, and all proceeds of proceeds. Proceeds shall include (i) any and all accounts, chattel paper, instruments, general intangibles, cash and other proceeds, payable to or for the account of Debtors, from time to time in respect of any of the Patent Collateral, (ii) any and all proceeds of any insurance, indemnity, warranty or guaranty payable to or for the account of Debtors from time to time with respect to any of the Patent Collateral, (iii) any and all claims and payments (in any form whatsoever) made or due and payable to Debtors from time to time in connection with any requisition, confiscation, condemnation, seizure or forfeiture of all or any part of the Patent Collateral by any Person acting under color of governmental authority, and (iv) any and all other amounts from time to time paid or payable under or in connection with any of the Patent Collateral or for or on account of any damage or injury to or conversion of any Patent Collateral by any Person.

"PTO" means the United States Patent and Trademark Office and any successor thereto.

"Secured Obligations" means all liabilities, obligations, or undertakings owing by Debtors to Secured Party or Lenders of any kind or description arising out of or outstanding under, advanced or issued pursuant to, or evidenced by the Loan Agreement, the other Loan Documents, or this Agreement, including, without limitation, all Advances, Term Loans and Letters of Credit, irrespective of whether for the payment of money, whether direct or indirect, absolute or contingent, due or to become due, voluntary or involuntary, whether now existing or hereafter arising, and including all interest (including interest that accrues after the filing of a case under the Bankruptcy Code) and any and all costs, fees (including attorneys' fees), and expenses which Debtors are required to pay pursuant to any of the foregoing, by law, or otherwise.

"United States" and "U.S." each mean the United States of America.

(b) Terms Defined in UCC. Where applicable and except as otherwise defined herein, terms used in this Agreement shall have the meanings ascribed to them in the UCC.

(c) Interpretation. In this Agreement, except to the extent the context otherwise requires:

(i) Any reference to a Section or a Schedule is a reference to a section hereof, or a schedule hereto, respectively, and to a subsection or a clause is, unless otherwise stated, a reference to a subsection or a clause of the Section or subsection in which the reference appears.

(ii) The words "hereof," "herein," "hereto," "hereunder" and the like mean and refer to this Agreement as a whole and not merely to the specific Section, subsection, paragraph or clause in which the respective word appears.

(iii) The meaning of defined terms shall be equally applicable to both the singular and plural forms of the terms defined.

(iv) The words "including," "includes" and "include" shall be deemed to be followed by the words "without limitation."

(v) References to agreements and other contractual instruments shall be deemed to include all subsequent amendments and other modifications thereto.

(vi) References to statutes or regulations are to be construed as including all statutory and regulatory provisions consolidating, amending or replacing the statute or regulation referred to.

(vii) Any captions and headings are for convenience of reference only and shall not affect the construction of this Agreement.

(viii) Capitalized words not otherwise defined herein shall have the respective meanings ascribed to them in the Loan Agreement and the rules of construction set forth in Section 1 of the Loan Agreement shall likewise govern this Agreement.

(ix) In the event of a direct conflict between the terms and provisions of this Agreement and the Loan Agreement, it is the intention of the parties hereto that both such documents shall be read together and construed, to the fullest extent possible, to be in concert with each other. In the event of any actual, irreconcilable conflict that cannot be resolved as aforesaid, the terms and provisions of the Loan Agreement shall control and govern; provided, however, that the inclusion herein of additional obligations on the part of the Debtors and supplemental rights and remedies in favor of Secured Party (whether under New York law or applicable federal law), in each case in respect of the Patent Collateral, shall not be deemed a conflict with the Loan Agreement.

2. Security Interest.

(a) Assignment and Grant of Security Interest.

(i) To secure the payment and performance of the Other Obligations, each of the Debtors hereby grants, assigns, transfers, and conveys a continuing first priority security interest to Secured Party (for the benefit of the Lenders) in all of such Debtors' right, title and interest in, to and under the following property, whether now existing or hereafter acquired or arising, and whether registered or unregistered (collectively, the "Patent Collateral"):

(A) all letters patent of the U.S. or any other country, all registrations and recordings thereof (including industrial design registrations), and all applications for letters patent of the U.S. or any other country, owned, held or

used by any Debtor in whole or in part, including all existing U.S. patents and patent applications of any Debtor which are described in Schedule A hereto, as the same may be amended or supplemented pursuant hereto from time to time, and together with and including all patent licenses held by any Debtor, together with all reissues, divisions, renewals, extensions, provisional applications, continuations, and continuations-in-part thereof and the inventions disclosed therein, and all rights corresponding thereto throughout the world, including the right to make, use, lease, sell and otherwise transfer the inventions disclosed therein, and all proceeds thereof, including all license royalties and proceeds of infringement suits (collectively, the "Patents");

(B) all claims, causes of action and rights to sue for past, present or future infringement or unconsented use of any of the Patents and all rights arising therefrom and pertaining thereto;

(C) all general intangibles (as defined in the UCC) and all intangible intellectual or other similar property of any Debtor of any kind or nature, whether now owned or hereafter acquired or developed, associated with or arising out of any of the Patents and not otherwise described above; and

(D) all products and Proceeds of any and all of the foregoing.

(ii) To secure the payment and performance of the Term Loan Obligations, each of the Debtors hereby grants, assigns, transfers, and conveys a continuing first priority security interest to Secured Party (for the benefit of the Lenders) in all of such Debtors' right, title and interest in, to and under the Patent Collateral.

(iii) Secured Party's security interest in the Patent Collateral securing the Other Obligations shall have priority over Secured Party's security interest in the Patent Collateral securing the Term Loan Obligations.

(b) Continuing Security Interest. Each of the Debtors agrees that this Agreement shall create a continuing first priority security interest in the Patent Collateral which shall remain in effect until terminated in accordance with Section 17 hereof.

(c) Incorporation into Loan Agreement. This Agreement shall be fully incorporated into the Loan Agreement and all understandings, agreements and provisions contained in the Loan Agreement shall be fully incorporated into this Agreement. Without limiting the foregoing, the Patent Collateral described in this Agreement shall constitute part of the Collateral in the Loan Agreement.

3. Further Assurances; Appointment of Secured Party as Attorney-in-Fact. Debtors at their own expense shall execute and deliver, or cause to be executed and delivered, to Secured Party any and all documents and instruments, in form and substance satisfactory to Secured Party, and take any and all action, which Secured Party may reasonably request from

time to time, to perfect and continue perfected, maintain the priority of or provide notice of Secured Party's security interest in the Patent Collateral and to accomplish the purposes of this Agreement. Secured Party shall have the right to, in the name of Debtors, or in the name of Secured Party or otherwise, without notice to or assent by Debtors, and Debtors hereby irrevocably constitute and appoint Secured Party (and any of Secured Party's officers or employees or agents designated by Secured Party) as Debtors' true and lawful attorney-in-fact with full power and authority, after the occurrence and during the continuance of any Event of Default, (i) to sign the name of Debtors on all or any of such documents or instruments and perform all other acts that Secured Party deems necessary or advisable in order to perfect or continue perfected, maintain the priority or enforceability of or provide notice of Secured Party's security interest in, the Patent Collateral, and (ii) to execute any and all other documents and instruments, and to perform any and all acts and things for and on behalf of Debtors, which Secured Party may deem necessary or advisable to maintain, preserve and protect the Patent Collateral and to accomplish the purposes of this Agreement, including (A) to defend, settle, adjust or institute any action, suit or proceeding with respect to the Patent Collateral, (B) to assert or retain any rights under any license agreement for any of the Patent Collateral, including any rights of Debtors arising under Section 365(n) of the Bankruptcy Code, and (C) to execute any and all applications, documents, papers and instruments for Secured Party to use the Patent Collateral, to grant or issue any exclusive or non-exclusive license with respect to any Patent Collateral (it being understood that so long as no Event of Default has occurred and is continuing, Debtors may grant or issue licenses in the ordinary course of business with respect to the Patent Collateral), and to assign, convey or otherwise transfer title in or dispose of the Patent Collateral, provided, however, if Secured Party requests that Debtors execute such documents or perform such acts described in clauses (i) and (ii) above and Debtors fail to do so to the reasonable satisfaction of Secured Party, then Secured Party shall have the right to execute such documents or perform such acts described in clauses (i) and (ii) above notwithstanding the nonoccurrence of any Event of Default. The power of attorney set forth in this Section 3, being coupled with an interest, is irrevocable so long as this Agreement shall not have terminated in accordance with Section 17.

4. Representations and Warranties. Debtors represent and warrant to Secured Party as follows:

(a) No Other Patents. Schedule A sets forth a true and correct list of all of the existing Patents that currently are issued or registered, or for which any currently pending application for registration has been filed, with the PTO or any corresponding or similar trademark office of any other U.S. (federal or state) or foreign jurisdiction, and that are owned or otherwise held or used by Debtors.

(b) Validity. Each of the Patents listed in Schedule A is subsisting and, except as disclosed in Schedule A, has not been adjudged invalid or unenforceable, in whole or in part, and, to the best of Debtors' knowledge, each of the Patents is valid and enforceable.

(c) Ownership of Patent Collateral; No Violation. Debtors have rights in and good and defensible title to the existing Patent Collateral. With respect to the Patent

Collateral shown on Schedule A hereto as owned by them, Debtors are the sole and exclusive owners thereof, free and clear of any Liens and rights of others (other than Liens in favor of Secured Party covenants by Debtors not to sue third persons and the rights of the licensee of a license provided to such licensee by Debtors as licensor), including licenses and shop rights. For any Patent Collateral for which either Debtor is either a licensor or a licensee pursuant to a license or licensing agreement regarding such Patent Collateral: (i) each such license or licensing agreement is in full force and effect; (ii) Debtors are not in default of any of their obligations thereunder; (iii) there exists no event, condition or occurrence which, with the giving of notice or lapse of time, or both, would constitute a breach or default by any Debtor thereunder; (iv) no party to any such agreement has given any Debtor notice of its intention to cancel, terminate or fail to renew any such agreement; and (v) other than the parties to such agreements, no other Person has any rights in or to any of such Patent Collateral. To the best of Debtors' knowledge, the past, present and contemplated future use of the Patent Collateral by Debtors has not, does not and will not infringe upon or violate any right, privilege or license agreement of or with any other Person.

(d) No Infringement. To the best of Debtors' knowledge, no material infringement or unauthorized use presently is being made of any of the Patent Collateral by any Person.

(e) Powers. Debtors have the unqualified right, power and authority to pledge and to grant to Secured Party (for the benefit of the Lenders) a continuing first priority security interest in all of Debtors' right, title, and interest in and to the Patent Collateral pursuant to this Agreement, and to execute, deliver and perform their obligations in accordance with the terms of this Agreement, without the consent or approval of any other Person except as already obtained.

5. Covenants. So long as any of the Secured Obligations remain unsatisfied, Debtors agree that they will comply with all of the covenants, terms and provisions of this Agreement, the Loan Agreement and the other Loan Documents, and Debtors will promptly give Secured Party written notice of the occurrence of any event that could have a material adverse effect on any of the Patents or the Patent Collateral, including (x) any petition under the Bankruptcy Code filed by or against any licensor of any of the Patents for which Debtors are licensees, (y) any notice of opposition or petition for cancellation filed against any of the Patents, and (z) any other claim asserted in a judicial, administrative or other proceeding concerning the Patents.

6. Future Rights. For so long as any of the Secured Obligations shall remain outstanding, or, if earlier, until Secured Party shall have released or terminated, in whole but not in part, its interest in the Patent Collateral, if and when any Debtor shall obtain rights to any additional patentable inventions, or become entitled to the benefit of any Patent, or Patent Collateral or any improvement thereof (whether pursuant to any license or otherwise), the provisions of Section 2 shall automatically apply thereto and Debtors shall give to Secured Party prompt notice thereof. Debtors shall do all things deemed necessary or advisable by Secured Party to ensure the validity, perfection, priority and enforceability of the security interests of

Secured Party in such future acquired Patent Collateral. Debtors hereby authorize Secured Party to modify, amend or supplement the Schedules hereto and to re-execute this Agreement from time to time on Debtors' behalf and as its attorney-in-fact to include any future patents which are or become Patent Collateral and to cause such re-executed Agreement or such modified, amended or supplemented Schedules to be filed with the PTO.

7. Secured Party's Duties. Notwithstanding any provision contained in this Agreement, Secured Party shall have no duty to exercise any of the rights, privileges or powers afforded to it and shall not be responsible to Debtors or any other Person for any failure to do so or delay in doing so. Except for the accounting for moneys actually received by Secured Party hereunder or in connection herewith, Secured Party shall have no duty or liability to exercise or preserve any rights, privileges or powers pertaining to the Patent Collateral.

8. Remedies. From and after the occurrence and during the continuation of an Event of Default, Secured Party shall have all rights and remedies available to it under the Loan Agreement, the other Loan Documents, and applicable law (which rights and remedies are cumulative) with respect to the security interests in any of the Patent Collateral or any other Collateral. Debtors agree that such rights and remedies include the right of Secured Party as a secured party to sell or otherwise dispose of its Collateral after the occurrence and during the continuance of the Event of Default, pursuant to UCC Section 9-504. Debtors agree that Secured Party shall at all times have such royalty-free licenses, to the extent permitted by law, for any Patent Collateral that is reasonably necessary to permit the exercise of any of Secured Party's rights or remedies upon and during the continuance of an Event of Default with respect to (among other things) any asset of Debtors in which Secured Party has a security interest, including Secured Party's rights to sell, lease, or license General Intangibles, inventory, tooling or packaging which is acquired by Debtors (or their successors, permitted assignees or trustees in bankruptcy). In addition to and without limiting any of the foregoing, upon the occurrence and during the continuance of an Event of Default, Secured Party shall have the right but shall in no way be obligated to bring suit, or to take such other action as Secured Party deems necessary or advisable, in the name of Debtors or Secured Party, to enforce or protect any of the Patent Collateral, in which event Debtors shall, at the request of Secured Party, do any and all lawful acts and execute any and all documents required by Secured Party in aid of such enforcement. To the extent that Secured Party shall elect not to bring suit to enforce such Patent Collateral, Debtors agree to use all reasonable measures and its diligent efforts, whether by action, suit, proceeding, license or otherwise, to prevent the infringement, misappropriation or violation thereof by others and for that purpose agrees diligently to maintain any action, suit or proceeding against any Person necessary and appropriate to prevent such infringement, misappropriation or violation.

9. Binding Effect. This Agreement shall be binding upon, inure to the benefit of and be enforceable by Debtors and Secured Party and their respective successors and assigns.

10. Notices. All notices and other communications hereunder shall be in writing and shall be mailed, sent or delivered in accordance with the Loan Agreement.

11. GOVERNING LAW AND VENUE: JURY TRIAL WAIVER. THIS AGREEMENT SHALL BE GOVERNED BY AND CONSTRUED IN ACCORDANCE WITH THE LAWS OF THE STATE OF NEW YORK, APPLICABLE TO CONTRACTS MADE AND WHOLLY PERFORMED THEREIN, EXCEPT TO THE EXTENT THAT THE VALIDITY OR PERFECTION OF THE ASSIGNMENT AND SECURITY INTERESTS HEREUNDER IN RESPECT OF ANY PROPERTY ARE GOVERNED BY FEDERAL LAW, IN WHICH CASE SUCH CHOICE OF NEW YORK LAW SHALL NOT BE DEEMED TO DEPRIVE SECURED PARTY OF SUCH RIGHTS AND REMEDIES AS MAY BE AVAILABLE UNDER FEDERAL LAW. THE VALIDITY OF THIS AGREEMENT, CONSTRUCTION, INTERPRETATION, AND ENFORCEMENT, AND THE RIGHTS OF THE PARTIES HERETO SHALL BE DETERMINED UNDER, GOVERNED BY, AND CONSTRUED IN ACCORDANCE WITH THE LAWS OF THE STATE OF NEW YORK. THE PARTIES AGREE THAT ALL ACTIONS OR PROCEEDINGS ARISING IN CONNECTION WITH THIS AGREEMENT SHALL BE TRIED AND LITIGATED ONLY IN THE STATE AND FEDERAL COURTS LOCATED IN THE COUNTY OF NEW YORK, STATE OF NEW YORK OR, AT THE SOLE OPTION OF SECURED PARTY, IN ANY OTHER COURT IN WHICH SECURED PARTY SHALL INITIATE LEGAL OR EQUITABLE PROCEEDINGS AND WHICH HAS SUBJECT MATTER JURISDICTION OVER THE MATTER IN CONTROVERSY. EACH DEBTOR AND SECURED PARTY WAIVES, TO THE EXTENT PERMITTED UNDER APPLICABLE LAW, ANY RIGHT EACH MAY HAVE TO ASSERT THE DOCTRINE OF FORUM NON CONVENIENS OR TO OBJECT TO VENUE TO THE EXTENT ANY PROCEEDING IS BROUGHT IN ACCORDANCE WITH THIS SECTION 11.

EACH DEBTOR AND THE SECURED PARTY HEREBY WAIVE THEIR RESPECTIVE RIGHTS TO A JURY TRIAL OF ANY CLAIM OR CAUSE OF ACTION BASED UPON OR ARISING OUT OF THIS AGREEMENT OR ANY OF THE TRANSACTIONS CONTEMPLATED HEREIN, INCLUDING CONTRACT CLAIMS, TORT CLAIMS, BREACH OF DUTY CLAIMS, AND ALL OTHER COMMON LAW OR STATUTORY CLAIMS. EACH DEBTOR AND THE SECURED PARTY REPRESENT THAT EACH HAS REVIEWED THIS WAIVER AND EACH KNOWINGLY AND VOLUNTARILY WAIVES ITS JURY TRIAL RIGHTS FOLLOWING CONSULTATION WITH LEGAL COUNSEL. IN THE EVENT OF LITIGATION, A COPY OF THIS AGREEMENT MAY BE FILED AS A WRITTEN CONSENT TO A TRIAL BY THE COURT.

12. Entire Agreement; Amendment. This Agreement, together with the Schedules hereto, the Loan Agreement and the Loan Documents, contains the entire agreement of the parties with respect to the subject matter hereof and supersedes all prior drafts and communications relating to such subject matter. Neither this Agreement nor any provision hereof may be modified, amended or waived except by the written agreement of the parties, as provided in the Loan Agreement. Notwithstanding the foregoing, Secured Party may re-execute this Agreement or modify, amend or supplement the Schedules hereto as provided in Section 6 hereof.

13. Severability. If one or more provisions contained in this Agreement shall be invalid, illegal or unenforceable in any respect in any jurisdiction or with respect to any party, such invalidity, illegality or unenforceability in such jurisdiction or with respect to such party shall, to the fullest extent permitted by applicable law, not invalidate or render illegal or unenforceable any such provision in any other jurisdiction or with respect to any other party, or any other provisions of this Agreement.

14. Counterparts. This Agreement may be executed in any number of counterparts and by different parties hereto in separate counterparts, each of which when so executed shall be deemed to be an original and all of which taken together shall constitute but one and the same agreement.

15. Loan Agreement. Debtors acknowledge that the rights and remedies of Secured Party with respect to the security interest in the Patent Collateral granted hereby are more fully set forth in the Loan Agreement and all such rights and remedies are cumulative.

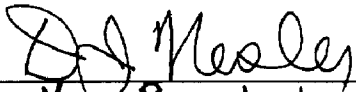
16. No Inconsistent Requirements. Debtors acknowledge that this Agreement and the other Loan Documents may contain covenants and other terms and provisions variously stated regarding the same or similar matters, and Debtors agree that all such covenants, terms and provisions are cumulative and all shall be performed and satisfied in accordance with their respective terms.

17. Termination. Upon the indefeasible payment in full of the Secured Obligations, including the cash collateralization, expiration, or cancellation of all Secured Obligations, if any, consisting of Letters of Credit, and the full and final termination of any commitment to extend any financial accommodations under the Loan Agreement, this Agreement shall terminate and Secured Party shall execute and deliver such documents and instruments and take such further action reasonably requested by Debtors and at Debtors' expense as shall be necessary to evidence termination of the security interest granted by Debtors to Secured Party hereunder.

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IN WITNESS WHEREOF, the parties hereto have duly executed this Agreement,
as of the date first above written.

GENERAL DATACOMM INDUSTRIES, INC.,
a Delaware corporation

By: 
Title: Vice President

GENERAL DATACOMM, INC.
a Delaware corporation

By: 
Title: Vice President

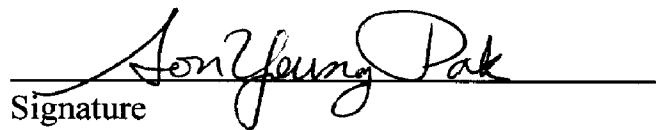
FOOTHILL CAPITAL CORPORATION,
a California corporation

By: 
Title: Vice President

STATE OF NEW YORK)
) ss.:
COUNTY OF NEW YORK)

On May 14, 1999, before me, S.Y. Pak, Notary Public, personally
appeared Dennis Nesler, personally known to me (or proved to me on the basis of
satisfactory evidence) to be the person(s) whose name(s) is/are subscribed to the within
instrument and acknowledged to me that he/she/they executed the same in his/her/their
authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or
the entity upon behalf of which the person(s) acted, executed the instrument.

WITNESS my hand and seal.


Signature

[SEAL]

COMM. # _____
Notary Public – Connecticut

COUNTY
My Comm. Expires _____

SON YOUNG PAK
NOTARY PUBLIC, State of New York
No. 01PA5070584
Qualified in New York County
Certificate Filed in New York County
Commission Expires ~~Dec. 20, 1999~~ 2/3/01

FROM: DAVID GORDON LAW OFFICES - UPDATED APRIL 28, 1999

CASE #	INVENTOR	TITLE	DATE OF FILING	SERIAL#	STATUS	FOREIGN	
						PATENT #	FILED
049	POD1974	Card Enclosure	8/13/85	765,282	ISSUED	4,717,216	
DC-975A	Sorrentino	Captive Screw and Assembly	5/26/87	054,299	ISSUED	4,877,364	NO
061	Robinson	A/D Converter	1/27/86	822,396	ISSUED	4,667,180	YES
	Robinson	A/D Converter (Voltage)	1/28/87	007,715	ISSUED	4,689,607	
	Robinson	Current Rectifier	1/28/87	007,714	ISSUED	4,745,395	Not renewed
	Robinson	Transconductance Amp	1/28/87	007,717	ISSUED	4,691,190	
063	Jordan	PLA Precharge Circuit	1/23/85	821,176	ISSUED	4,692,639	Not renewed
066	Jacobsen	MUX Bus Structure	8/01/86	893,169	ISSUED	4,815,074	YES
067	Manning	Byte Synch for Bit Interleaved MUX	6/19/86	876,229	ISSUED	4,710,920	YES-Not renewed
068/070	Manning/Reeves	Highly Efficient MUX with Variable Control and Data Rates	6/19/86	867,232	ISSUED	4,727,536	
004	Boreland	ADPCM DSP	7/02/87	069,433	ISSUED	4,926,355	YES-Not renewed
	Boreland	ADPCM DSP		470,109	ABANDONED		
004	Boreland	Microinstruction Sequencer	7/02/87	069,430	ISSUED	4,891,754	YES
004	Boreland	Serial Arithmetic Processor	7/02/87	069,432	ISSUED	4,858,163	YES
004	Boreland	PALU	7/02/87	069,431	ISSUED	4,888,722	YES
079	Wheen	Checksum Frame Synch	8/12/86	896,259	ISSUED	4,729,123	
108	Gorniak	Pressure Sensitive Mouse	5/1/87	044,969	ISSUED	4,961,138	YES
08A					10/2/89	418,969	
044	Ostrander et al	Slide Lock Mechanism	2/11/87	013,507	ISSUED	4,783,034	

PATENT

REEL: 9935 FRAME: 0159

D #	CASE	INVENTOR	TITLE	DATE OF FILING	SERIAL#	STATUS	PATENT #	FOREIGN FILED
0103	GDC-024	K. Bains	Framing Algorithm	10/19/88	259,803	ISSUED	4,881,224	Aus, Can, EPO
0104	GDC-026	K. Bains	Synchronization Technique	1/30/89	304,015	ISSUED	4,930,125	Aus Can, EPO
0105A	GDC-025A	K. Bains	Excursion Algorithm	10/19/88	259,856	ISSUED	4,888,770	Aus Can, EPO
0105B	GDC-025B	D. Gordon	Optimal Excursion Algorithm	10/19/88	259,855	ISSUED	4,885,745	Aus Can, EPO
0120	GDC-030	Reymond/Sorrentino	Vector Connector	6/13/89	366,546	ISSUED	4,966,556	CN, EPO, JP
0120C1	GDC-030C1	Reymond/Sorrentino	Vector Connector	10/29/90	605,521	ISSUED	5,256,073	CN, EPO, JP
0120CIP1	GDC-030CIP1	Reymond/Sorrentino	Vector Connector	10/29/90	605,523	ISSUED	5,215,471	CN, EPO, JP
0120CIP2	GDC-030CIP2	Reymond/Sorrentino	Vector Switches	10/29/90	605,522	Abandoned	in favor of GDC-030 CIP4	
01004	GDC-030CIP3	Reymond	Vector IC's	05/08/91	697,201	ISSUED	5,160,270	CN, EPO, JP
0120CIP4	GDC-030CIP4	Reymond	Vector Switches	10/26/93	143,443	ISSUED	5,425,649	
090009	GDC-034	Ghelberg et al	Line Rates	7/26/90	558,701	ISSUED	5,157,651	AUS, CN
091001	GDC-039	Evans/Vellucci	RS232/V.35 Auto-Detect	7/09/91	727,822	ISSUED	5,331,672	Aus, Can, EPO
090013	GDC-040	Duggan et al.	Multipoint/Multidrop	8/26/91	749,897	Abandoned for GDC-040C1		
090013A	GDC-040C1			5/2/94	196,173	5,524,107	Aus, Can, EPO	
091006	GDC-041	Reymond	Sine-Wave Back Plane	10/26/93	143,442	ISSUED	5,517,532	
092003	GDC-046	Abdelsayed	Secondary Channel V.Fast			low priority		
092004/5	GDC-047	Katze/Kurdzo	Feeder mux Multiplexer	1/20/93	007,074	ISSUED	5,469,434	AUS CN, EPO
092006	GDC-048	Reymond	Transformed Current Sensing Relay	05/11/93	064,460	ISSUED	5,426,698	CN, EPO
091003	GDC-049	Reymond	Degenerative Bypass Capacitor			Hold as trade secret		
090006A	GDC-050A	Reymond	High Speed Synchronous Digital Data Bus System Having Underterminated	3/27/95	411,343		5,818,884	CN, EPO

PATENT

04/28/99

D #	CASE	INVENTOR	TITLE	DATE OF FILING	SERIAL#	STATUS	PATENT #	FOREIGN FILED
95003	GDC-066	Pattavina	Modified Phase Frequency Detector		Hold as trade secret			
95004	GDC-067	Nahas et al.	Standard Independent Multimedia Multipoint Server	10/2/97	08/942,675	Awaiting PTO reply		
95013	GDC-068	Nguyen	Multicast Delivery Tree Switching	7/10/97	08/891,176	Rejected on 1st Office Action		
95014	GDC-069	Semaan	Conferencing Environment for Reservation Requests	1/16/96	08/586,259	ISSUED	5,680,392	CN,EPO
95011	GDC-070	Chen et al.	User Programmable OAM Loopbacks and Physical Loopback Detection in an ATM Network	12/15/95	08/573,244	ISSUED	5,659,540	
95012	GDC-071	Chen et al.	Allow Access to ILMI Address Registration MIB From Multiple Links from SNMP Manager		Hold as trade secret			
95010	GDC-072	Rojas et al.	Fast Adaptive Routing for ATM	3/27/96	08/624,812	ISSUED	5,781,529	
95015	GDC-073	Rojas	Route Encapsulation and Recovery of Node Dependent Paths		Hold as Trade Secret			
95009	GDC-074	Wang	TFTP Architecture to Transfer non-file SNMPs MIB for ATM Billing System	2/5/97	08/796,079	Awaiting 1st Office Action		
95016	GDC-075	Chen et al.	Proxy Connection End-Points for ATM OAM Functions	7/5/96	08/675,955	ISSUED	5,864,555	
95007	GDC-076	Blain	Inband IP Packet Processing	5/31/96	08/658,842	Some claims allowable		
95017	GDC-077	Wells	Call Correlation Tags for Billing/Statistics	3/29/96	08/622,897	Allowed		
95017A	GDC-078	Jones	Data Network Switch with Fault Tolerance	10/7/97	08/930,973	Awaiting 1st Office Action		CN,EPO

PATENT

REEL: 9935 FRAME: 0162

04/28/99

<u>D #</u>	<u>CASE</u>	<u>INVENTOR</u>	<u>TITLE</u>	<u>DATE OF FILING</u>	<u>SERIAL#</u>	<u>STATUS</u>	<u>PATENT #</u>	<u>FOREIGN FILED</u>
D95017B	GDC-079	Jones	A Traffic Shaping ATM Network Switch	9/15/97	08/913,815	Awaiting 1st Office Action		CN, EPO
D95017C	GDC-080	Jones	ATM Switch Network	5/10/96	08/644,308		5,841,773	
D96001	GDC-081	Reymond	Synchronous Bus Fabric ATM Switch					
GDC-082		Jones	ATM Network Switch Congestion Control	12/4/97	08/973,441	Awaiting 1st Office Action		CN, EPO
GDC-083		Jones	Controlling the Flow of ATM Cells in an ATM Network	12/4/97	08/973,442	Awaiting 1st Office Action		CN, EPO
D96005	GDC-084	Worster	Method & Apparatus for Connection Admission Control of Variable Bit Rate Traffic in an ATM Switch	10/4/96 10/1/97	08/942,021	Awaiting 1st Office Action		
D96004	GDC-085	O'Neill	Service Specific Convergence Sublayer For Narrowband Packetization and Transport					CAN, EPO
D96004A	GDC-086	Pepper/ Clough	ATM Network Switch Having Enhanced Call Setup Capability	2/27/98	09/029,585			
D96004B	GDC-087	Jones	ATM Switch with VC Priority Buffers	3/5/98	09/029,295			CAN, EPO
D96004C	GDC-088	Jones	Controlled Available Bit Rate Service in an ATM Switch	5/19/98	09/077,219			CAN, EPO
D96002	GDC-089	Schlar et al.	Direct Telnat Protocol Over SPVCs					

PATENT

REEL: 9935 FRAME: 0163

SERIAL# STATUS PATENT #

INVENTOR TITLE

DATE OF
FILING

D # CASE

D #	CASE	INVENTOR	TITLE	DATE OF FILING	SERIAL#	STATUS	PATENT #	FOREIGN FILED
D96003	GDC-090	Lucas	Automatic Isolation of Network/Customer Premises Fault on a DSL Loop			Hold as trade secret		
D96006	GDC-091	Cumberton	High Speed Switching of Transport Circuits					
D96007	GDC-092	Kahoun	Panel Mount Feed Through DirectConnecting Power Block	2/5/97	08/796,080	Some claims allowable		
D96008	GDC-093/ GDC-094	St.Hilaire et. al St.Hilaire et. al	XMS Call Setup) Circuit Caching)	8/20/97	08/915,496	Allowed		
D96010	GDC-095	Rottow	Conference Reservation Acceptance Algorithm	6/16/97	08/877,463	Allowed		PCT
D96011	GDC-096	Tabet	Audio Automatic Gain Control Algorithm	7/7/97	08/888,571	Rejected 1st Office Action		
D96012	GDC-097	Bachmann Et al	Planned Capacity Based Routing for Wide Area ATM Systems					
D96013	GDC-098	Bachmann Et al	Load Balancing in ATM Networks					
D96014	GDC-099	Bachmann Et al	Priority in ATM Networks					
D96015	GDC-100	Reymond	Dual Complementary Crossflow Blower			Keep as trade secret		
D96012	GDC-102	Mathur, Falik, Hudson	Timing Recovery of Voice Sample Stream (Joint with Diamond & National Semiconductor)					
D97009	GDC-105	Cole	Shaping Filter For High Data Rate Signalling	3/12/97	60/040,432	Abandoned		
D97001	GDC-107	Reymond	Enhanced Etched PCB Inductor	5/9/97	08/853,813	Some claims allowable		

PATENT

REEL: 9935 FRAME: 0164

#	CASE	INVENTOR	TITLE	DATE OF FILING	SERIAL#	STATUS	PATENT #	FOREIGN FILED
7002	GDC-108	Kurdzo et al.	Voice Server Module for ATM Switch	1/29/98	09/015,303	Awaiting 1st Office Action		
7003	GDC-109	Kurdzo et al.	ATM Switch Voice Server Module Having DSP Array	1/29/98	09/015,302	Awaiting 1st Office Action		
7004	GDC-110	Kurdzo et al.	ATM Switch Voice Server Module Utilizing Flag Signalling	1/29/98	09/015,301			
7005	GDC-111	Steinbach Et 'al.	Synchronizing ATM AAL2 Minicell Generation to Multiple Voice Processing Functions					
7011	GDC-113	Poretsky	Precedence and Preemption in ATM Call Admission Control	5/9/97	08/853,654	Some claims allowable		
7021	GDC-116	Goldstein Et al.	PCM Equalizer	10/22/97	08/955,756	Some claims allowable		
7019	GDC-117	Worster Et Al.	Multicast Connection Label Allocator			On hold		
7023	GDC-118	Nichols et al.	Hot Insertable UTOPIA Interface	10/14/98	09/172,545	File		
7020	GDC-119	Evans et al.	Automatic Backup and Restoral for Single Point T1 Failures...	9/28/98	09/162,470			
8003	GDC-120	Chen et al.	SS7 Signaling Gateway Server for Voice Trunking over ATM			File		
7024	GDC-121	Claydon	Multi-tier 3D PCB Assembly			Hold as Trade Secret		
7022	GDC-122	Goldstein Et al.	Method of Signal Analysis to Determine Digital Network Impairments			Hold as Trade Secret		

PATENT

REEL: 9935 FRAME: 0165

04/28/99

FOREIGN
FILED

SERIAL# STATUS PATENT #

DATE OF
FILING

INVENTOR TITLE

D # CASE

98001	GDC-123	Sorrentino	Card Panel with Integral EMI Spring Track	6/18/98	09/099,790		
98004	GDC-124	Drucker et al.	Translation Table and Constellation Design for a PCM Modem...			File	
98006	GDC-126	C. Johnson Et al.	Voice Transmission Utilizing ATM Protocols				
98008	GDC-128	I. Hussain	A Dynamic Buffer Management for ATM Switches	7/22/98	60/093,681		
98009	GDC-129	I. Hussain	An Explicit Rate ABR Algorithm for New-Generation of ATM Switches	7/22/98	60/093,826		
99001	GDC-130	P. Cassella	Method for Mapping Narrowband DSO Circuits into AAL Type 2 SVCs	4/9/99	09/289,463	Awaiting PTO reply	
99002	GDC-131	P. Cassella	Derivation of AAL Type 2 SVC Characteristics Based Upon Signalled Narrowband Parameter	4/9/99	09/289,464	Awaiting PTO reply	
99003	GDC-132	P. Cassella Et al.	Distribution of Signalling in Narrowband to Broadband Interworking Switches			File	
99004	GDC-133	R. Novick	Traffic Shaping Mechanism for Packetized Data in a Digital Network			File	

PATENT

REEL: 9935 FRAME: 0166

GDC

COMPANY PRIVATE

File No. COUNTRY	Client Owner	Patent Title	Inventors	App # App Date	Pat # Pat Date
00150-0003-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC.	SYNCHRONOUS PROGRAMMABLE MIXED FORMAT TIME DIVISION MULTIPLEXER	CIECIERSKI, WALTER V. ET AL.	106,775 MARCH 03, 1971.	951,840 JULY 23, 1974
00150-0003-999 UNITED STATES	GENERAL DATACOMM INDUSTRIES, INC.	SYNCHRONOUS PROGRAMMABLE MIXED FORMAT TIME DIVISION MULTIPLEXER	CIECIERSKI, WALTER V. ET AL.	40,008 MAY 23, 1970	3,631,801 JANUARY 04, 1972
00150-0003-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC.	CLOSED LOOP TEST METHOD AND APPARATUS FOR DUPLEX DATA TRANSMISSION MODEM	DAVIS, S. I. ET AL.	106,773 MARCH 03, 1971	951,197 JULY 30, 1974
00150-0003-999 UNITED STATES	GENERAL DATACOMM INDUSTRIES, INC.	CLOSED LOOP TEST METHOD AND APPARATUS FOR DUPLEX DATA TRANSMISSION MODEM	DAVIS, STEVEN J. LIBESMAN, RICHARD A.	05267,390 JUNE 29, 1972	RE27,864 JANUARY 01, 1974
00150-0006-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC.	LOW HARMONIC DISTORTION FSK OSCILLATOR	DAVIS, S.	106,772 MARCH 03, 1971	979,242 JUNE 26, 1973
00150-0006-999 UNITED STATES	GENERAL DATACOMM INDUSTRIES, INC.	LOW HARMONIC DISTORTION FSK OSCILLATOR	DAVIS, S.	43,699 JUNE 05, 1970	3,621,470 NOVEMBER 16, 1971
00150-0007-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC.	PROGRAMMABLE ASYNCHRONOUS DATA BUFFER HAVING MEANS TO TRANSMIT ERROR PROTECTED CHANNEL CONTROL SIGNALS	CIECIERSKI, WALTER V. ET AL.	106,774 MARCH 03, 1971	971,282 JULY 15, 1975

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001504-0007-012 JAPAN	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM INDUSTRIES, INC.	PROGRAMMABLE ASYNCHRONOUS DATA BUFFER HAVING MEANS TO TRANSMIT ERROR PROTECTED CHANNEL CONTROL SIGNALS	RAYMOND, WELLES K. CIECIERSKI, WALTER V.	47-056499 JUNE 06, 1972	1159973 AUGUST 28, 1982
001504-0007-999 UNITED STATES	GENERAL DATACOMM INDUSTRIES, INC.	PROGRAMMABLE ASYNCHRONOUS DATA BUFFER HAVING MEANS TO TRANSMIT ERROR PROTECTED CHANNEL CONTROL SIGNALS	CIECIERSKI, WALTER V. ET AL.	40,006 MAY 25, 1970	3,883,645 JUNE 06, 1972
001504-0013-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC.	CLOSED DATA LOOP TEST METHOD AND APPARATUS FOR DATA TRANSMISSION MODEM	DAVIS, S. J.	143,580 MAY 31, 1972	931,331 JULY 16, 1974
001504-0013-999 UNITED STATES	GENERAL DATACOMM INDUSTRIES, INC.	CLOSED DATA LOOP TEST METHOD AND APPARATUS FOR DATA TRANSMISSION MODEM	DAVIS, S. J.	176,428 AUGUST 10, 1971	3,743,931 JULY 03, 1973
001504-0015-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM INDUSTRIES, INC.	WIDE BAND FREQUENCY DISCRIMINATOR	ALLINGER, WILLIAM B. DAVIS, STEVEN J.	150,040 AUGUST 23, 1972	971,040 OCTOBER 28, 1973
001504-0015-999 UNITED STATES	GENERAL DATACOMM INDUSTRIES, INC.	WIDE BAND FREQUENCY DISCRIMINATOR	ALLINGER, WILLIAM B. ET AL.	204,734 DECEMBER 07, 1971	3,764,927 OCTOBER 09, 1973
001504-0015-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM INDUSTRIES, INC.	METHOD AND APPARATUS FOR CONTROL SIGNALING FDM SYSTEM	COUTURIER, ROBERT A. DAVIS, STEVEN J. ROBERTS, HOWARD	169,141 APRIL 19, 1973	1,022,692 DECEMBER 13, 1977

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File No. COUNTRY	Client Owner	Patent Title	Inventors	App # App Date	Pat # Pat Date
001560-0016-999 UNITED STATES PATENTED	GENERAL DATA COMM INDUSTRIES, INC.	METHOD AND APPARATUS FOR CONTROL SIGNALING IN FIRM SYSTEM	COUTURE, ROBERT A. DAVIS, STEVEN J. ROBBINS, HOWARD	017246,519 APRIL 24, 1972	3,869,577 MARCH 04, 1975
001560-0018-999 UNITED STATES PATENTED	GENERAL DATA COMM INDUSTRIES, INC.	METHOD AND APPARATUS FOR CLOSED LOOP TESTING OF FIRST AND SECOND MODULATORS AND DEMODULATORS	COUTURE, ROBERT A. DAVIS, STEVEN J. ROBBINS, HOWARD	050648,990 MARCH 22, 1976	4,009,731 AUGUST 02, 1977
001560-0021-001 CANADA	GENERAL DATA COMM INDUSTRIES, INC.	METHOD AND APPARATUS FOR TESTING TELETYPEWRITER TERMINALS	SOLTYSIAK	143,591 JUNE 21, 1972	964,593 MARCH 18, 1975
001560-0021-003 GERMANY PATENTED	GENERAL DATA COMM INDUSTRIES, INC.	METHOD AND APPARATUS FOR TESTING TELETYPEWRITER TERMINALS	SOLTYSIAK INVENTOR NOT NAMED	P 224234,4 OCTOBER 02, 1972	2,548,234 OCTOBER 02, 1972
001560-0021-004 FRANCE	GENERAL DATA COMM INDUSTRIES, INC.	METHOD AND APPARATUS FOR TESTING TELETYPEWRITER TERMINALS	SOLTYSIAK	72,33035 SEPTEMBER 18, 1972	72,33035 SEPTEMBER 18, 1972
001560-0021-006 BELGIUM	GENERAL DATA COMM INDUSTRIES, INC.	METHOD AND APPARATUS FOR TESTING TELETYPEWRITER TERMINALS	SOLTYSIAK	122,128 SEPTEMBER 18, 1972	738,949 SEPTEMBER 18, 1972
001560-0021-012 JAPAN	GENERAL DATA COMM INDUSTRIES, INC.	METHOD AND APPARATUS FOR TESTING TELETYPEWRITER TERMINALS	SOLTYSIAK	104,581/72 OCTOBER 20, 1972	1,105,063 OCTOBER 21, 1981
001560-0022-001 CANADA	GENERAL DATA COMM INDUSTRIES, INC.	INDUCTOR-LESS TELEPHONE LINE HOLDING CIRCUIT GIVING HIGH A.C. SHORT IMPEDANCES	DAVIS, S. ET AL.	144,618 JUNE 14, 1972	964,153 JANUARY 14, 1975

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001568-0022-012 JAPAN	GENERAL DATACOMM INDUSTRIES, INC.	INDUCTOR-LESS TELEPHONE LINE HOLDING CIRCUIT GIVING HIGH A.C. SHUNT IMPEDANCES	DAVIS, S. ET AL.	90,517,72 SEPTEMBER 11, 1972	1,174,714 SEPTEMBER 08, 1982
001568-0023-999 UNITED STATES PATENTED	GENERAL DATACOMM INDUSTRIES, INC.	INDUCTOR-LESS TELEPHONE LINE HOLDING CIRCUIT GIVING HIGH A.C. SHUNT IMPEDANCES	DAVIS, STEVEN J. DOXTYAK, STEPHEN L.	05198,780 NOVEMBER 15, 1971	3,798,803 FEBRUARY 03, 1974
001568-0023-999 UNITED STATES PATENTED	GENERAL DATACOMM INDUSTRIES, INC.	VOLTAGE, CURRENT, OR POWER CONTROLLER UTILIZING A SWITCHED REACTANCE A.C. SHUNT REGULATOR	REYMOND, WELLES R.	05358,043 JUNE 11, 1973	2,944,909 MARCH 16, 1978
001568-0024-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM INDUSTRIES, INC.	METHOD AND APPARATUS FOR SIGNALING IN A COMMUNICATION SYSTEM	JOHNSON, CHARLES P. LUCAS, FREDRICK A.	294,610 JANUARY 09, 1978	1,082,787 JULY 29, 1980
001568-0024-999 UNITED STATES PATENTED	GENERAL DATACOMM INDUSTRIES, INC.	METHOD AND APPARATUS FOR SIGNALING IN A COMMUNICATION SYSTEM	JOHNSON, CHARLES P. LUCAS, FREDRICK A.	05780,666 JANUARY 19, 1977	4,095,043 JUNE 15, 1978
001568-0025-999 UNITED STATES PATENTED	GENERAL DATACOMM INDUSTRIES, INC.	SEQUENCE GENERATION BY READING FROM DIFFERENT MEMORIES AT DIFFERENT TIMES	JØRGENSEN, CHRISTIAN CARL	05780,103 MAY 26, 1977	4,122,309 OCTOBER 24, 1978
001568-0040-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM INDUSTRIES, INC.	AUTOMATIC FRAMING IN TIME DIVISION MULTIPLEXER	HANSTON, DEAN A. LAMBERT, DAVID A.	393,864 JANUARY 11, 1982	1,184,324 MARCH 19, 1985

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001580-0040-012 JAPAN	GENERAL DATACOMM INDUSTRIES, INC.	AUTOMATIC FRAMING IN TIME DIVISION MULTIPLEXER	HAMPTON, DEAN A. LAMBERT, DAVID A.	57-50748 JANUARY 11, 1982	170348 JANUARY 11, 1982
PATENTED	GENERAL DATACOMM INDUSTRIES, INC.				
001580-0040-014 AUSTRALIA	GENERAL DATACOMM INDUSTRIES, INC.	AUTOMATIC FRAMING IN TIME DIVISION MULTIPLEXER	LAMBERT, D. ET AL.	82-300134.0 JANUARY 12, 1982	008395 JANUARY 12, 1982
001580-0040-099 UNITED STATES	GENERAL DATACOMM INDUSTRIES, INC.	AUTOMATIC FRAMING IN TIME DIVISION MULTIPLEXER	LAMBERT, DAVID A. HAMPTON, DEAN A.	08024,339 JANUARY 12, 1981	4,400,993 JULY 17, 1984
PATENTED					
001580-0041-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC.	PROGRAMMABLE CLOCK RATE GENERATOR	BOND, WILLIAM C. PROFFET, GARY A.	393,873 JANUARY 11, 1982	1,180,060 DECEMBER 27, 1984
001580-0043-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC.	METHOD AND APPARATUS FOR ESTABLISHING FRAME SYNCHRONIZATION	HAMPTON, DEAN A. PROFFET, GARY A. JACOBSON, CHRISTIAN C.	398,887 JANUARY 11, 1982	1,134,325 MARCH 18, 1985
001580-0043-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC.	METHOD AND APPARATUS FOR DISTRIBUTING CONTROL SIGNALS	PROFFET, GARY A.	393,863 JANUARY 11, 1982	1,179,433 DECEMBER 11, 1984
001580-0043-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC.	METHOD AND APPARATUS FOR INTERLEAVING CONTROL SIGNALS	LAMBERT, DAVID A. PROFFET, GARY A.	393,874 JANUARY 11, 1982	1,179,432 DECEMBER 11, 1984

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001560-0046-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM INDUSTRIES, INC.	THREE STATE LOOP KEYS	JACOBSEN, CHRISTIAN C.	392,017 DECEMBER 10, 1981	1,177,024 NOVEMBER 13, 1984
001560-0054-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM INDUSTRIES, INC.	SECONDARY CHANNEL METHOD AND APPARATUS	GOLDBERG, HUGH M. DOMBROWSKI, LAWRENCE C.	441,648 DECEMBER 19, 1983	1,213,649 NOVEMBER 04, 1986
001560-0054-012 JAPAN	GENERAL DATACOMM INDUSTRIES, INC.	SECONDARY CHANNEL METHOD AND APPARATUS	GOLDBERG, H. ET AL.	500704/84 DECEMBER 21, 1983	DECEMBER 21, 2003
001560-0066-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM INDUSTRIES, INC.	ACTIVE TRANSDUCTANCE FILTER DEVICE	ROBINSON, JEFFREY L.	419,055 AUGUST 20, 1985	1,246,165 DECEMBER 06, 1988
001560-0066-999 UNITED STATES PATENTED	GENERAL DATACOMM INDUSTRIES, INC.	ACTIVE TRANSDUCTANCE FILTER DEVICE	ROBINSON, JEFFREY L.	07642,912 AUGUST 21, 1984	4,839,542 JUNE 13, 1989
001560-0079-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM INDUSTRIES, INC.	METHOD AND APPARATUS FOR CLOSED LOOP TESTING IN FDM SYSTEM	COLLIER, ROBERT A. DAVIS, STEVEN J. BOHNS, GEORGE	287,842 SEPTEMBER 30, 1977	1,096,276 AUGUST 01, 1978
001560-0089-999 UNITED STATES	GENERAL DATACOMM INDUSTRIES, INC.	METHOD AND APPARATUS FOR TESTING TELETYPEWRITER TERMINALS	LIBERMAN, R. A. ET AL.	194,270 NOVEMBER 01, 1971	3,769,454 OCTOBER 30, 1973

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001560-0091-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM INDUSTRIES, INC.	METHOD AND APPARATUS FOR DISTRIBUTING CONTROL SIGNALS	PROFF, GARY A.	467,728 NOVEMBER 13, 1994	1,200,893 JANUARY 28, 1996
001560-0109-001 CANADA PATENTED	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM INDUSTRIES, INC.	CIRCULAR FIRST-IN, FIRST OUT BUFFER SYSTEM FOR GENERATING INPUT AND OUTPUT ADDRESSES FOR READ/WRITE MEMORY INDEPENDENTLY	RASBERRY, JOSEPH D. NITSCHKE, KARL D.	511,711 JUNE 16, 1996	1,266,130 FEBRUARY 20, 1999 FEBRUARY 20, 2007
001560-0109-999 UNITED STATES PATENTED	GENERAL DATACOMM INDUSTRIES, INC.	CIRCULAR FIRST-IN, FIRST OUT BUFFER SYSTEM FOR GENERATING INPUT AND OUTPUT ADDRESSES FOR READ/WRITE MEMORY INDEPENDENTLY	RASBERRY, JOSEPH D. NITSCHKE, KARL D.	07747,095 JUNE 20, 1985	4,803,654 FEBRUARY 07, 1989
001560-0124-001 CANADA PATENTED	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM, INC.	INTELLIGENT SYNCHRONOUS MODEM AND COMMUNICATION SYSTEM	GORNIAK, ANDREW M. FARGANO, MICHAEL J.	522,784 NOVEMBER 12, 1986	1,271,563 JULY 10, 1990 JULY 10, 2007
001560-0124-002 UNITED KINGDOM PATENTED	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM INDUSTRIES, INC.	INTELLIGENT SYNCHRONOUS MODEM AND COMMUNICATION SYSTEM INCORPORATING THE SAME	GORNIAK, ANDREW M. FARGANO, MICHAEL J.	8306889.4 NOVEMBER 14, 1986	GBC028794 NOVEMBER 14, 1986 NOVEMBER 14, 2006
001560-0124-003 GERMANY PATENTED	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM INDUSTRIES, INC.	INTELLIGENT SYNCHRONOUS MODEM AND COMMUNICATION SYSTEM INCORPORATING THE SAME	GORNIAK, ANDREW M. FARGANO, MICHAEL J.	8306889.4 NOVEMBER 14, 1986	F3032878.3-08 NOVEMBER 14, 1986 NOVEMBER 14, 2006
001560-0124-004 FRANCE PATENTED	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM INDUSTRIES, INC.	INTELLIGENT SYNCHRONOUS MODEM AND COMMUNICATION SYSTEM INCORPORATING THE SAME	GORNIAK, ANDREW M. FARGANO, MICHAEL J.	8306889.4 NOVEMBER 14, 1986	F30328794 NOVEMBER 14, 1986 NOVEMBER 14, 2006

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00158-0124-217 BRIEF PATENT COPY.	GENERAL DATACOMM INDUSTRIES, INC.	INTELLIGENT SYNCHRONOUS MODEM AND COMMUNICATION SYSTEM INCORPORATING THE SAME	GORNIAK, ANDREW M. PARGANO, MICHAEL J.	8308897.4 NOVEMBER 14, 1986	0224794 NOVEMBER 14, 1986 NOVEMBER 14, 2006
COMPLETED	GENERAL DATACOMM INDUSTRIES, INC.				
00158-0125-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC.	ANALOG-DIGITAL CONVERTER	ROBINSON, JEFFREY L.	528,134 JANUARY 26, 1987	1,332,514 AUGUST 06, 1996 AUGUST 04, 2013
PATENTED	GENERAL DATACOMM, INC.				
00158-0135-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC.	BIT INTERLEAVED MULTIPLEXER SYSTEM PROVIDING BYTE SYNCHRONIZATION FOR COMMUNICATING APPARATUS	MANNING, DAVID J.	539,715 JUNE 15, 1987	1,261,271 JANUARY 30, 1990 JANUARY 30, 2007
PATENTED	GENERAL DATACOMM, INC.				
00158-0135-002 UNITED KINGDOM	GENERAL DATACOMM INDUSTRIES, INC.	BIT INTERLEAVED MULTIPLEXER SYSTEM PROVIDING BYTE SYNCHRONIZATION FOR COMMUNICATING APPARATUS	MANNING, DAVID J.	8730427.4 JUNE 18, 1987	GB0251588 JUNE 18, 1987 JUNE 18, 2007
PATENTED	GENERAL DATACOMM, INC.				
00158-0135-217 BRIEF PATENT COPY.	GENERAL DATACOMM INDUSTRIES, INC.	BIT INTERLEAVED MULTIPLEXER SYSTEM PROVIDING BYTE SYNCHRONIZATION FOR COMMUNICATING APPARATUS	MANNING, DAVID J.	8730427.4 JUNE 18, 1987	0051388 JUNE 18, 1987 JUNE 18, 2007
COMPLETED	GENERAL DATACOMM, INC.				
00158-0135-001 CANADA	GENERAL DATACOMM INDUSTRIES, INC.	VARIABLE CONTROL AND DATA RATES IN HIGHLY EFFICIENT MULTIPLEXER	MANNING, DAVID J. REEVES, JONATHAN M.	539,714 JUNE 15, 1987	1,265,270 JANUARY 30, 1990 JANUARY 30, 2007
PATENTED	GENERAL DATACOMM, INC.				

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File No. COUNTRY	Client Owner	Patent Title	Inventors	App # App Date	Pat # Pat Date
001540-0136-002 UNITED KINGDOM PATENTED	GENERAL DATA COMM. INDUSTRIES, INC. GENERAL DATA COMM. INC.	VARIABLE CONTROL AND DATA RATES IN HIGHLY EFFICIENT MULTIPLEXER	MANNING, DAVID J. REEVES, JONATHAN M.	57306476.6 JUNE 18, 1987	GB0001587 JUNE 18, 1987 JUNE 18, 2007
001540-0136-227 EUROPEAN PATENT CONY. COMPLETED	GENERAL DATA COMM INDUSTRIES, INC. GENERAL DATA COMM. INC.	VARIABLE CONTROL AND DATA RATES IN HIGHLY EFFICIENT MULTIPLEXER	MANNING, DAVID J. REEVES, JONATHAN M.	47305426.6 JUNE 18, 1987	0241587 JUNE 18, 1987 JUNE 18, 2007
001540-0137-001 CANADA PATENTED	GENERAL DATA COMM INDUSTRIES, INC. CHRISTIAN C. JACOBSEN	HIGH SPEED BIT INTERLEAVED TIME DIVISION MULTIPLEXER FOR MULTINODE COMMUNICATION SYSTEMS	JACOBSEN, CHRISTIAN C.	5730925 JUNE 17, 1987	1,264,534 MARCH 06, 1990 MARCH 06, 2007
001540-0137-002 UNITED KINGDOM PATENTED	GENERAL DATA COMM INDUSTRIES, INC. GENERAL DATA COMM. INC.	HIGH SPEED BIT INTERLEAVED TIME DIVISION MULTIPLEXER FOR MULTINODE COMMUNICATION SYSTEMS	JACOBSEN, CHRISTIAN C.	57306160.0 JULY 13, 1987	GB0255253 JULY 13, 1987 JULY 13, 2007
001540-0137-227 EUROPEAN PATENT CONY. COMPLETED	GENERAL DATA COMM INDUSTRIES, INC. GENERAL DATA COMM. INC.	HIGH SPEED BIT INTERLEAVED TIME DIVISION MULTIPLEXER FOR MULTINODE COMMUNICATION SYSTEMS	JACOBSEN, CHRISTIAN C.	57306160.0 JULY 13, 1987	0255253 JULY 13, 1987 JULY 13, 2007
001540-0151-001 CANADA PATENTED	GENERAL DATA COMM INDUSTRIES, INC. GENERAL DATA COMM. INC.	SYSTEM AND APPARATUS FOR PROVIDING THREE DIMENSIONS OF INPUT TO A HOST PROCESSOR	CORNIAK, ANDREW M.	565,378 APRIL 29, 1988	1,324,424 NOVEMBER 16, 1993 NOVEMBER 16, 2010
001540-0151-002 UNITED KINGDOM PATENTED	GENERAL DATA COMM INDUSTRIES, INC. GENERAL DATA COMM. INC.	SYSTEM AND APPARATUS FOR PROVIDING THREE DIMENSIONS OF INPUT TO A HOST PROCESSOR	CORNIAK, ANDREW M.	38004817.3 APRIL 29, 1988	GB0602253 APRIL 29, 1988 APRIL 29, 2008

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001560-0152-003 GERMANY PATENTED	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM, INC.	SYSTEM AND APPARATUS FOR PROVIDING THREE DIMENSIONS OF INPUT TO A HOST PROCESSOR	GORNIAK, ANDREW M.	88044815.3 APRIL 29, 1988	P3852370.5-48 APRIL 29, 1988 APRIL 29, 2008
001560-0152-004 FRANCE PATENTED	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM, INC.	SYSTEM AND APPARATUS FOR PROVIDING THREE DIMENSIONS OF INPUT TO A HOST PROCESSOR	GORNIAK, ANDREW M.	88044815.3 APRIL 29, 1988	FR0362255 APRIL 29, 1988 APRIL 29, 2008
001560-0152-012 JAPAN PENDING	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM, INC.	SYSTEM AND APPARATUS FOR PROVIDING THREE DIMENSIONS OF INPUT TO A HOST PROCESSOR	GORNIAK, ANDREW M.	63-504375 APRIL 29, 1988	APRIL 29, 2008
001560-0152-227 EUROPEAN PATENT CONV. COMPLETED	GENERAL DATACOMM INDUSTRIES, INC. GENERAL DATACOMM, INC.	SYSTEM AND APPARATUS FOR PROVIDING THREE DIMENSIONS OF INPUT TO A HOST PROCESSOR	GORNIAK, ANDREW M.	28004815.3 APRIL 29, 1988	0957253 APRIL 29, 1988 APRIL 29, 2008
001560-0152-001 CANADA PATENTED	GENERAL DATACOMM INDUSTRIES, INC. ROBERT DUGGAN	MULTIPORT/MULTIDROP COMPUTER COMMUNICATIONS	DUGGAN, ROBERT	567,156 MAY 18, 1988	1,370,288 MAY 05, 1992 MAY 05, 2009
001560-0152-002 UNITED KINGDOM PATENTED	GENERAL DATACOMM INDUSTRIES, INC. ROBERT DUGGAN	MULTIPORT/MULTIDROP COMPUTER COMMUNICATIONS	DUGGAN, ROBERT	88304437.2 MAY 17, 1988	GB0292226 MAY 17, 1988 MAY 17, 2008
001560-0152-217 EUROPEAN PATENT CONV. COMPLETED	GENERAL DATACOMM INDUSTRIES, INC. ROBERT DUGGAN	MULTIPORT/MULTIDROP COMPUTER COMMUNICATIONS	DUGGAN, ROBERT	88304437.2 MAY 17, 1988	0292226 MAY 17, 1988 MAY 17, 2008

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001560-0162-001 UNITED KINGDOM PATENTED	GENERAL DATA COMM INDUSTRIES, INC. INC.	APPARATUS AND METHOD FOR ADAPTIVELY OPTIMIZING EQUALIZATION DELAY OF DATA COMMUNICATION EQUIPMENT	POTTER, WILLIAM J.	89902316.2 JANUARY 13, 1989	GB9401246 JANUARY 13, 1989 JANUARY 13, 2009
001560-0162-001 EUROPEAN PATENT CONY. CONFLICTED	GENERAL DATA COMM INDUSTRIES, INC. INC.	APPARATUS AND METHOD FOR ADAPTIVELY OPTIMIZING EQUALIZATION DELAY OF DATA COMMUNICATION EQUIPMENT	POTTER, WILLIAM J.	89902316.2 JANUARY 13, 1989	0401246 JANUARY 13, 1989 JANUARY 13, 2009
001560-0163-001 CANADA PATENTED	GENERAL DATA COMM INDUSTRIES, INC. INC.	BRANCH METRIC ALGORITHM FOR EIGHT-DIMENSIONAL SIGNAL CONSTELLATION	GOLDSTEIN, YURI ABDELSAYED, WILLIAM R. COLE, PAUL D.	395,761 APRIL 04, 1989	1,335,170 APRIL 04, 1995 APRIL 04, 2012

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