

6-23-99

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COVER SHEET

101087380

To: Honorable Commissioner of Patents and Trademarks:

Please record the attached original document or copy thereof.

1. Name of conveying parties:

- a) Rao V. Annapragada
- b) Milind G. Weling

2. Name and address of receiving party:

- a) Name: VLSI Technology, Inc.
- b) Address: 1109 McKay Drive, San Jose, CA 95131

3. Nature of conveyance

- | | | | |
|-------------------------------------|--------------------|--------------------------|-------------------|
| <input checked="" type="checkbox"/> | Assignment | <input type="checkbox"/> | Merger |
| <input type="checkbox"/> | Security Agreement | <input type="checkbox"/> | Change of Name |
| <input type="checkbox"/> | Other _____ | <input type="checkbox"/> | License Agreement |

Execution Dates: June 22, 1999 and June 22, 1999, respectively.

4. Application Number or Patent Number: Not yet assigned

The title of the new application is: SEMICONDUCTOR DIELECTRIC STRUCTURE AND METHOD FOR MAKING THE SAME

5. Please send all correspondence concerning this document to:

Tobi C. Clinton
 MARTINE PENILLA & KIM, LLP
 830 West Evelyn Avenue
 Sunnyvale, CA 94086
 Tel. No. (408) 749-6900
 Fax No.: (408) 749-6901

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6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41): \$40.00

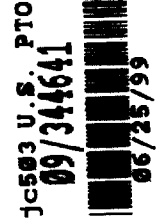
- Enclosed
- Authorized to be charged to Deposit Account No. 50-0805 (Order No. VTIIP314)

8. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Date: June 25, 1999



 Tobi C. Clinton
 Registration No. 43,553



ASSIGNMENT OF PATENT APPLICATION

(Accompanying Application)

Whereas we the undersigned inventors have invented certain new and useful improvements as set forth in the patent application entitled:

SEMICONDUCTOR DIELECTRIC STRUCTURE AND METHOD FOR MAKING THE SAME

(Atty. Docket No: VTI1P314) for which we have executed an application for a United States Letters Patent.

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, We the undersigned inventors hereby:

- 1) Sell, assign and transfer to **VLSI Technology, Inc.**, a Delaware corporation having a place of business at 1109 McKay Drive, San Jose, CA 95131, (hereinafter referred to as "ASSIGNEE"), the entire right title and interest in any and all improvements and inventions disclosed in, application based upon, and Patent (including foreign patents) granted upon the information which is disclosed in the above referenced application.
- 2) Authorize and request the Commissioner of Patents to issue any and all Letters Patents resulting from said application or any division, continuation, substitute or reissue thereof to the ASSIGNEE.
- 3) Agree to execute all papers and documents and, entirely at the ASSIGNEE's expense, perform any acts which are reasonably necessary in connection with the prosecution of said application, as well as any derivative and applications thereof, foreign applications based thereon, and/or the enforcement of patents resulting from such applications.
- 4) Agree that the terms, covenants and conditions of this assignment shall inure to the benefit of the Assignee, its successors, assigns and other legal representative, and shall be binding upon the inventors, as well as the inventor's heirs, legal representatives and assigns.
- 5) Warrant and represent that we have not entered, and will not enter into any assignment, contract, or understanding that conflicts with this assignment.

Signed on the dates indicated beside our signatures.

- 1) Signature: Rao Annapragada Date: 6/22/99
Typed Name: Rao V. Annapragada
- 2) Signature: Milind Weling Date: 6/22/99
Typed Name: Milind G. Weling

Attorney Docket No. VTI1P314

RECORDED: 06/23/1999

PATENT
REEL: 010076 FRAME: 0422