FORM PTO-1594 1-31-92

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4. Application number(s) or patent number(s): If this document is being filed together with a new application, the execution date of the application: <u>July 2, 1999</u>				
A.	Patent Application Number(s):	B. Patent Number(s):		
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5.	Name and address of party to whom correspondence concerning document should be mailed:	6. Total number of applications and registrations involved:		
Name:	Mr. Ernest F. Chapman	7. Total fee (37 CFR 3.41): \$40 ∖		
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> **PATENT REEL: 010094 FRAME: 0848**

(99S0394)

In consideration of value received, I, having a residence as stated below above my name, the sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are listed below) of an invention described in an application for United States patent entitled:

SEMICONDUCTOR DEVICE AND LSI DEFECT ANALYZING METHOD USING THE SAME

sell and assign to

KABUSHIKI KAISHA TOSHIBA, a Japanese corporation, having a place of business at 72 Horikawa-cho, Saiwai-ku, Kawasaki-shi, Japan

its successors, and assigns or nominees, hereinafter referred to as "Assignee", my entire right, title and interest in and to said invention as disclosed, shown and described in said United States patent application:

(check one)	[X] executed concurrently herewith;		
	[] executed on	;	
	[] Application No.	, filed	;

and in and to all applications for patent and patents for invention, in all countries of the world, including all divisions, reissues, continuations, substitutes and extensions thereof and all rights arising under or pursuant to any and all international agreements, treaties or laws relating to the protection of industrial property, including rights of priority, resulting from the filing of any of said applications; and I authorize and request any official whose duty is to issue patents, to issue any patent on said invention or resulting therefrom to said Assignee, and I agree that on request and without further consideration, but at the expense of said Assignee, I will communicate to said Assignee or its representatives all facts known to me respecting said invention and testify in any legal proceedings, sign all lawful papers, execute all divisional, continuing, reissue, or other applications, make all rightful oaths and declarations, and generally do everything possible to aid said Assignee to obtain and enforce proper patent protection for said invention in all countries.

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Finnegan, Henderson, Farabow, Garrett & Dunner L.L.P., 1300 I Street, N.W., Washington, D.C. 20005-3315, U. S. A.

[Inventor]

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Starn Tamura

Itaru Tamura

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