

10-08-1999

Patent and Trademark Office



101165609

To the Honorable Commissioner of Patents and Trademarks. Ple.

1. Name of conveying party(ies):

Graham Kirsch
Simon Kershaw

Additional name(s) of conveying party(ies) attached ☐ Yes ☒ No

2. Name and address of receiving party(ies):

Name: LSI Logic Corporation

Internal Address: _____

Street Address: 1551 McCarthy Boulevard

City Milpitas State CA ZIP 95035

Additional name(s) & address(es) attached? ☐ Yes ☒ No

3. Nature of Conveyance:

- ☒ Assignment
☐ Merger
☐ Security Agreement
☐ Change of Name
☐ Other _____

Execution Date: September 25, 1999

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of this application is:

A. Patent Application No.(s)

09/262,816

B. Patent No.(s)

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: B. Noel Kivlin

Internal Address: Conley, Rose & Tayon, P.C.

Street Address: P.O. Box 398

City Austin State TX ZIP 78767-0398

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41):\$ 40.00

- ☒ Enclosed
☐ Authorized to be charged to deposit account

8. Deposit account number:

(Attach a duplicate copy of this page if paying by deposit account)

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9. Statement and Signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

B. Noel Kivlin

Name of Person Signing
Reg. No. 34,876

[Signature]
Signature

10-1-99

Date

Total number of pages: 3

Docket No.: P-3577

PATENT

ASSIGNMENT

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I/WE, the undersigned, , do hereby sell, assign, and transfer to: LSI LOGIC CORPORATION, a corporation of Delaware, having a place of business at 1551 McCarthy Boulevard, Milpitas, California 95035, ("Assignee"), its successors, assigns, and legal representatives, the entire right, title and interest for the United States and all foreign countries, in and to any and all inventions and improvements which are disclosed in the application for United States Letters Patent,

- () which has been executed by the undersigned concurrently herewith,
(X) which was filed on March 5 19 99 and assigned Serial No. 09/262,816 .

and is entitled: "MICROPROCESSOR DEBUGGING" and in and to said application and all divisional, continuing, substitute, renewal, reissue, and all other applications for Letters Patent which have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original and reissued patents which have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application; and

Agree that said Assignee may apply for and receive Letters Patent for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney and other papers; communicate to said Assignee, its successors, assigns, and representatives, all facts known to the undersigned relating to said improvements and the history thereof; and generally do everything possible which said Assignee, its successors, assigns or representatives shall consider desirable for aiding in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements and all applications to patents and all patents on said improvements, in said Assignee, its successors, assigns and legal representatives; and

Covenant with said Assignee, its successors, assigns and legal representatives that no assignment, grant mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

| | | |
|---|--------------------------|--|
| Name: _____ Graham Kirsch | Date: _____, 1999 | Witness: _____ _____ Printed |
| Name: <u>SM Kershaw</u> Simon Martin Kershaw | Date: <u>9/11</u> , 1999 | Witness: <u>[Signature]</u> _____ Brendon Slade Printed |

Docket No.: P-3577

PATENT

ASSIGNMENT

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I/WE, the undersigned, , do hereby sell, assign, and transfer to: LSI LOGIC CORPORATION, a corporation of Delaware, having a place of business at 1551 McCarthy Boulevard, Milpitas, California 95035, ("Assignee"), its successors, assigns, and legal representatives, the entire right, title and interest for the United States and all foreign countries, in and to any and all inventions and improvements which are disclosed in the application for United States Letters Patent,

() which has been executed by the undersigned concurrently herewith,
 (X) which was filed on March 5 19 99 and assigned Serial No. 09/262,816 .

and is entitled: "MICROPROCESSOR DEBUGGING" and in and to said application and all divisional, continuing, substitute, renewal, reissue, and all other applications for Letters Patent which have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original and reissued patents which have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application; and

Agree that said Assignee may apply for and receive Letters Patent for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney and other papers; communicate to said Assignee, its successors, assigns, and representatives, all facts known to the undersigned relating to said improvements and the history thereof; and generally do everything possible which said Assignee, its successors, assigns or representatives shall consider desirable for aiding in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements and all applications to patents and all patents on said improvements, in said Assignee, its successors, assigns and legal representatives; and

Covenant with said Assignee, its successors, assigns and legal representatives that no assignment, grant mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Name: G. Kirsch Date: 25 Feb, 1999 Witness: XPS
 Graham Kirsch
 Witness: A.W.I. BURMAN
 Printed
 Name: _____ Date: _____, 1999 Witness: _____
 Simon Martin Kershaw
 Witness: _____
 Printed

Assignment

RECORDED: 10/05/1999

PATENT
REEL: 010284 FRAME: 0672