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11-29-1999



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ER SHEET

JCS42 U.S. PTO
09/439021
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To: Honorable Commissioner of Patents and Trademarks:

Please record the attached original document(s) or copy thereof.

1. Name of conveying party(ies)
a) Rao V. Annapragada
2. Name and address of receiving party(ies):
a) Name: VLSI Technology, Inc.
Address: 1109 McKay Drive
San Jose, CA 95131

3. Nature of conveyance

<input checked="" type="checkbox"/>	Assignment	<input type="checkbox"/>	Merger
<input type="checkbox"/>	Security Agreement	<input type="checkbox"/>	Change of Name
<input type="checkbox"/>	Other _____	<input type="checkbox"/>	License Agreement

Execution Date: October 13, 1998

4. Application Number(s) or Patent Number(s): Not yet assigned

The title of the (new) application is:

PROCESS TO IMPROVE ADHESION OF CAP LAYERS IN INTEGRATED CIRCUITS

5. Please send all correspondence concerning this (these) documents to:

HICKMAN STEPHENS & COLEMAN, LLP
P.O. Box 52037
Palo Alto, CA 94303-0746
Tel. No.: (650) 470-7430
Fax No.: (650) 470-7440

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41): \$40.00

- | | |
|-------------------------------------|--|
| <input checked="" type="checkbox"/> | Enclosed |
| <input type="checkbox"/> | Authorized to be charged to Deposit Account No. 50-0384
(Order No. <u>VTI1P202A</u>) |

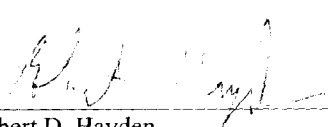
8. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Date: November 11, 1999

11/24/1999 DTHOMAS 00000052 09439021

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40.00 DP


Robert D. Hayden
Registration No. 42,645

Attorney Docket No. VTI1P202A

(Revised 01/96)

PATENT
REEL: 010394 FRAME: 0228

ASSIGNMENT OF PATENT APPLICATION

(Not Accompanying Application)

Whereas I the undersigned inventor(s) have invented certain new and useful improvements as set forth in the patent application entitled:

PROCESS TO IMPROVE ADHESION OF CAP LAYERS IN INTEGRATED CIRCUITS

for which I have executed an application for a United States Letters Patent which was filed in the U.S. Patent and Trademark Office on July 22, 1998, and which bears the Application No. 09/120,895.

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I the undersigned inventor(s) hereby:

1) Sell(s), assign(s) and transfer(s) to VLSI Technology, Inc., a Delaware corporation having a place of business at 1109 McKay Drive, San Jose, California 95131, (hereinafter referred to as ("ASSIGNEE")), the entire right title and interest in any and all improvements and inventions disclosed in, application(s) based upon, and Patent(s) (including foreign patents) granted upon the information which is disclosed in the above referenced application.

2) Authorize and request the Commissioner of Patents to issue any and all Letters Patents resulting from said application or any division(s), continuation(s), substitutes(s) or reissue(s) thereof to the ASSIGNEE.

3) Agree to execute all papers and documents and, entirely at the ASSIGNEE's expense, perform any acts which are reasonably necessary in connection with the prosecution of said application, as well as any derivative and applications thereof, foreign applications based thereon, and/or the enforcement of patents resulting from such applications.

4) Agree that the terms, covenants and conditions of this assignment shall inure to the benefit of the Assignee, its successors, assigns and other legal representative, and shall be binding upon the inventor(s), as well as the inventor's heirs, legal representatives and assigns.

5) Warrant and represent that I have not entered, and will not enter into any assignment, contract, or understanding that conflicts with this assignment.

Signed on the date indicated beside my signature.

1) Signature: Rao Annapragada Date: 10/13/98
Typed Name: Rao V. Annapragada