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ASSIGN



SHEET

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To: Honorable Commissioner of Patents and Trademarks:

Please record the attached original document or copy thereof.

1. Name of conveying parties:
  - a) Milind Weling
  - b) Subhas Bothra
  - c) Calvin Todd Gabriel
  - d) Michael Misheloff
2. Name and address of receiving party:
  - a) Name: VLSI Technology, Inc.
  - b) Address: Philips Silicon Valley Center, 1000 West Maude Ave.,  
Sunnyvale, CA 94086-2810
3. Nature of conveyance

<input checked="" type="checkbox"/>	Assignment	<input type="checkbox"/>	Merger
<input type="checkbox"/>	Security Agreement	<input type="checkbox"/>	Change of Name
<input type="checkbox"/>	Other _____	<input type="checkbox"/>	License Agreement

Execution Dates: December 10, 1999, respectively.

4. Application Number or Patent Number: 09/429,540 filed October 28, 1999

The title of the new application is: METHODS FOR FORMING CO-AXIAL INTERCONNECT LINES IN A CMOS PROCESS FOR HIGH SPEED APPLICATIONS

5. Please send all correspondence concerning this document to:

**Albert S. Penilla**  
MARTINE PENILLA & KIM, LLP  
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Sunnyvale, CA 94086  
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40.00 DP

6. Total number of applications and patents involved: 1
7. Total fee (37 CFR 3.41): \$40.00

<input checked="" type="checkbox"/>	Enclosed
<input checked="" type="checkbox"/>	Authorized to be charged to Deposit Account No. 50-0805 (Order No. <u>VTI1P321</u> )
8. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Date: December 14, 1999

  
Albert S. Penilla, Esq.  
Registration No. 39,487

Attorney Docket No. VTI1P321

(Revised 01/96)

**PATENT**  
**REEL: 010448 FRAME: 0800**

# ASSIGNMENT OF PATENT APPLICATION

(Not Accompanying Application)

Whereas we the undersigned inventors have invented certain new and useful improvements as set forth in the patent application entitled:

## METHODS FOR PERFORMING CO-AXIAL INTERCONNECT LINES IN A CMOS PROCESS FOR HIGH SPEED APPLICATIONS

(Atty. Docket No.: VTIIP321) for which we have executed an application for a United States Letters Patent which was filed in the U.S. Patent and Trademark Office on October 28, 1999, and which bears the Application No. 09/429,540.

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, We the undersigned inventors hereby:

- 1) Sell, assign and transfer to **VLSI Technology, Inc.**, a Delaware corporation having a place of business at Philips Silicon Valley Center, 1000 West Maude Ave., Sunnyvale, CA 94086-2810, (hereinafter referred to as "ASSIGNEE"), the entire right title and interest in any and all improvements and inventions disclosed in, applications based upon, and Patents (including foreign patents) granted upon the information which is disclosed in the above referenced application.
- 2) Authorize and request the Commissioner of Patents to issue any and all Letters Patents resulting from said application or any divisions, continuations, substitutes or reissues thereof to the ASSIGNEE.
- 3) Agree to execute all papers and documents and, entirely at the ASSIGNEE's expense, perform any acts which are reasonably necessary in connection with the prosecution of said application, as well as any derivative and applications thereof, foreign applications based thereon, and/or the enforcement of patents resulting from such applications.
- 4) Agree that the terms, covenants and conditions of this assignment shall inure to the benefit of the Assignee, its successors, assigns and other legal representative, and shall be binding upon the inventors, as well as the inventor's heirs, legal representatives and assigns.
- 5) Warrant and represent that we have not entered, and will not enter into any assignment, contract, or understanding that conflicts with this assignment.

Signed on the dates indicated beside our signatures.

- |    |  |                               |
|----|--|-------------------------------|
| 1) | Signature: <u>Milind Weling</u><br>Typed Name: Milind Weling             | Date: <u>December 10 1999</u> |
| 2) | Signature: <u>Subhas Bothra</u><br>Typed Name: Subhas Bothra             | Date: <u>12/10/99</u>         |
| 3) | Signature: <u>Calvin Todd Gabriel</u><br>Typed Name: Calvin Todd Gabriel | Date: <u>10 December 1999</u> |
| 3) | Signature: <u>Michael Misheloff</u><br>Typed Name: Michael Misheloff     | Date: <u>12/10/99</u>         |