

01-04-2000

Form PTO-1595  
6-93



U.S. Department of Commerce  
Patent and Trademark Office

*MKD 12.14.99*

101236267

To the Honorable Commissioner of Patents and Trademarks. Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):  
 (1) Alex Chalfin  
 (2) Jeffrey Daudel  
 (3) Mark Grossman  
 (4) Shrijcet Mukherjee  
 (5) Peter Ostrin  
 (6) Jarrett Redd

Additional name(s) of conveying party(ies) attached?  yes  no

2. Name and address of receiving party(ies):  
 Name: Silicon Graphics, Inc.  
 Street Address: 1600 Amphitheatre Parkway  
 City: Mountain View State: CA Zip Code: 94043  
 Country: USA

3. Nature of Conveyance:  
 Assignment  Merger  
 Security Agreement  Change of Name  
 Other \_\_\_\_\_

Execution Dates: (1) December 3, 1999, (2) December 3, 1999, (3) November 29, 1999, (4) November 28, 1999, (5) November 29, 1999, and (6) November 29, 1999.

Additional name(s) & address(es) attached?  yes  no

*09/455995*

4. Application number(s) or patent number(s):

A. Patent Application No(s).

B. Patent No(s).

Additional numbers attached?  yes  no

5. Name and address of party to whom correspondence concerning document should be mailed:  
 Name: **Sterne, Kessler, Goldstein & Fox P.L.L.C.**  
 Internal Address: c/o Michael Messinger  
 Street Address: 1100 New York Ave., N.W. Suite 600  
 City: Washington State: D.C. Zip Code: 20005-3934

6. Total number of applications and patents involved  
1

7. Total fee (37 C.F.R. § 3.41).....\$ 40.00  
 Enclosed  
 Authorized to be charged to Deposit Account

8. Deposit Account Number: 19-0036

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9. Statement and signature.  
 12/30/1999 KHARLE... 00000019 09459995  
*To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.*

Michael V. Messinger  
 Name of Person Signing  
 Registration No. 37,575

*Michael Messinger*  
 Signature

12/14/99  
 Date

Total number of pages including cover sheet, attachments and document 6

OMB NO. 0651-0011 (exp.4/94)

Mail documents to be recorded with required cover sheet information to:  
 Commissioner of Patents and Trademarks, Box Assignments  
 Washington, D.C. 20231

Jc675 U.S. PTO  
 09/455995  
 12/14/99

# ASSIGNMENT

In consideration of the sum of One Dollar (\$1.00) or equivalent and other good and valuable consideration paid to each of the undersigned inventor(s): (1) Alex Chalfin, (2) Jeffrey Daudel, (3) Mark Grossman, (4) Shrijeet Mukherjee, (5) Peter Ostrin, and (6) Jarrett Redd, the undersigned inventor(s) hereby sell(s) and assign(s) to Silicon Graphics, Inc. (the Assignee) his/her entire right, title and interest, including the right to sue for past infringement and to collect for all past, present and future damages:

check applicable box(es)  for the United States of America (as defined in 35 U.S.C. § 100),  
 and throughout the world,

(a) in the invention(s) known as Synchronization of Hardware Simulation Processes for which application(s) for patent in the United States of America has (have) been executed by the undersigned on (1) 11/29/99, (2) \_\_\_\_\_, (3) 11/29/99, (4) 11/28/99, (5) 11/29/99, and (6) \_\_\_\_\_ (also known as United States Application No. (to be determined), filed Herewith), in any and all applications thereon, in any and all Letters Patent(s) therefor, and

(b) in any and all applications that claim the benefit of the patent application listed above in part (a), including continuing applications, reissues, extensions, renewals and reexaminations of the patent application or Letters Patent therefor listed above in part (a), to the full extent of the term or terms for which Letters Patents issue, and

(c) in any and all inventions described in the patent application listed above in part (a), and in any and all forms of intellectual and industrial property protection derivable from such patent application, and that are derivable from any and all continuing applications, reissues, extensions, renewals and reexaminations of such patent application, including, without limitation, patents, applications, utility models, inventor's certificates, and designs together with the right to file applications therefor; and including the right to claim the same priority rights from any previously filed applications under the International Agreement for the Protection of Industrial Property, or any other international agreement, or the domestic laws of the country in which any such application is filed, as may be applicable;

all such rights, title and interest to be held and enjoyed by the above-named Assignee, its successors, legal representatives and assigns to the same extent as all such rights, title and interest would have been held and enjoyed by the Assignor had this assignment and sale not been made.

The undersigned inventor(s) agree(s) to execute all papers necessary in connection with the application(s) and any continuing (continuation, divisional, or continuation-in-part), reissue, reexamination or corresponding application(s) thereof and also to execute separate assignments in connection with such application(s) as the Assignee may deem necessary or expedient.

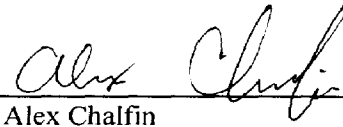
The undersigned inventor(s) agree(s) to execute all papers necessary in connection with any interference or patent enforcement action (judicial or otherwise) related to the application(s) or any continuing (continuation, divisional, or continuation-in-part), reissue or reexamination application(s) thereof and to cooperate with the Assignee in every way possible in obtaining evidence and going forward with such interference or patent enforcement action.

The undersigned inventor(s) hereby represent(s) that he/she has full right to convey the entire interest herein assigned, and that he/she has not executed, and will not execute, any agreement in conflict therewith.

The undersigned inventor hereby grant(s) Douglas J. Crisman, Esquire, Registration No. 39,951, John Brigden, Esquire, Registration No. 40,530 of SILICON GRAPHICS, INC., 1600 Amphitheatre Parkway, Mail Stop 6L-710, Mountain View, CA 94043-1351; Robert Greene Sterne, Esquire, Registration No. 28,912; Edward J. Kessler, Esquire, Registration No. 25,688; Jorge A. Goldstein, Esquire, Registration No. 29,021; Samuel L. Fox,

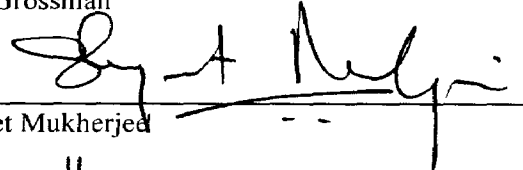
Esquire, Registration No. 30,353; David K.S. Cornwell, Esquire, Registration No. 31,944; Robert W. Esmond, Esquire, Registration No. 32,893; Tracy-Gene G. Durkin, Esquire, Registration No. 32,831; Michele A. Cimbala, Esquire, Registration No. 33,851; Michael B. Ray, Esquire, Registration No. 33,997; Robert E. Sokohl, Esquire, Registration No. 36,013; Eric K. Steffe, Esquire, Registration No. 36,688; Michael Q. Lee, Esquire, Registration No. 35,239; and Steven R. Ludwig, Registration No. 36,203, of STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C., 1100 New York Avenue, N.W., Suite 600, Washington, D.C. 20005-3934, power to insert in this assignment any further identification that may be necessary or desirable in order to comply with the rules of the United States Patent and Trademark Office for recordation of this document.


IN WITNESS WHEREOF, executed by the undersigned inventor(s) on the date opposite his/her name.

Date: 3rd Dec 99 Signature of Inventor:   
Alex Chalfin

Date: \_\_\_\_\_ Signature of Inventor: \_\_\_\_\_  
Jeffrey Daudel

Date: 29 NOV 99 Signature of Inventor:   
Mark Grossman

Date: 28th November 1999 Signature of Inventor:   
Shrijeet Mukherjed

Date: 29 November 1999 Signature of Inventor:   
Peter Ostrin

Date: \_\_\_\_\_ Signature of Inventor: \_\_\_\_\_  
Jarrett Redd

## ASSIGNMENT

In consideration of the sum of One Dollar (\$1.00) or equivalent and other good and valuable consideration paid to each of the undersigned inventor(s): (1) Alex Chalfin, (2) Jeffrey Daudel, (3) Mark Grossman, (4) Shrijeet Mukherjee, (5) Peter Ostrin, and (6) Jarrett Redd, the undersigned inventor(s) hereby sell(s) and assign(s) to Silicon Graphics, Inc. (the Assignee) his/her entire right, title and interest, including the right to sue for past infringement and to collect for all past, present and future damages:

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 and throughout the world,

(a) in the invention(s) known as Synchronization of Hardware Simulation Processes for which application(s) for patent in the United States of America has (have) been executed by the undersigned on (1) \_\_\_\_\_, (2) 12/3/88, (3) \_\_\_\_\_, (4) \_\_\_\_\_, (5) \_\_\_\_\_, and (6) \_\_\_\_\_ (also known as United States Application No. (to be determined), filed Herewith), in any and all applications thereon, in any and all Letters Patent(s) therefor, and

(b) in any and all applications that claim the benefit of the patent application listed above in part (a), including continuing applications, reissues, extensions, renewals and reexaminations of the patent application or Letters Patent therefor listed above in part (a), to the full extent of the term or terms for which Letters Patents issue, and

(c) in any and all inventions described in the patent application listed above in part (a), and in any and all forms of intellectual and industrial property protection derivable from such patent application, and that are derivable from any and all continuing applications, reissues, extensions, renewals and reexaminations of such patent application, including, without limitation, patents, applications, utility models, inventor's certificates, and designs together with the right to file applications therefor; and including the right to claim the same priority rights from any previously filed applications under the International Agreement for the Protection of Industrial Property, or any other international agreement, or the domestic laws of the country in which any such application is filed, as may be applicable;

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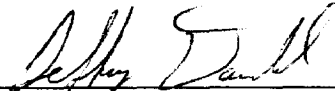
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IN WITNESS WHEREOF, executed by the undersigned inventor(s) on the date opposite his/her name.

Date: \_\_\_\_\_ Signature of Inventor: \_\_\_\_\_  
Alex Chalfin

Date: 12/3/99 Signature of Inventor:   
Jeffrey Daudel

Date: \_\_\_\_\_ Signature of Inventor: \_\_\_\_\_  
Mark Grossman

Date: \_\_\_\_\_ Signature of Inventor: \_\_\_\_\_  
Shrijeet Mukherjee

Date: \_\_\_\_\_ Signature of Inventor: \_\_\_\_\_  
Peter Ostrin

Date: \_\_\_\_\_ Signature of Inventor: \_\_\_\_\_  
Jarrett Redd

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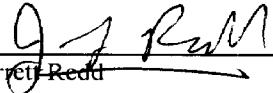
Date: \_\_\_\_\_ Signature of Inventor: \_\_\_\_\_  
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Shrijeet Mukherjee

Date: \_\_\_\_\_ Signature of Inventor: \_\_\_\_\_  
Peter Ostrin

Date: Nov 29, 1999 Signature of Inventor:   
Jarrett Redd