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FORM PTO-1595 (Rev. 6-93)

OMB No. 0651-0011(exp. 4/94)

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Patent and Trademark Office

101210	2207/7450
To the Honorable Commissioner of Patents and Trademarks	s: Please record the attached original documents or copy thereof.
1. Name of conveying party(ies):	2. Name and address of receiving party(ies):
Prakash S. ARUNACHALAM Pankaj KUKKAL	Name: Intel Corporation Internal Address: 2200 Mission College Boulevard Street Address: P.O. Box 58119
Additional name(s) of conveying party(ies) attached? □Yes ⊠No	Santa Clara, CA 95052-8119
3. Nature of conveyance:	- 14 - 15
 ☑ Assignment ☐ Merger ☐ Change of Name ☐ Other 	Additional name(s) & address(es) attached? □Yes ⊠No Toology
Execution Date: December 6, 1999	
4. Application number(s) or patent number(s);	
If this document is being filed together with a new application, t	the execution date of the application is: December 6, 1999
A. Patent Application No.(s)	B. Patent No.(s)
Additional numbers	attached: □ Yes ⊠ No
5. Name and address of party to whom correspondence concerning document should be mailed:	6. Total number of applications and patents involved:
Robert L. Hails, Jr. Kenyon & Kenyon	7. Total fee (37 CFR 3.41)\$40.00 □ Enclosed ⊠ Authorized to be charged to deposit account
Suite 700	8. Deposit account number:
1500 K Street, N.W. Washington, DC 20005-1257	11-0600 —
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Robert L. Hails, Jr. (Reg. No.39,702) Name of Person Signing Signature	Describer 9, 1999 Jes including cover sheet: 2
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PATENT Docket No. 2207/7450

<u>ASSIGNMENT</u>

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, the undersigned hereby sell, assign, and transfer to Intel Corporation, a corporation of Delaware, having a principal place of business at 2200 Mission College Blvd., P.O. Box 58119, Santa Clara, California 95052-8119 ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements that are disclosed in the United States Patent Application entitled:

ITERATIVE METHOD OF PARASITICS ESTIMATION FOR INTEGRATED CIRCUIT DESIGNS

filed herewith, and in and to said United States Patent Application and all divisional, continuing, substitute, renewal, reissue, and other patent applications that have been or shall be filed in the United States or any foreign country on any of said improvements; and in and to all original and reissued patents that have been or shall be issued in the United States or any foreign country on said improvements; and in and to all rights of priority resulting from the filing of said United States Patent Application;

agree that said Assignee may apply for and receive a patent or patents for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and representatives all facts known to the undersigned relating to said improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or representatives in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives; and

covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Date: 12/6/, 1999

Date: <u>12 / 6</u>, 1999

Prakash S. ARUNACHALAM

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Pankai KUKKAL

Assignment Document Return Address:

Robert L. Hails, Jr. Esq. KENYON & KENYON Suite 700, 1500 K Street, N.W. Washington, D.C. 20005

RECORDED: 12/09/1999

PATENT REEL: 010477 FRAME: 0497