MRD 1-10-00

PATENTS ONLY (ref OG 7/28

2.

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original document or copy thereof.

1.	Name c	ρ£	Party(ies)	conveying	an	
	interest.					

Ichiro KONO; Kazuo YANO Naoki KATO

Name and Address of Party(ies) receiving an interest:

HITACHI, LTD. 6, Kanda Surugadai 4-chome Chiyoda-ku, Tokyo, JAPAN

Description of the interest conveyed: Assignment

Application number(s) or patent number(s).

Additional sheet attached? \_\_\_ Yes XXX No

If this document is being filed together with a new application, the execution date of the application is:

DATE

Patent Application No.(s)

Patent No. (s)

09/436,501

Name and address of party to whom correspondence concerning document should be mailed:

> Thomas E. Beall, Jr. Beall Law Offices 104 East Hume Avenue Alexandria, Virginia 22301

- Number of applications and patents involved:
- Amount of fee enclosed or authorized to be charged: \$40.00
- The Commissioner is hereby authorized to charge Deposit Account No. 02-1540 if no check is attached.

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9. Execution date of attached document: December 16 and December 17, 1999

10. To the best of my knowledge and belief, I declare under penalty of perjury under the laws of the United States of America that the foregoing information is true and correct and that any attached copy is a true copy of the original

document.

Daniel J. Stanger Name of Person Signing

January 10, 2000

Date

Total number of pages being submitted: 2

**PATENT REEL: 010489 FRAME: 0115** 

## ASSIGNMENT

(譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by HITACHI, LTD., a corporation organized under the laws of Japan, located at 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo, Japan, receipt of which is hereby acknowledged I do hereby sell and assign to said HITACHI, LTD., its successors and assigns, all my right, title and interest, in and for the United States of America, in and to

## SEMICONDUCTOR INTEGRATED CIRCUIT FOR LOW POWER AND HIGH SPEED OPERATION

invented by me— (if only one is named below) or us— (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said HITACHI, LTD., its successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said HITACHI, LTD.

Signed on the date(s) indicated aside our signatures:

RECORDED: 01/10/2000

	INVENTOR(S) (発明者フルネームサイン)	Date Signed (署名日)
1)	Schiro Rono	12/17/1889
2)	Kan hop	12/16/1999
3)	Marki Lato	(2/(6/1995
4)		_
5)		
6)		
7)		- <del> </del>
8)		
9)		
10)		

PATENT
REEL: 010489 FRAME: 0116