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4a. Paten: Application No.(s) <u>09/449,140</u> 4b. Patent No.(s) <u></u>	Mark J. Murphy Name of Person Signing Signature Reg. No. 34,225 Date: February 23, 2000		
X Execution Date: 2/17/2000 4. Application Number(s) or Patent number(s) (If this document is being filed together with a new application, the execution date of the application is:)	9. Statement and Signature: To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.		
3. Nature of Conveyance X Assignment Merger Security Agreement Change of Name Other	 7. Total Fee (37 CFR 3.41): \$40.00 7a. X Enclosed 7b. X Authorized to be Charged to Deposit Account For Any Deficiency 8. Deposit Account Number: 50/1039 (A duplicate copy of this page is attached) 		
243-0036 Japan 2b. Name and Address of Receiving Party(ies): Name: Address	Edward D. Manzo, Esq. COOK, ALEX, McFARRON, MANZO, CUMMINGS & MEHLER, LTD. 200 West Adams Street, Suite 2850 Chicago, Illinois 60606 6. Total Number of Applications and Patents Involved: 1		
2a. Name and Address of Receiving Party(ies): Name: Semiconductor Energy Laboratory Co., Ltd. Address: 398, Hase, Atsugi-shi, Kanagawa-ken			
Name of Conveying Party(ies): Hisashi OHTANI	5. Name and Address of Party to Whom Correspondence Concerning Document Should be Mailed		
Sir: Please record the attached original document or copy t	thereof. Total number of pages (with cover sheet): 2		
Assistant Commissioner for Patents Washington, D. C. 20231	MENT COVER SHEET February 23.		
For: Method Of Manufacturing Thin Film Transistor Art Unit: 2825 Examiner:	D.C. 20231, on <u>February 24</u>		
In re application of: Hisashi Ohtani Serial No: 09/449,140 Filed: November 24, 1999	Postal Service as first class mail in envelope addressed to: Commissioner for Patents, Washing		

PATENT

REEL: 010577 FRAME: 0983

Attorney	Docket	SEL	147	
Amorney	DOCKEL			

ASSIGNMENT

RECORDED: 02/28/2000

PATENT REEL: 010577 FRAME: 0984