

A S S I G N M E N T

WHEREAS I/We, the below named inventor(s), [hereinafter referred to as Assignor(s)], have made an invention entitled: NONVOLATILE SEMICONDUCTOR MEMORY AND METHOD FOR MANUFACTURING THE SAME, VOLATILE SEMICONDUCTOR MEMORY AND METHOD FOR MANUFACTURING THE SAME, AND SEMICONDUCTOR MEMORY IN WHICH NONVOLATILE (*) for which I/WE executed an application for United States Letters Patent concurrently herewith or on _____ or filed an application for United States Letters Patent on _____, 19____ (Serial No. _____); and

WHEREAS, KABUSHIKI KAISHA TOSHIBA, a corporation of Japan, whose post office address is 72 Horikawa-cho, Saiwai-ku, Kawasaki-shi, Kanagawa-ken, Japan (hereinafter referred to as Assignee), is desirous of securing the entire right, title, and interest in and to this invention, the application for United States Letters Patent on this invention and the Letters Patent to be issued upon this application;

NOW THEREFORE, be it known that, for good and valuable consideration the receipt of which from assignee is hereby acknowledged, I/WE, as assignor(s), have sold, assigned, transferred, and set over, and do hereby sell, assign, transfer, and set over unto the assignee, its lawful successors and assigns, my/our entire right, title, and interest in and to this invention and this application, and all divisions, and continuations thereof, and all Letters Patent of the United States which may be granted thereon, and all reissues thereof; and I/WE hereby authorize and request the Commissioner of Patents and Trademarks of the United States to issue all Letters Patent for this invention to assignee, its successors and assigns, in accordance with the terms of this Assignment;

AND, I/WE HEREBY further covenant and agree that I/We will, without further consideration, communicate with assignee, its successors and assigns, any facts known to me/us respecting this invention and testify in any legal proceeding, sign all lawful papers when called upon to do so, execute and deliver all papers that may be necessary or desirable to perfect the title to this invention in said assignee, its successors and assigns, execute all divisional, continuation, and reissue applications, make all rightful oaths and generally do everything possible to aid assignee, its successors and assigns, to obtain and enforce proper patent protection for this invention in the United States, it being understood that any expense incident to the execution of such papers shall be borne by the assignee, its successors and assigns.

AND, I/WE HEREBY authorize and request the attorneys I/we have empowered in the Declaration and Power of Attorney in this application, to insert here in parentheses (Application No. _____, filed _____) the filing date and application number of said application when known.

IN TESTIMONY WHEREOF, I/We have hereunto set our hand(s).

1. Full Name of Sole or First Assignor :	Assignor's Signature	: Date
Kuniyoshi YOSHIKAWA	<i>Kuniyoshi Yoshikawa</i>	: March 2, 2000
Address		: Citizenship
Tokyo, Japan		: Japan
2. Full Name of Second Assignor :	Assignor's Signature	: Date
		:
Address		: Citizenship
		:
3. Full Name of Third Assignor :	Assignor's Signature	: Date
		:
Address		: Citizenship
		:

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P. · WASHINGTON, D.C.

(*) SEMICONDUCTOR MEMORY AND VOLATILE SEMICONDUCTOR MEMORY ARE MIXED AND METHOD FOR MANUFACTURING THE SAME