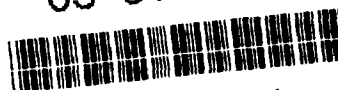


MRA  
2-2400

FORM PTO-1595  
1-31-92



03-31-2000



101302491

U.S. DEPARTMENT OF COMMERCE  
Patent and Trademark Office

To the Honorable Commissioner of Patents and Trademarks. Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Watkins-Johnson Company

☐ Individual(s)

☐ General

Partnership

☒ Corporation - State California

☐ Other

Additional name(s) of conveying

party(ies) attached? ☐ Yes ☒ No

☐ Association

☐ Limited

Partnership

2. Name and address of receiving party(ies):

Name: Canadian Imperial Bank of Commerce

Internal Address: \_\_\_\_\_

Street Address: 425 Lexington Avenue

City: New York

State: New York

Zip: 10017

3. Nature of Conveyance:

☐ Assignment

☐ Merger

☒ Security Agreement

☐ Change of Name

☐ Other

☐ Individual(s) citizenship

☐ Association

☐ General Partnership

☐ Limited Partnership

☐ Corporation - State

☒ Other A Canadian Chartered Bank

If assignee is not domiciled in the U.S., a domestic representative designation is attached: ☐ Yes ☐ No

(Designation must be a separate document from Assignment)

Execution Date: January 31, 2000

Additional name(s) & address(es) attached?

☐ Yes ☒ No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is:

A. Patent Application No.(s)

05188,099	60/013,030
05428,797	08/661,334
08/105,292	08/948,771
08/575,294	08/926,175
08/811,574	09/378,337
60/013,020	60/136,699
09/006,093	
60/004,112	
08/813,003	

B. Patent No.(s)

3,584,306	Re. 36,185	5,732,345
3,638,126	5,513,390	5,873,036
3,562,651	5,361,409	5,639,343
3,908,148	5,551,074	5,613,859
3,980,919	5,752,181	5,823,791
3,913,216	5,678,226	5,629,654
3,893,157	5,854,974	5,652,179
4,935,377	5,768,268	5,745,328
5,374,328	5,799,248	

Additional numbers attached? ☐ Yes ☒ No

03/30/2000 DNGUYEN 00000009 188099

01 FC:581

1640.00 OP

PATENT  
REEL: 010639 FRAME: 0115

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Maria S. Swiatek

Internal Address: FLEHR HOHBACH TEST

ALBRITTON & HERBERT LLP

Street Address: SUITE 3400

FOUR EMBARCADERO CENTER

City: SAN FRANCISCO

State: CA

Zip: 94111-4187

6. Total number of applications and patents involved: 41

7. Total fee (37 CFR 3.41):.....\$ 1640.00

☒ Enclosed

☐ Authorized to be charged to deposit account

8. Deposit account number: 06-1300

Please debit any underpayment or credit any overpayment to the above deposit account.

Our Order No. G-63687-10/AJT/MSS

(Attach duplicate of this page if paying by deposit account)

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9. Statement and signature.

*To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.*

Maria S. Swiatek, Reg. No. 37,244

Name of Person Signing

  
Signature

January 31, 2000

Date

Total number of pages including cover sheet, attachments and document: [ ]

OMB No. 0651-0011 (exp. 4/94)

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Mail documents to be recorded with required cover sheet information to:

Commissioner of Patents and Trademarks  
Box Assignments  
Washington, DC 20231

File No. G-63687-10/AJT/MSS

1004066

Rev. 8/93

## GRANT OF PATENT SECURITY INTEREST

**WHEREAS, WATKINS-JOHNSON COMPANY**, a California corporation (**"Grantor"**), owns and uses in its business, and will in the future adopt and so use, various intangible assets, including the Patent Collateral (as defined below); and

**WHEREAS**, Watkins-Johnson Company, a California corporation, as successor of FP-WJ Acquisition Company, a California corporation (**"Borrower"**), has entered into a Credit Agreement dated as of January 31, 2000 (said Credit Agreement, as it may heretofore have been and as it may hereafter be amended, supplemented or otherwise modified from time to time, being the **"Credit Agreement"**) with the financial institutions named therein (collectively, together with their respective successors and assigns party to the Credit Agreement from time to time, the **"Lenders"**), Canadian Imperial Bank of Commerce, as Administrative Agent for the Lenders (in such capacity, **"Secured Party"**), BT Commercial Corporation, as syndication agent for Lender (in such capacity, the **"Syndication Agent"**) and as co-arranger (in such capacity, the **"Co-Arranger"**), IBM Credit Corporation, as co-agent (in such capacity, the **"Co-Agent"**) and CIBC World Markets Corp., as lead Arranger and bookrunner (in such capacity, the **"Lead Arranger"**), pursuant to which Lenders have made certain commitments, subject to the terms and conditions set forth in the Credit Agreement, to extend certain credit facilities to Borrower; and

**WHEREAS**, Borrower may from time to time enter, or may from time to time have entered, into one or more Interest Rate Agreements (collectively, the **"Lender Interest Rate Agreements"**) with one or more Persons that are Lenders or Affiliates of Lenders at the time such Lender Interest Rate Agreements are entered into (in such capacity, collectively, **"Interest Rate Exchangers"**); and

**WHEREAS**, pursuant to the terms of a Security Agreement dated as of January 31, 2000 (as amended, supplemented or otherwise modified from time to time, the **"Security Agreement"**), among Grantor, Secured Party and the other grantors named therein, Grantor has agreed to create in favor of Secured Party a secured and protected interest in, and Secured Party has agreed to become a secured creditor with respect to, the Patent Collateral;

**NOW, THEREFORE**, for good and valuable consideration, the receipt and adequacy of which are hereby acknowledged, subject to the terms and conditions of the Security Agreement, Grantor hereby grants to Secured Party, for the benefit of the Lenders, Administrative Agent, Syndication Agent, Co-Arranger, Co-Agent and Interest Rate Exchangers, a security interest in all of Grantor's right, title and interest in and to the following, in each case whether now or hereafter existing or in which Grantor now has or hereafter acquires an interest and wherever the same may be located (the **"Patent Collateral"**):

- (i) all rights, title and interest (including rights acquired pursuant to a license or otherwise but only to the extent permitted by agreements governing such license or other use) in and to all patents and patent applications and rights and interests in patents and patent applications under any domestic or foreign law that are presently, or in the future may be, owned or held by such Grantor and all patents and patent applications and rights,

title and interests in patents and patent applications under any domestic or foreign law that are presently, or in the future may be, owned by such Grantor in whole or in part (including, without limitation, the patents and patent applications listed in Schedule A), all rights (but not obligations) corresponding thereto to sue for past, present and future infringements and all re-issues, divisions, continuations, renewals, extensions and continuations-in-part thereof (all of the foregoing being collectively referred to as the **"Patents"**); and

(ii) all proceeds, products, rents and profits of or from any and all of the foregoing Patent Collateral and, to the extent not otherwise included, all payments under insurance (whether or not Secured Party is the loss payee thereof), or any indemnity, warranty or guaranty, payable by reason of loss or damage to or otherwise with respect to any of the foregoing Patent Collateral. For purposes of this Grant of Patent Security Interest, the term **"proceeds"** includes whatever is receivable or received when Patent Collateral or proceeds are sold, exchanged, collected or otherwise disposed of, whether such disposition is voluntary or involuntary.

Notwithstanding anything herein to the contrary, in no event shall the Patent Collateral include, and Grantor shall be not deemed to have granted a security interest in, any of Grantor's rights or interests in any license, contract or agreement to which Grantor is a party or any of its rights or interests thereunder to the extent, but only to the extent, that such a grant would, under the terms of such license, contract or agreement or otherwise, result in a breach of the terms of, or constitute a default under any license, contract or agreement to which Grantor is a party; provided, that immediately upon the ineffectiveness, lapse or termination of any such provision, the Patent Collateral shall include, and Grantor shall be deemed to have granted a security interest in, all such rights and interests as if such provision had never been in effect.

Grantor does hereby further acknowledge and affirm that the rights and remedies of Secured Party with respect to the security interest in the Patent Collateral granted hereby are more fully set forth in the Security Agreement, the terms and provisions of which are incorporated by reference herein as if fully set forth herein.

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**IN WITNESS WHEREOF**, Grantor has caused this Grant of Patent Security Interest to be duly executed and delivered by its officer thereunto duly authorized as of the \_\_\_\_ day of January, 2000.

**WATKINS-JOHNSON COMPANY**

By: \_\_\_\_\_



Malcolm Caraballo  
President and Chief Executive Officer

**SCHEDULE A  
TO  
GRANT OF PATENT SECURITY INTEREST**

<b>Title of the Invention/Inventors</b>	<b>Serial No./ Filing Date</b>	<b>Patent No./ Issue Date</b>	<b>Foreign Countries</b>
HIGH FREQUENCY CONVERTER	USSN 05/668,718 Filed	US 3,584,306 Issued 06/08/71 Expired	
HIGH FREQUENCY CONVERTER; Robert Griffith, et al.	USSN 05/851,923	US 3,638,126 Issued 01/25/72 Expired	
MICROWAVE HYBRID JUNCTION CIRCUIT AND FREQUENCY TRANS		3,562,651 02/09/71 Expired	
ELECTRO-OPTICAL TRANSDUCER AND STORAGE TUBE	SN 188,099 Filed 10/12/71	Abandoned	
ELECTRO-OPTICAL TRANSDUCER AND STORAGE TUBE	SN 428,797 Filed 12/27/73	Abandoned	
ELECTRO-OPTICAL TRANSDUCER AND STORAGE TUBE	SN 524,775 Filed 11/18/74	3,908,148 09/23/75 Expired	
RECTANGULAR BEAM LAMINAR FLOW ELECTRON	SN 535,098 Filed 12/20/74	3,980,919 09/14/76 Expired	
METHOD OF FABRICATING A PRECISION ALIGNED SEMICONDUCTOR ARRAY	SN 371,743 Filed 06/20/73	3,913,216 10/21/75 Expired	
SEMICONDUCTOR TARGET WITH INTEGRAL BEAM SHIELD	SN 366,534 Filed 06/04/73	3,893,157 07/01/75 Expired	
METHOD OF FABRICATING A MICROWAVE FET HAVING GATE WITH SUBMICRON LENGTH; Strifler, Cantos	SN 07/388,627 Filed 08/01/89	US 4,935,377 Issued 06/19/90	
METHOD OF FABRICATING MICROWAVE FET HAVING GATE WITH SUBMICRON LENGTH; Strifler, Cantos	SN 02-181366 Filed 07/09/90	Abandoned per client 08/25/98. Not yet abandoned in JPO.	Japan
METHOD OF FABRICATING GROUP III-IV COMPOUND SEMICONDUCTOR DEVICES USING SELECTIVE ETCHING; Brunemeier, Remba, Resenblatt, Schmukler, Strifler	SN 08/037,074 Filed 03/25/93	US 5,374,328 Issued 12/20/94 UNDER REISSUE	
ETCH STOP MMIC PROCESS; Brunemeier, Remba, Resenblatt, Schmukler, Strifler	SN 108762 Filed 02/24/94	IL 108762 Issued 02/28/96	Israel

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<b>Title of the Invention/Inventors</b>	<b>Serial No./ Filing Date</b>	<b>Patent No./ Issue Date</b>	<b>Foreign Countries</b>
ETCH STOP MMIC PROCESS; Brunemeier, Remba, Resenblatt, Schmukler, Strifler	SN 6-521065 Filed 03/03/94	JP 2871857 Issued 01/08/99	Japan
ETCH STOP MMIC PROCESS; Brunemeier, Remba, Resenblatt, Schmukler, Strifler	SN 704223/94 Filed 03/03/94	Abandoned per client e-mail 07/15/98	S. Korea
METHOD OF FABRICATING GROUP III-IV COMPOUND SEMICONDUCTOR DEVICES USING SELECTIVE ETCHING; Brunemeier, Remba, Resenblatt, Schmukler, Strifler	SN 94910829.4 Filed 03/03/94		Europe
METHOD OF FABRICATING GROUP III-V COMPOUND SEMICONDUCTOR DEVICES USING SELECTIVE ETCHING; Brunemeier, Remba, Resenblatt, Schmukler, Strifler	PCT/US94/02328 Filed 03/03/94	Closed	PCT
METHOD OF FABRICATING GROUP III-IV COMPOUND SEMICONDUCTOR DEVICES USING SELECTIVE ETCHING; Brunemeier, Remba, Resenblatt, Schmukler, Strifler	USSN 08/751,776 Filed 12/05/96 Reissue Appl. of U.S. Pat. 5,374,328 Issued 12/20/94	US Re. 36,185 Issued 04/06/99	
BIASED FET MIXER; Vice	SN 08/149,671 Filed 11/09/93	US 5,513,390 Issued 04/30/96	
FET MIXER; Vice	USSN 08/004,234 Filed 01/14/93	US 5,361,409 Issued 11/01/94	
BALANCED REFLECTION TRANSFORMER; Vice	USSN 08/105,292 Filed 08/10/93	Abandoned	
BALANCED REFLECTION TRANSFORMER; Vice	USSN 08/376,126 Filed 01/19/95	US 5,551,074 Issued 08/27/96	
METHOD AND APPARATUS FOR REDUCING INTERMODULATION DISTORTION IN A MIXER; Vice	USSN 08/574,071 Filed 12/18/95	US 5,752,181 Issued 05/12/98	
UNBALANCED FET MIXER; Li et al.	USSN 08/333,775 Filed 11/03/94	US 5,678,226 Issued 10/14/97	
UNBALANCED FET MIXER; Li et al	SN 2174395 Filed 04/17/96		Canada
UNBALANCED FET MIXER; Li et al	SN 8-95461 Filed 04/17/96		Japan
UNBALANCED FET MIXER; Li et al	SN 11527/96 Filed 04/17/96		S. Korea
UNBALANCED FET MIXER; Li et al	SN 96302777.6 Filed 04/19/96	Published	Europe
HIGH DYNAMIC RANGE DIODE MIXERS; Li	USSN 08/420,249	US 5,854,974	

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<b>Title of the Invention/Inventors</b>	<b>Serial No./ Filing Date</b>	<b>Patent No./ Issue Date</b>	<b>Foreign Countries</b>
	Filed 04/11/95	Issued 12/29/98	
COMPENSATED RING MIXERS; XIAOHUI LI	SN 8-531207 Filed 04/11/96		Japan
COMPENSATED RING MIXERS; XIAOHUI LI	SN 707788/97 Filed 04/11/96		S. Korea
COMPENSATED RING MIXERS; XIAOHUI LI	SN 96912704.2 Filed 04/11/96	Published	Europe
COMPENSATED RING MIXERS; Li	PCT/US96/05061 Filed 04/11/96	Closed	PCT
MIXER CIRCUITS; Vice	SN 08/575,294 Filed 12/20/95	Abandon per client letter 10/07/99.	
TRIPLE-BALANCED PASSIVE TRANSMISSION FET MIXER; Vice	PCT/US96/19964 Filed 12/18/96	Closed	PCT
SYSTEM AND METHOD FOR DYNAMIC CHANNEL ALLOCATION IN A CELLULAR COMMUNICATION NETWORK; Plaschke, Vucetic, Kline	USSN 08/811,574 Filed 03/05/97		
WIRELESS COMMUNICATION SYSTEM WITH DYNAMIC CHANNEL ALLOCATION; Vucetic, Plaschke, Kline	SN 97917516.3 Filed 03/07/97		Europe
WIRELESS COMMUNICATION SYSTEM WITH DYNAMIC CHANNEL ALLOCATION; Vucetic, Plaschke, Kline	PCT/US97/03789 Filed 03/07/97	Closed	PCT
WIRELESS COMMUNICATION SYSTEM WITH DYNAMIC CHANNEL ALLOCATION; Plaschke, Vucetic, Kline	USSN 60/013,020 Filed 03/08/96	Closed in favor of 08/811,574	
DIGITAL BASE STATION FOR CELLULAR COMMUNICATION SYSTEM; Kline, Harris, Anderson	USSN 08/504,175 Filed 07/19/95	US 5,768,268 Issued 06/16/98	
WIDEBAND BASE STATION ARCHITECTURE FOR DIGITAL CELLULAR COMMUNICATIONS SYSTEMS; Anderson, Kline, Harris	USSN 09/006,093 Filed 01/13/98		
WIDEBAND BASE STATION ARCHITECTURE FOR DIGITAL CELLULAR COMMUNICATIONS SYSTEMS; Anderson, Kline, Harris	SN PI9609566-0 Filed 07/17/96	Abandoned	
WIDEBAND BASE STATION ARCHITECTURE FOR DIGITAL CELLULAR COMMUNICATIONS SYSTEMS; Anderson,	SN 96196635.1 Filed 07/17/96		China w/ extension to Hong Kong



<b>Title of the Invention/Inventors</b>	<b>Serial No./ Filing Date</b>	<b>Patent No./ Issue Date</b>	<b>Foreign Countries</b>
Kline, Harris			
WIDEBAND BASE STATION ARCHITECTURE FOR DIGITAL CELLULAR COMMUNICATIONS SYSTEMS; Anderson, Kline, Harris	SN 99100229.5 Filed 01/18/99		Hong Kong
WIDEBAND BASE STATION ARCHITECTURE FOR DIGITAL CELLULAR COMMUNICATIONS SYSTEMS; Anderson, Kline, Harris	SN 9-506889 Filed 07/17/96		Japan
WIDEBAND BASE STATION ARCHITECTURE FOR DIGITAL CELLULAR COMMUNICATIONS SYSTEMS; Anderson, Kline, Harris	SN 9801370-9 Filed 07/17/96		Singapore
WIDEBAND BASE STATION ARCHITECTURE FOR DIGITAL CELLULAR COMMUNICATIONS SYSTEMS; Anderson, Kline, Harris	SN 96925374.9 Filed 07/17/96	Published	Europe
WIDEBAND BASE STATION ARCHITECTURE FOR DIGITAL CELLULAR COMMUNICATIONS SYSTEMS; Anderson, Kline, Harris	PCT/US96/11954 Filed 07/17/96	Closed	PCT
SINGLE BALUN BALANCED MIXER CIRCUIT; Vice	USSN 08/575,409 Filed 12/20/95	US 5,799,248 Issued 08/25/98	
QUASI-DOUBLE BALANCED PASSIVE REFLECTION FET MIXER; Vice	PCT/US96/20548 Filed 12/18/96	Closed	PCT
DUAL BALUN BALANCED MIXER CIRCUIT; Vice	USSN 08/575,293 Filed 12/20/95	US 5,732,345 Issued 03/24/98	
QUASI-DOUBLE BALANCED DUAL- TRANSFORMER PASSIVE REFLECTION FET MIXER; Vice	PCT/US96/19965 Filed 12/18/96	Abandoned	PCT
MULTI-USER CELLULAR NETWORK; Vucetic	USSN 08/717,300 Filed 09/20/96	US 5,873,036 Issued 02/16/99	
MULTI-USER CELLULAR NETWORK; Vucetic	USSN 60/004,112 Filed 09/21/95	Closed in favor of 08/717,300	
METHOD OF CHARACTERIZING GROUP III- V EPITAXIAL SEMICONDUCTOR DEVICES INCORPORATING AN ETCH STOP LAYER; Dobkin	USSN 08/571,518 Filed 12/13/95	US 5,639,343 Issued 06/17/97	
METHOD OF CHARACTERIZING GROUP III- V EPITAXIAL SEMICONDUCTOR WAFERS INCORPORATING AN ETCH STOP LAYER;	SN 2,238,533 Filed 12/06/96		Canada

<b>Title of the Invention/Inventors</b>	<b>Serial No./ Filing Date</b>	<b>Patent No./ Issue Date</b>	<b>Foreign Countries</b>
Dobkin			
METHOD OF CHARACTERIZING GROUP III-V EPITAXIAL SEMICONDUCTOR WAFERS INCORPORATING AN ETCH STOP LAYER; Dobkin	SN 9-522114 Filed 12/06/96		Japan
METHOD OF CHARACTERIZING GROUP III-V EPITAXIAL SEMICONDUCTOR WAFERS INCORPORATING AN ETCH STOP LAYER; Dobkin	SN 96943610.4 Filed 12/06/96	Published	Europe
METHOD OF CHARACTERIZING GROUP III-V EPITAXIAL SEMICONDUCTOR WAFERS INCORPORATING AN ETCH STOP LAYER; Dobkin	PCT/US96/19406 Filed 12/06/96	Closed	PCT
CONNECTOR ASSEMBLY FOR DETACHABLY CONNECTING A PRINTED WIRING BOARD TO A COAXIAL TRANSMISSION LINE; Tobias, Bellantoni	USSN 08/563,299 Filed 11/28/95	US 5,613,859 Issued 03/25/97	
CONNECTOR ASSEMBLY FOR DETACHABLY CONNECTING A PRINTED WIRING BOARD TO A COAXIAL TRANSMISSION LINES CONNECTOR; Tobias, Bellantoni	USSN 08/744,623 Filed 11/06/96	US 5,823,791 Issued 10/20/98	
A CONNECTOR ASSEMBLY FOR DETACHABLY CONNECTING A PRINTED WIRING BOARD TO A COAXIAL TRANSMISSION LINES CONNECTOR; Tobias, Bellantoni	SN 8-315230 Filed 11/26/96		Japan
CONNECTOR ASSEMBLY FOR DETACHABLY CONNECTING A PRINTED WIRING BOARD TO A COAXIAL TRANSMISSION LINE; Tobias, Bellantoni	SN 58641/96 Filed 11/28/96		S. Korea
WIRELESS BASE STATION WITH NEAR-FAR GAIN COMPENSATION; Raleigh, Pollack	USSN 08/813,003 Filed 03/04/97		
WIRELESS BASE STATION WITH NEAR-FAR GAIN COMPENSATION; Raleigh, Pollack	SN 97914976.2 Filed 03/07/97		Europe
WIRELESS BASE STATION WITH NEAR-FAR GAIN COMPENSATION; Raleigh, Pollack	PCT/US97/03784 Filed 03/07/97	Closed	PCT
WIRELESS BASE STATION WITH NEAR-FAR GAIN COMPENSATION; Raleigh, Pollack	USSN 60/013,030 Filed 03/08/96	Closed in favor of 08/813,003	
BROADSIDE MICROWAVE COUPLED BALUNS; Li	USSN 08/661,334 Filed 06/14/96		

<b>Title of the Invention/Inventors</b>	<b>Serial No./ Filing Date</b>	<b>Patent No./ Issue Date</b>	<b>Foreign Countries</b>
BROADSIDE MICROWAVE COUPLED BALUNS; Li	SN 10-501637 Filed 05/28/97		Japan
BROADSIDE MICROWAVE COUPLED BALUNS; li	SN 710241/98 Filed 05/28/97		S. Korea
A MULTI-LAYER PRINTED WIRING BOARD HAVING INTEGRATED BROADSIDE MICROWAVE COUPLER; Li	SN 86108238 Filed 06/12/97	Abandon per client instr. 06/15/98.	Taiwan
A MULTI-LAYER PRINTED WIRING BOARD HAVING INTEGRATED BROADSIDE MICROWAVE COUPLER; Li	SN 97927850.4 Filed 05/28/97		Europe
A MULTI-LAYER PRINTED WIRING BOARD HAVING INTEGRATED BROADSIDE MICROWAVE COUPLER; Li	PCT/US97/09236 Filed 05/28/97	Closed	PCT
CO-PLANAR WAVEGUIDE COUPLER; Frick	USSN 08/643,852 Filed 05/06/96	US 5,629,654 Issued 05/13/97	
CO-PLANAR WAVEGUIDE COUPLER; Frick	SN 9-540265 Filed 05/06/97		Japan
CO-PLANAR WAVEGUIDE COUPLER; Frick	SN 97925593.2 Filed 05/06/97		Europe
CO-PLANAR WAVEGUIDE COUPLER; Frick	PCT/US97/08298 Filed 05/06/97	Closed	PCT
METHOD OF FABRICATING SUB-MICRON GATE ELECTRODE BY ANGLE AND DIRECT EVAPORATION; Strifler, Lee, Hitchens, Remba	USSN 08/638,950 Filed 04/24/96	US 5,652,179 Issued 07/29/97	
METHOD AND APPARATUS FOR SELF TUNING CELLULAR SYSTEM; Lober, Fouladi	USSN 08/948,771 Filed 10/10/97		
ELECTROMAGNETIC IMPULSE SUPPRESSION CIRCUIT; Ballantoni	USSN 08/811,111 Filed 03/03/97	US 5,745,328 Issued 04/28/98	
ELECTROMAGNETIC IMPULSE SUPPRESSION CIRCUIT; Ballantoni	SN 2281945 Filed 12/13/97		Canada
ELECTROMAGNETIC IMPULSE SUPPRESSION CIRCUIT; Ballantoni	SN 97953345.2 Filed 12/31/97		Europe
ELECTROMAGNETIC IMPULSE SUPPRESSION CIRCUIT; Ballantoni	PCT/US97/23572 Filed 12/31/97	Closed	PCT
TOTEM POLE MIXER HAVING GROUNDED SERIALLY CONNECTED STACKED FET PAIR; Vice	USSN 08/926,175 Filed 09/09/97		
TOTEM POLE MIXER HAVING GROUNDED	PCT/US98/18791		

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<b>Title of the Invention/Inventors</b>	<b>Serial No./ Filing Date</b>	<b>Patent No./ Issue Date</b>	<b>Foreign Countries</b>
SERIALLY CONNECTED STACKED FET PAIR; Vice	Filed 09/09/98		
HIGH CAPACITY COMMUNICATION SYSTEM	09/378,337 08/20/99		
HIGH CAPACITY COMMUNICATIONS SYSTEM	60/136,699 05/28/99	Closed	

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