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To the Honorable Assistant Commissioner for Patents: Please	record the attached original documents or copy thereof.		
Name of conveying party(ies): Ishwardutt Parulkar Sridhar Narayanan	Name and address of receiving party(ies) Name: Sun Microsystems, Inc.		
Additional name(s) of conveying party(ies) attached? PYes XINo	Internal Address:		
3. Nature of conveyance:			
> Assignment □ Merger	Street Address: 901 San Antonio Road		
Security Agreement			
Double Double	City: Palo Alto State: CA ZIP: 94303		
Execution Date: June 1, 2000	Additional name(s) & address(es) attached? Yes Xo No		
4. Application number(s) or patent number(s): 0904050 If this document is being filed together with a new application, the execution date of the application is $6/15/00$			
A. Patent Application No.(s)	B. Patent No.(s)		
 Additional numbers attached? □ Yes 🕏 No			
Name and address of party to whom correspondence 6. concerning document should be mailed:	Total number of applications and patents involved: 1		
Name: <u>D'Alessandro & Ritchie</u> 7.	Total fee (37 CFR 3.41)\$ 40.00		
Internal Address:	🕱 Enclosed		
	Authorized to be charged to deposit account		
8.	Deposit Account number:		
Street Address: P.O. Box 640640	04-0025		
City: San Jose State: CA ZIP: 95164-0640	(Attach duplicate copy of this page if paying by deposit account)		
2000 ASCUTT 00000212 09594656 DO NOT USE THIS SPACE			
9. Statement and signature. To the best of my knowledge and belief, the foregoing information true copy of the original document.			
	u Mone 6/15/2000		
Name of Person Signing Total number of pages including cover sheet, attachments, and document: Mail documents to be recorded with required cover sheet information to: Assistant Commissioner for Patents, Box Assignment, Washington, DC 20231			

ASSIGNMENT

WHEREAS, we, Ishwardutt Parulkar, a citizen of India, residing at 1408 Church St., San Francisco, CA 94131, and Sridhar Narayanan, a citizen of India, residing at 22330 Homestead Road, #226, Cupertino, CA 95014, have invented a "Boundary Scan Cell Architecture with Complete Set of Operational Modes for High Performance Integrated Circuits" for which we have executed application papers for a U.S. patent thereon which is being filed concurrently herewith; and

WHEREAS, Sun Microsystems, Inc., a Delaware corporation, having a place of business 901 San Antonio Road, Palo Alto, California 94303-4900, is desirous of acquiring the exclusive right, title and interest in and to said invention and in and to the Letters Patent to be granted and issued therefor in the United States of America and its territories and possessions, and all countries foreign thereto;

NOW, THEREFORE, for a valuable consideration, the receipt of which is hereby acknowledged, we, Ishwardutt Parulkar and Sridhar Narayanan, do sell, assign, transfer and set over unto the said Sun Microsystems, Inc., its successors and assigns, the full and exclusive right, title and interest in and to said invention, and in and to any and all Letters Patent to be granted and issued therefor, not only for, to and in the United States of America, its territories and possessions, but also for, to and in all other countries including all priority rights under the International Convention; and we hereby authorize and request the Commissioner of Patents and Trademarks to issue said Letters Patent to said Sun Microsystems, Inc., its successors and assigns, in accordance with this Assignment.

WITNESS MY HAND at	SUNNYVALE	, California, thisl ^{s+} day of
	X).)	1, in allien
Ishwardutt Parulkar	Sridhar Narayanan	

PATENT
RECORDED: 06/15/2000 REEL: 010904 FRAME: 0112