

07-28-2000

U.S. Department of Commerce
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PATENT



101416497

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☐ Correction of PTO Error
Reel # Frame #

☐ Corrective Document
Reel # Frame #

Conveyance Type

☒ Assignment ☐ Security Agreement

☐ License ☐ Change of Name

☐ Merger ☐ Other

U.S. Government
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☐ Departmental File ☐ Secret File

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07/28/2000 DNGUYEN 00000042 08672298

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Mail documents to be recorded with required cover sheet(s) information to:
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PATENT
REEL: 010909 FRAME: 0439

Correspondent Name and Address

Area Code and Telephone Number (609) 987-6880

Name Arthur L. Plevy

Address (line 1) Buchanan Ingersoll PC

Address (line 2) 650 College Road East

Address (line 3) Princeton, NJ 08540

Address (line 4)

Pages

Enter the total number of pages of the attached conveyance document including any attachments.

18

Application Number(s) or Patent Number(s)

☒ Mark if additional numbers attached

Enter either the Patent Application Number or the Patent Number (DO NOT ENTER BOTH numbers for the same property).

Patent Application Number(s)

08672298 09391006 09168146

09257119 09249352 09309228

09307689 09374567 09307913

Patent Number(s)

4994868 5144405 5172072

5404109 5278529 5281929

5262740 5406234 5228967

If this document is being filed together with a new Patent Application, enter the date the patent application was signed by the first named executing inventor.

Month Day Year

Patent Cooperation Treaty (PCT)

Enter PCT application number
only if a U.S. Application Number
has not been assigned.

PCT PCT PCT

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Number of Properties

Enter the total number of properties involved.

63

Fee Amount

Fee Amount for Properties Listed (37 CFR 3.41): \$ 2,520.00

Method of Payment:
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To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. Charges to deposit account are authorized, as indicated herein.

Arthur L. Plevy

Name of Person Signing

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FORM PTO-1619C
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Patent Application Number(s)

09399884

Patent Number(s)

5273642

5168242

5428327

5374899

6005267

5309006

5313175

4581076

5420527

5767721

5180998

5338950

5428840

5565696

5753968

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Application Number(s) or Patent Number(s)☐

Mark if additional numbers attached

*Enter either the Patent Application Number or the Patent Number (DO NOT ENTER BOTH numbers for the same property).***Patent Application Number(s)****Patent Number(s)**

4525448	4654547	4832761
4701573	4679010	4788509
4965218	4782032	4956308
4847212	4849376	4897563
4948752	4918493	4962050

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Execution Date
Month Day Year

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Execution Date
Month Day Year

Name (line 2)

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Name (line 1)

Name (line 2)

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Application Number(s) or Patent Number(s)

☐ Mark if additional numbers attached

Enter either the Patent Application Number or the Patent Number (DO NOT ENTER BOTH numbers for the same property).

Patent Application Number(s)

Patent Number(s)

5965935	5748056	5805043
5734189	4804635	4740823
4833512	4814836	4768073
4755858	4647878	4816967
4709300	5536666	

ASSIGNMENT OF PATENTS

WHEREAS, ITT INDUSTRIES, INC., an Indiana corporation and ITT MANUFACTURING ENTERPRISES, INC., an Indiana corporation (collectively, the "ASSIGNORS"), own certain domestic and foreign patents and patent applications (including, without limitation, all counterparts thereof in any other country and all reissues, divisions, continuations, continuations-in-part, renewals and extensions of the foregoing) based on inventions, discoveries, designs or writings purchased or developed primarily for the Business (as such term is defined in the Asset Purchase Agreement dated as of January 26, 2000 by and among Assignors M/A Com, Inc. and Assignee (as hereinafter defined) or any predecessor thereof (collectively, the "PATENTS"), including the right to claim the benefit of any priority dates or provisional applications relating thereto under any applicable laws, and any renewal, substitute or reissue thereof, for the full term for which the same may be granted, specifically including, without limitation, the patents and patent applications identified in Schedule A attached hereto;

WHEREAS, ASSIGNORS wish to confirm the assignment of their entire interests in such PATENTS to Tyco Electronics Logistics AG, a Swiss corporation (the "ASSIGNEE"), and ASSIGNEE wishes to confirm acceptance of the assignment of such PATENTS;

NOW, THEREFORE, TO ALL WHOM IT MAY CONCERN: Be it known that nunc pro tunc as of March 6, 2000 in consideration of one dollar (\$1.00) and for other good and valuable consideration, the receipt of which is hereby acknowledged, ASSIGNORS assign and transfer to ASSIGNEE all right, title and interest of ASSIGNORS in and to said PATENTS, including the right to seek and obtain for ASSIGNEE's own benefit injunctive relief, recovery of damages for any past and/or future infringements, including enhanced damages and/or attorney's fees for willful infringement.

ASSIGNORS further covenant that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would conflict with this assignment and sale.

ASSIGNORS further covenant that ASSIGNORS will, upon ASSIGNEE's request, promptly execute and deliver to ASSIGNEE or its legal representative any and all papers or instruments required to maintain and enforce said PATENTS that may be necessary or desirable to carry out the purposes hereof.

This Agreement may be executed simultaneously in two or more counterparts, each of which shall be deemed an original, but all of which together shall constitute one and the same instrument.

IN WITNESS, WHEREOF, ASSIGNORS and ASSIGNEE have caused this assignment to be signed on the dates set forth below.

ASSIGNORS

ITT INDUSTRIES, INC.

Date: MAY 26, 2000

By: [Signature]

Title: ASSISTANT SECRETARY

ITT MANUFACTURING ENTERPRISES, INC.

Date: 5/30/00

By: [Signature]

Title: President

ASSIGNEE

TYCO ELECTRONICS LOGISTICS AG

Date: 15.5.00

By: [Signature]
J. Frischbrecht

Title: Member of Management

ASSIGNEE

TYCO ELECTRONICS LOGISTICS AG

Date: 15.5.00

By: [Signature]
M. Weibel

Title: Member of Management

SCHEDULE A

PENDING APPLICATIONS		
Application No.	File Date	Title
08/672298	June 28, 1996	Gaas Negative Voltage Generator
09/391006	Sept. 7, 1999	MES/MIS PFT
09/168,146	Oct. 7, 1998	2.9 to 5.1 Volt Powered Inductor-Less Step Up DC to DC Converter
09/257,119	Feb. 25, 1999	Compact FET Design
09/249,352	Feb. 12, 1999	Low RF Loss Direct Die Attach Process and Apparatus
09/309,228	May 10, 1999	Single Drain Switch/Single NVG Operation of Dual Band MESFET Power Amplifiers
08/307,689	May 10, 1999	A Circuit For Simultaneous Temperature and Process Compensation For Use In RFIC Power Amplifier Control
09/374,567	Aug. 14, 1999	Size Reduction Method and Apparatus For Use In Semiconductor Circuits
09/307,913	May 10, 1999	Power Sensing Apparatus for Power Amplifiers
09/399,884	Sept. 21, 1999	Subresolution Alignment Mark Windows For Photolithography

ISSUED PATENTS		
Patent No.	Issue Date	Title
4525448	June 25, 1985	Sub-Half-Micron Gates
4654547	Mar. 31, 1987	Gallium As Buff/Comparator
4832761	May 23, 1989	GA AS MMIC Process
4701573	Oct. 20, 1987	Semiconductor Chip Housing
4679010	July 7, 1987	GAAS Isolators/Circulators
4788509	Nov. 29, 1988	Multifunction MMIC Chip
4965218	Oct. 23, 1990	Self-Aligned Gate Realignment Employing Planarizing Overetch
4782032	Nov. 1, 1988	Self-Aligned GAAS Device
4956308	Sept. 11, 1990	Self-Aligned FLD-Effect Trans
4847212	July 11, 1989	Self-Aligned Gate FET/Process with Self-limiting Undercut of an Implant Mask
4849376	July 18, 1989	Self-Aligned Refractory Gate Process
4849563	Jan. 30, 1990	N-Way MMIC Redundant Switch
4948752	Aug. 14, 1990	Making Sagets On Buffer Layer
4918493	April 17, 1990	Making Sagets On Buffer Layer
4962050	Oct. 9, 1990	GAAS Fet Manufacturing Process Employing Channel Confining Layers
4994868	Feb. 19, 1991	Heterojunction Confined Channel FET
5144405	Sept. 1, 1992	Temperature Compensation Apparatus
5172072	Dec. 15, 1992	High Frequency Harmonic Injection Power Amplifier

ISSUED PATENTS		
Patent No.	Issue Date	Title
5404109	Apr. 4, 1995	Method and Apparatus For Testing Circuits Containing Active Devices
5278529	Jan. 11, 1994	Broadband Microstrip Filter Apparatus Having Inteleaved Resonator Sections
5281929	Jan 25, 1994	Microstrip Twisted Broadside Coupler Apparatus
5262740	Nov. 16, 1993	Microstrip Transformer Apparatus
5406234	April 11, 1995	Tunable Microwave Filter Apparatus
5228967	July 20, 1993	Apparatus and Method For Electroplating Wafers
5273642	Dec. 28, 1993	Apparatus and Method For Electroplating Wafers
5309006	May 3, 1994	FET Crossbar Switch Device Particularly Useful For Microwave Applications
5180998	Jan. 19, 1993	Switched Transmission Line Phase Shifter Apparatus Employing Multiple FETS
5168242	Dec. 1, 1992	Active-Type Broadband Power Divider
5313175	May 17, 1994	Broadband Tight Coupled Microstrip Line Structures
5338950	Aug. 16, 1994	Multiple Port Thin Film Capacitor
5428327	June 27, 1995	Improved Microwave Feedthrough Apparatus
4581076	April 8, 1986	Selectively Implanting GAAS Semiconductor Substrates Through A Metallic Layer
5428840	June 27, 1995	Monolithic Double Balanced Microstrip Mixer With Flat Conversion Loss
5374899	Dec. 20, 1994	Self Biased Power Amplifier Employing FETs
5420527	May 30, 1995	Temperature And Supply Insensitive TTL Or CMOS To 0/-5 V Translator
5565696	Oct. 15, 1996	Planar Ion-Implanted GaAs MESFETs With Improved Open-Channel Burn-Out Characteristics
5536666	July 16, 1996	Planar Ion-Implanted GeAs MESFETs Improved Open-Channel Burn-Out Characteristics
6005267	Dec. 21, 1999	MES/MIS FET
5767721	June 16, 1998	Switch Circuit For FET Devices Having Negative Threshold Voltages Which Utilize A Positive Voltage Only
5753968	May 19, 1998	Low Loss Ridged Microstrip Line For Monolithic Microwave MMIC Applications
5965935	Oct. 12, 1999	Low Loss Ridged Microstrip Line For Monolithic Microwave MMIC Applications
5748056	May 5, 1998	Compact 90 Degree Monolithic GAAS Coupler For Wireless Applications
5805043	Sept. 8, 1998	High Q Compact Inductors For Monolithic Integrated Circuit Applications
5734189	March 31, 1998	Low Parasitic Source Inductance Field-Effect Transistor
4804635	Feb. 14, 1989	GASFET
4740823	April 26, 1988	Photo-Detectors
4833512	May 23, 1989	Photo Detector

ISSUED PATENTS		
Patent No.	Issue Date	Title
4814836	March 21, 1989	Photoconductor
4768073	Aug. 30, 1988	VLSI
4755858	July 5, 1988	Field Effect Transistor
4647878	March 3, 1987	Coax Directional Coupler
4816967	March 28, 1989	HFIC Connector
4709300	Nov. 24, 1987	Jumper For Semi Assembly