

07-21-2000

Form-PTO-1595  
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TRANSMIT



TION

Atty. Docket: 2666.44

101410790

To the Commissioner of Patents and T

... original documents or copy thereof.

1. Name of conveying party(ies):

MARVELL SEMICONDUCTOR, INC.

Additional name(s) of conveying party(ies) attached?

☐ Yes☒ No

3. Nature of conveyance:

☒ Assignment☐ Merger☐ Security Agreement☐ Change of Name☐ Other

Execution Date: June 22, 2000

2. Name and address of receiving party(ies):

Name: MARVELL TECHNOLOGY GROUP, LTD.

Foreign Address: Clarendon House, 2 Church Street

P.O. Box 1022

Hamilton HM DX, Bermuda

Additional name(s) & address(es) attached? ☐ Yes ☒ No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: June 22, 2000

A. Patent Application Number:

Filing Date:

B. Title of Invention:

METHOD AND APPARATUS FOR DETERMINING ERROR  
CORRECTION CODE FAILURE RATE FOR ITERATIVE  
DECODING ALGORITHMS

Additional numbers attached?

☐ Yes☒ No5. Name and address of party to whom correspondence  
concerning document should be mailed:

Name: Fitzpatrick, Cella, Harper &amp; Scinto

30 Rockefeller Plaza

New York, New York 10112-3801

Telephone No.: (212) 218-2100

Facsimile No.: (212) 218-2200

6. Number of applications and patents involved:

One

7. Total fee (37 CFR 3.41): . . . \$ 40.00

☒ Enclosed☐ Authorized to be charged to deposit account

8. Deposit account number (for deficiency or excess)

06-1205

(Attach duplicate copy of this page if paying by deposit account):

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9. Statement and signature.

*To the best of my knowledge and belief, the foregoing information is true and the attached is the original document or is a true copy of the original document.*

Richard P. Bauer, Reg. No. 31,588

Name of Person Signing

Signature

June 26, 2000

Date

Total number of pages including cover sheet, attachments, and documents: 4

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PATENT  
REEL: 010924 FRAME: 0753

# ASSIGNMENT

## Corporate

Marvell Semiconductor, Inc., a California corporation, having a principal place of business at 645 Almanor Avenue, Sunnyvale, CA 94086 (hereafter the "Assignor"), is the owner by respective Assignment of United States Patent Application identified below (hereafter sometimes called the "Patent Application").

Marvell Technology Group, LTD, a corporation of Bermuda, having a place of business at Clarendon House, 2 Church Street, P.O. Box 1022, Hamilton HM DX, Bermuda (hereafter the "Assignee"), desires to acquire all of the rights to the Patent Application and all inventions described and claimed therein.

THEREFORE, in consideration of the sum of One Dollar (\$1.00) or equivalent and other good and valuable consideration paid to it, Assignor hereby sells and assigns to the Assignee the entire right, title and interest in and to the Patent Application identified below, and all inventions described and claimed therein, in any and all Letters Patent therefor, and in any and all reissues, extensions, renewals, reexaminations, divisions and continuations of such applications or Letters Patent to the full end of the term or terms for which such Letters Patent issue, such entire right, title and interest to be held and enjoyed by the above-named Assignee the same as they would have been held and enjoyed by the Assignor had this Assignment and sale not been made.

<u>Marvell Semiconductor, Inc. Case No.</u>	<u>U. S. Serial No.</u>	<u>U. S. Filing Date</u>	<u>Title</u>
MP0044		filed herewith	<b>Method and Apparatus for Determining Error Correction Code Failure Rate for Iterative Decoding Algorithms</b>

By its undersigned representative, the Assignor agrees

- a. to execute all papers necessary in connection with the Patent Application and any continuing, divisional, reissue, reexamination or corresponding application thereof and also to execute separate Assignment in connection with such application as the Assignee may deem necessary or expedient;
- b. to execute all papers necessary in connection with any interference which may be declared concerning the Patent Application or any continuation, division, reissue or reexamination thereof and to cooperate with the Assignee in every way possible in obtaining evidence and going forward with such interference; and

c. to perform all affirmative acts which may be necessary to obtain a grant of a valid United States patent to the Assignee on any of the Patent Application and on any continuation, division, reissue or reexamination of any of the Patent Application.

The Assignor hereby covenants that it has the full right to convey the entire interest herein assigned, and that it has not executed, and will not execute, any agreement in conflict therewith.

Marvell Semiconductor, Inc., a California corporation, certifies that it is the Assignee of the entire right, title and interest in the Patent Application identified above by virtue of Assignment from the inventors of the Patent Application identified above. Assignment to be recorded in the Patent and Trademark Office as identified below.

<u>Marvell Semiconductor,</u> <u>Inc.</u> <u>Case No.</u>	<u>U. S. Serial</u> <u>No.</u>	<u>Assignment Execution Date</u>
MP0044	filed herewith	6/22/2000 <b>Method and Apparatus for Determining Error Correction Code Failure Rate for Iterative Decoding Algorithms</b>

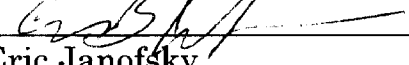
The undersigned has reviewed the documents in the Patent Application identified above and, to the best of undersigned's knowledge and belief, title is in the Assignor identified above.

The undersigned is empowered to sign this certificate on behalf of the Assignor.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

IN WITNESS WHEREOF, executed by the Assignor's undersigned representative  
on the date following the undersigned's name.

Marvell Semiconductor, Inc.

By:   
Eric Janofsky

General Patent Counsel

Title

Date 6/22/2000