FORM PTO-1596

U.S. DEPARTMENT OF COMMERCE

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DOCKET NO.:4459-029 101416		
To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereto:		
1. Name of conveying party(ies):	2. Name and address of receiving party(ies):	
Chun Hung LIN	Name: Advanced Semiconductor Engineering, Inc.	
	Internal Address:	
	michiai Audiess.	
Additional name(s) of conveying party(ies) attached?	, in the second	
3. Nature of conveyance:		
X Assignment _ Merger	Street Address:	
_ Security Agreement _ Change of Name	No.26, Chin 3 <sup>rd</sup> Road, Nantze Export Processing Zone, Kaoshiung	
Other	City: State or Area: Zip/Postal Code: Country: TAIWAN, R.O.C.	
Execution Date: March 29 2000	Additional name(s) & address(es) attached?	
4. Application number(s) or patent number(s):	I	
If the document is being filed together with a new application, the execution	1	
A. Patent Application No(s).	B. Patent No(s). 09 615836	
	09 013 130	
Additional numbers attached?		
<ol><li>Name and address of party to whom correspondence concerning document should be mailed:</li></ol>	6. Total number of applications and patents involved: 1	
Name: LOWE HAUPTMAN GOPSTEIN GILMAN & BERNER	7. Total fee (37 CFR 3.41) \$40.00	
Internal Address:	Is enclosed	
Street Address: 1700 Diagonal Road, Suite 310 City: Alexandria State: VA ZIP: 22314	8. Deposit account number: 07-1337	
DO NOT USE THIS SPACE		
9. Statement and signature.  To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.		
Benjamin J. Hauptman Registration No. 29,310		
Signature	July 13, 2000 Date	
•	Total number of pages comprising cover sheet: 1	
CMB No. 0851-0011 (exp. 4/94)		

## **ASSIGNMENT**

In consideration of the premises and other good and valuable consideration in hand paid, the receipt and sufficiency of which is hereby acknowledged, the undersigned,

(1)_	Chun Hung LIN (4)	
(2)_ (3)_	(5) (6)	
who	have made a certain new and useful invention, herel	by sell, assign and transfer unto
Adv	anced Semiconductor Engineering, Inc.	
Unit	successors and assigns (hereinafter designated "ASS red States of America as defined in 35 U.S.C. 100 in t	he invention entitled
MUI	LTICHIP MODULE HAVING STACKED CHIP ARRAI	NGEMENT
(a)	for which an application for United States Letters Pa by United States Serial No; o	
(b)	for which an application for United States Letters Pa	atent was executed on <u>May 29, 2000</u> ,
Trac exte und- inter	the undersigned hereby authorize and request the Undersigned hereby authorize and request the Undersity to issue any and all United States Letters Palensions, divisions, reissues, continuations, or continuater the International Convention for the Protection of Interest as ASSIGNEE, its successors, assigns and legal record in said application shall hereafter actions.	tent which may be granted therefor and any and all ations-in-part thereof, and the right to all benefits industrial Property to the said ASSIGNEE, for its representatives; the undersigned agree that the on behalf of said ASSIGNEE;
Stat furth or re and	AND the undersigned hereby agree to transfer a like sing application for and obtaining original, divisional, rese, upon request of the said ASSIGNEE, its successor remuneration, in and to any improvements, and applicated to the said invention; and to execute any paper legal representatives, deemed essential to ASSIGNE beby transferred.	eissued or extended Letters Patent of the United ors, assigns and legal representatives, and without oplications for patent based thereon, growing out of s by the said ASSIGNEE, its successors, assigns
SIG	NED on the date indicated aside our signatures:	
	INVENTORS	DATE SIGNED
1)	Signature: Chun Hung LIN ame: Chun Hung LIN	May 29, 2000
,	Signature: ame:	
	Signature:	

USPOA-ASE-P00-049.doc

**RECORDED: 07/13/2000** 

PATENT REEL: 010943 FRAME: 0506