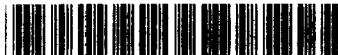


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Execution Date: 5/5/00

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Name: Chartered Semiconductor Manufacturing Ltd.

Internal Address:

Street Address:

60 Woodlands Industrial Park D
Street 2City: Singapore Zip: 738406
Country: SingaporeAdditional name(s) & address(es) attached? Yes ☒ No

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If this document is being filed together with a new application, the execution date of the application is: 5/5, 5/10 & 6/2/00A. Patent Application No(s)B. Patent No.(s)Additional numbers attached? ☐ Yes ☒ No

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In consideration of the payment by ASSIGNEE to ASSIGNOR of the sum of One Dollar (\$1.00), the receipt of which is hereby acknowledged, and for other good and valuable consideration,

ASSIGNOR:

CHER LIANG CHA, RANDALL
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ASSIGNEE:

Chartered Semiconductor Manufacturing Ltd.
60 Woodlands Industrial Park D
Street 2
Singapore 738406
Singapore
Nationality

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(complete one of the following)

- ☒ the entire right, title and interest
☐ an undivided _____ percent (____%) interest for the United States and its territorial possessions
(check the following box if foreign rights are also to be assigned)
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in and to any and all improvements which are disclosed in the invention entitled:

A Method Of Fabricating CMOS Devices Featuring Dual Gate Structures And A High Dielectric Constant Gate Insulator Layer

and which is found in

- (a) ☐ U.S. patent application executed on even date herewith
- (b) ☒ U.S. patent application executed on 5/5, 5/10 & 10/2/00
- (c) ☐ U.S. application serial no. _____ filed on _____
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Type or print the name of the above person authorized to sign on
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___ Notarization of Legalization Page Added.