

08-18-2000

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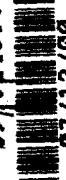
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To the Honorable Commissioner of Patents and Trademarks, please return the enclosed original documents or copy thereof.

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2. Name and address of receiving party(ies):

TING CHEONG ANG, SHYUE FONG QUEK, SANG
YEE LOONG, JUN SONGName: Chartered Semiconductor Manufacturing Ltd.

Internal Address:

Street Address:

60 Woodlands Industrial Park D

Street 2

City: Singapore

Zip: 738406

Country: Singapore

Additional name(s) or conveying party(ies) attached? ☐ Yes ☒ No

3. Nature of conveyance

☒ Assignment☐ Merger☐ Security Agreement☐ Change of Name☐ other _____Execution Date: 6/27/00Additional name(s) & address(es) attached? Yes ☐ No ☒

4. Application number(s) or patent number(s):

09/614558

If this document is being filed together with a new application, the execution date of the application is: 6/27/00A. Patent Application No(s)B. Patent No.(s)Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: GEORGE O. SAILE

Internal Address: _____

Street Address: 20 MCINTOSH DRIVECity: POUGHKEEPSIE State: NY ZIP: 126036. Total no. of applications and patents involved: 17. Total fee (37 CFR 3.41): \$ 40.00☐ Enclosed☒ Authorized to be charged to deposit acct.8. Deposit account number: 19-0033

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Name of Person Signing

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Signature

July 10, 2000

Date

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For ☒ Application or ☐ U.S. Patent

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ASSIGNMENT OF INVENTION

In consideration of the payment by ASSIGNEE to ASSIGNOR of the sum of One Dollar (\$1.00), the receipt of which is hereby acknowledged, and for other good and valuable consideration,

ASSIGNOR:

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Nationality

ASSIGNEE:

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Singapore
Nationality

and the successors, assigns and legal representatives of the ASSIGNEE

(complete one of the following)

- ☒ the entire right, title and interest
- ☐ an undivided _____ percent (____%) interest for the United States and its territorial possessions
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in and to any and all improvements which are disclosed in the invention entitled:

Method Of Forming Pid Protection Diode For Soi Wafer

and which is found in

- (a) ☒ U.S. patent application executed on even date herewith
(b) ☐ U.S. patent application executed on _____
(c) ☐ U.S. application serial no. _____ filed on _____
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IN WITNESS WHEREOF, I / We have signed this

27 June 2000 (Date of signing)

WARNING Date of signing must be the same as the date of execution of the application if item (a) was checked above.

[Signature]
Signature of ASSIGNOR(S)
[Signature]
[Signature]
[Signature]

If ASSIGNOR is a legal entity complete the following information

Type or print the name of the above person authorized to sign on behalf of ASSIGNOR

Title

Note: No witnessing, notarization or legalization is necessary, if the assignment is notarized or legalized then it will only be prima facie evidence (35 USC 261). Use next page if notarization is desired.

____ Notarization of Legalization Page Added.