



ASSISTANT

101462300

FR SHEET

8-30-00

To: Honorable Commissioner of Patents and Trademarks:

Please record the attached original document(s) or copy thereof.

1. Name and address of conveying party(ies)
  - a) Name: TSMC-Acer Semiconductor Manufacturing Corporation
  - b) Address: No. 6, Creation Road 2, Hsinchu Science-Based Industrial Park, Taiwan, R.O.C.
  
2. Name and address of receiving party(ies):
  - a) Name: Taiwan Semiconductor Manufacturing Company Ltd.
  - b) Address: No. 9, Creation Road 1, Hsinchu Science-Based Industrial Park, Taiwan, R.O.C.
  
3. Nature of conveyance
 

<input checked="" type="checkbox"/> Assignment	<input type="checkbox"/> Merger
<input type="checkbox"/> Security Agreement	<input type="checkbox"/> Change of Name
<input type="checkbox"/> Other _____	<input type="checkbox"/> License Agreement

Execution Date: [Fill-in execution date of assignment:] June 16, 2000

4. Application Number(s) or Patent Number(s) as follows:
  - (a) Application No.: 09/597,832  
Title of Application: METHOD FOR FABRICATING POLY-SPACERS
  - (b) Application No.: 09/592,175  
Title of Application: GATED DIODE FOR ESD PROTECTION

5. Please send all correspondence concerning this (these) documents to:

**Albert S. Penilla, Esq.**  
 MARTINE PENILLA & KIM, LLP  
 710 Lakeway Drive, Suite 170  
 Sunnyvale, CA 94086  
 Tel. No. (408) 749-6900  
 Fax No.: (408) 749-6901

6. Total number of applications and patents involved: 2 [Fill-in No.]

7. Total fee (37 CFR 3.41): \$ 80.00 [\$40 for each application]

- Enclosed  
 Authorized to be charged to Deposit Account No. 50-0805  
 (Order No. JLING000)

09/18/2000 MTHAI1 00000481 09597832


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80.00 DP

Attorney Docket No. JLING000

8. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Date: 8/23/00

  
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Albert S. Penilla, Esq.  
Registration No. 39,487

# ASSIGNMENT OF PATENT APPLICATIONS

TSMC-Acer Semiconductor Manufacturing Corporation, ("TSMC-Acer"), a corporation having a place of business at No. 6, Creation Road 2, Hsinchu Science-Based Industrial Park, Taiwan, R.O.C., certifies that to the best of assignee's knowledge and belief it is the assignee of United States Letters Patent Application Nos.:

- (a) Application No.: 09/597,832  
Title of Application: METHOD FOR FABRICATING POLY-SPACERS
- (b) Application No.: 09/592,175  
Title of Application: GATED DIODE FOR ESD PROTECTION

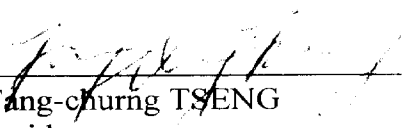
TSMC-Acer wishes to assign the above-referenced Patent Application Nos. to Taiwan Semiconductor Manufacturing Company Ltd. ("ASSIGNEE"). Accordingly, TSMC-Acer hereby:

- 1) Assigns and transfers to ASSIGNEE the entire right title and interest in any and all improvements and inventions disclosed in, application(s) based upon, and Patent(s) (including foreign patents) granted upon the information which is disclosed in the above-referenced Patent Application Nos.
- 2) Authorize and request the Commissioner of Patents to issue any and all Letters Patents resulting from said applications or any division(s), continuation(s), substitutes(s) or reissue(s) thereof to ASSIGNEE.

Signed on the date(s) indicated beside my (our) signature(s).

Dated: 8/30/2000, 2000.

TSMC-ACER SEMICONDUCTOR MANUFACTURING CORPORATION ("TSMC-Acer")

By:   
Name: Fang-chung TSENG  
Title: President

NOTARY

Signature \_\_\_\_\_  
(SEAL)