FORM PTO-1619A Expires 06/30/99

OMB 0651-0027

10-17-2000



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U.S. Department of Commerce Patent and Trademark Office **PATENT**

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Submission Type	Conveyance Type	e	anness gegintlight tiller skilmskile, ett hinnestille vergentlige i til kinne stelle skilmske et vilge have untgehave den bet
X New	Assignment	X Security Ag	reement
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Name (line 1) Amkor Technology, Inc.			April 28, 2000
Name (line 2) Second Party			Execution Date Month Day Year
Name (line 1)			
Name (line 2)			
Receiving Party	Mark if	additional names of re	ceiving parties attached
Name (line 1) Guardian Assets, Inc.			If document to be record an assignment and the receiving party is not dor
Name (line 2)			in the United States, an appointment of a domest representative is attache
Address (line 1) Goshen Corporate Park			(Designation must be a separate document from Assignment.)
Address (line 2) 1345 Enterprise Drive			
	PA / USA	TV.	19380 Zip Code
Address (line 3) West Chester	State/Count		<u> </u>
Address (line 3) West Chester City Domestic Representative Name and Ad	State/Count Idress Enter fo	r the first Receiving Pa	rty only.
City			rty only.
Domestic Representative Name and Ad			rty only.
Domestic Representative Name and Ad			rty only.
Domestic Representative Name and Ad Name Shearman & Sterling Address (line 1) 599 Lexington Avenue			rty only.
City Domestic Representative Name and Ad Name Shearman & Sterling Address (line 1) 599 Lexington Avenue Address (line 2) New York, NY 10022			rty only.

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PATENT

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REEL: 011122 FRAME: 0773

FORM PTO- Expires 06/30/99 OMB 0651-0027	1619B	Page 2	U.S. Department of Commerce Patent and Trademark Office PATENT
Corresponde	nt Name and Address	Area Code and Telephone Numb	212 848-4000
Name	Antoinette E. Baker		
Address (line 1)	Sherman & Sterling		
Address (line 2)	599 Lexington Avenue		
Address (line 3)	New York, NY 10022		
Address (line 4)			
Pages	Enter the total number of page including any attachments.	es of the attached conveyance d	ocument # 6
	Number(s) or Patent Nur		Mark if additional numbers attached
	e Patent Application Number or the atent Application Number(s)	e Patent Number (DO NOT ENTER B	OTH numbers for the same property). Patent Number(s)
See attached	atent Application (diliber(s)	See attached	
	7		
If this documen application was	nt is being filed together with a <u>new</u> s signed by the first named executi	Patent Application, enter the date the ing inventor.	he patent Month Day Year
Patent Coope	eration Treaty (PCT)	PCT PCT	PCT
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	nly if a U.S. Application Number as not been assigned.	PCT PCT	PCT
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	Antoinette E. Baker		
Name	e of Person Signing	Signature	Date

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Patents Granted Amkor Technology, Inc.

٦-							Assig	nees
#	Patent Number	Filed	Issued	Title	Inventor(s)	Amkor		Other
				Method of producing a strip of lead frames for				
1	5,183,724	12/18/90		integrated circuit dies in a continuous system	Johnson	Х		
				Slitter machine for use in manufacturing				
2	5,269,210	9/23/91		semiconductor devices	Johnson	X		
1		Į.		Method of and apparatus for producing a strip				1
_				of lead frames for integrated circuit dies in a				1
3	5,305,043	8/27/92	04/19/94	continuous system	Johnson	X		
				Method for forming plastic molded package				1
4	5,328,870	11/9/92	07/12/94	with heat sink for integrated circuit devices	Marrs	×		
_		*** ***	1011101	7	Marrs	,		
5	5,355,283	4/14/93	10/11/94	Ball grid array with via interconnection	Hirakawa	X		Teijin Limited
_	E 270 000	2/20/02	04/03/05	Method for forming an integrated circuit	Marrs			
6	5,378,869	3/26/93	01/03/95	package with via interconnection Packaged integrated circuit including heat	Hirakawa	X		Teijin Limited
7	E 204 042	4/10/04	01/10/05		Lerner			
	5,381,042	4/19/94	01/10/95	slug having an exposed surface Plastic molded package with heat sink for	Razu	×		ļ
	E 455 400	11/15/02	10/02/05		140		}	
8	5,455,462	11/15/93	10/03/83	integrated circuit devices Method for interconnection of integrated circuit	Marrs	X	}	
9	5 479 007	5/11/94	12/26/95	chip and substrate	Marra		})
3	5,478,007	3/11/94	12/20/30	Method for applying flux to ball grid array	Marrs Glenn		 	
10	5,482,736	8/4/94	01/09/96		Hollaway	1 .	Í	{
10	3,402,130	0/4/54	01/05/50	Method for forming a semiconductor device	nollaway	×	 	
		l	1	having a thermal dissipator and		İ	ł	1
11	5,482,898	3/27/95	01/00/06	electromagnetic shielding	Marrs		ł	1
 ' '	5,462,096	3/2//35	0 1/05/50	Integrated circuit package with via	Marrs	×	 	
12	5,483,100	6/2/92	01/00/06	interconnections formed in a substrate	Tadashi		ł	Teijin Limited
	3,405,100	<u> </u>	0 1103100	Semiconductor device having a thermal	Tadasiii	×	 	1 Cijiii Cirriited
13	5,485,037	3/27/95	01/16/96	dissipator and electromagnetic shielding	Marrs	×	Ì	1 1
	3,403,037	0/21/00	011100	Copper oxide-filled polymer die attach	Widi i S	 -^-	 	
1	1	1	i	adhesive composition for semiconductor	i		İ	1 1
14	5,582,772	6/7/95	12/10/96	package	Kwak	×	×	1
H	0,002,112	1		Ball grid array integrated circuit package with	Marrs	 ^	 	<u> </u>
15	5,583,378	5/16/94	12/10/96	thermal conductor	Molnar	×	Ì	
<u> </u>	1		1	Plastic packaged integrated circuit with heat	Glenn		1	
16	5,596,485	3/16/95	01/21/97	spreader	Holloway	×	1	
				Semiconductor package with integral heat	Shin	1]
17	5,629,561	12/12/95	05/13/97	dissipator	Do	X	×]
					Freyman	1		
1		1			Briar			
			[Mold runner removal from a substrate-based	Heo	ĺ		
18	5,635,671	3/16/94	06/03/97	packaged electronic device	Shim	×	×	[
			[Method for molding of integrated circuit		1		
19	5,637,273	10/11/96	06/10/97	package	Goo	x	X	<u> </u>
1	ļ	1		Method and circuit board structure for leveling		1		
1	}			solder balls in ball grid array semiconductor])
20	5,641,946	1/18/96	06/24/97	packages	Shim	×	X	<u> </u>
				Heat spreader suitable for use in		1		
				semiconductor packages having different pad]	
21	5,641,987	6/7/95	06/24/97	sizes	Lee	×	×	
1]		McMillan]	1]
1					Maslakow		1	1
22	5,650,593	2/6/95	07/22/97	Thermally enhanced chip carrier package	Castro	×	ļ	
		1		🕯 sa sa sa sa sa sa sa sa sa sa sa sa sa	Yoo	1		
1	1		C		Y00	1.	1	1
Ì	1	1	1	Chia mounting plate agest self-self-self-self-self-self-self-self-	Lee			
100	E-664 226	40/40/05	000000	Chip mounting plate construction of lead frame				
23	5,661,338	12/12/95	08/26/97	for semiconductor package	Han	×	×_	
1		1		Interdigitated wirehead programments found	Glenn			
24	5,672,909	7/29/96	09/30/97	Interdigitated wirebond programmable fixed voltage planes	Molnar		4.	-
129	3,012,909	1129190	03/30/9/	Packaged semiconductor die including heat	Hollaway	×	+	
25	5,701,034	5/3/94	12/23/97		Marrs	1.		.
1	-, 0,.01,004	1 0.0.04	1	Territ interioration	Livialia	1 x		

							Assignees		
L	#	Patent Number	Filed	Issued	Title	inventor(s)	Amkor	Anam	Other
- [ĺ					Shim			*
1	26	5,708,567	11/13/96	01/13/98	ring-type heat sink	Heo	×	×	

Patents Granted Amkor Technology, Inc.

				Method for checking a wire bond of a	Heo			
27	5,712,570	9/19/95	01/27/98	semiconductor package	Youm	X	х	
1				Method of making a packaged semiconductor	1			
28	5,722,161	5/1/96	03/03/98	die including heat sink with locking feature	Marrs	X		
- [,	[Semiconductor lead frame having connection				
29	5,723,899	8/29/95	03/03/98	bar and guide rings	Shin	x	x	
		{	•	Ball grid array semiconductor package with				
		i		improved dissipation and dehumidification	Shim		1	
30	5,729,432	1/18/96	03/17/98	effect	Heo	_x	x	
		i			Seo			
31	5,740,956	12/12/95	04/21/98	Bonding method for semiconductor chips	Jang	x	x	ļ
1				Power drawing circuit for two-wire switching				
32	5,760,498	8/6/96	06/02/98	unit	Park		x	
		1		Printed circuit board having epoxy barrier				
		!		around a throughout slot and ball grid array	Ha I		}	
33	5,767,446	10/24/96	06/16/98	semiconductor package	Heo	x	x	
				Integrated circuit chip to substrate				
34	5,795,818	12/6/96	08/18/98	interconnection and method	Marrs	x		
	<u>-</u>	1			Glenn			
		1			Hollaway		(
35	5,796,163	5/23/97	08/18/98	Solder ball joint	Panczak	~	}	Į.
┝┷┼	0,,00,100	3,200	30,10,30	Method For Fabricating a Heat Sink-Integrated	anuzan	X	<u> </u>	
36	5,807,768	9/4/96	09/15/98	Semiconductor Package	Shin			
301	3,007,700	914/90	09/13/90	Semiconductor Fackage		×	X	
27	E 907 000	44/44/04	40/07/00	Homegeneous Chie Carries Basks as	McMillian			
37	5,827,999	11/14/94	10/27/98	Homogeneous Chip Carrier Package	Maslakow	X		
_			,	Socket Assembly for Integrated Circuit Chip	McMillian			
38	5,829,988	11/14/96	11/03/98	Carrier Packager	Maslakow	X		
				l			į į	•
39	5,838,951	8/28/96	11/17//98	Wafer Map Conversion Method	Song	X	X	
		1		Unit Printed Circuit Board Carrier Frame For	ļ ;]	
				Ball Grid Array Semiconductor Packages And			1	
				Method For Fabricating Ball Grid Array			}	
40	5,854,741	5/17/96	12/29/98	Semiconductor Packages Using The Same	Shim, Heo	X	x	
1 1		1		ĺ	Shin, Han,			
1 1			[Grid Array Type Lead Frame And Lead End	Yoon,			
		\		Grid Array Semiconductor Package Employing	Kwak	['	
41	5,866,939	12/31/96	02/02/99	The Same	Han	×	x	
42	5,858,149	11/14/96	01/12/99	Process For Bonding Semiconductor Chip	Seo, Song	×	x	
					Shim,		1	
			[Flexible Circuit Board For Ball Grid Array	Young	1	1	
43	5,864,470	6/25/97	01/26/99	Semiconductor Package	Darveaux	×	×	
				Semiconductor Package Including Heat Sink	1	 ^	 	
		1		With Layered Conductive Plate And Non-	1	1		
44	5,854,511	11/14/96	12/29/98	Conductive Tape Bonding To Leads	Shin, Lee	×	x	
 	0,007,011	11400		Tanada Topo Donaing To Leads	Freyman,	 ^	 ^	
					Maxcy,	ł	{	
45	5,852,870	4/24/96	12/29/98	 Method of Making a Grid Array Assembly	Brair		1	
1	3,032,070	4124180	1223/30	Carrier Strip and Molded Flex Circuit Ball Grid		×	 	
46		4/24/96	01/12/99	Array	Freyman,	l		
174	<u> </u>	4124190	01/12/00	Mounting For A Semiconductor Integrated	Darveaux	<u> </u>	 	
127	5 967 260	0/0/07	03/03/00	Circuit Device	Class	.	1	
47	5,867,368	9/9/97	02/02/99	Solder Ball Land Metal Structure of Ball Grid	Glenn,	×		
10	E 970 000	414107	02/46/02		1.	1	1	
48	5,872,399	4/1/97	02/16/99	Semiconductor Package	Lee	×	x	
1	F 050 545	10/51/00	04/40/0=	Semiconductor Package and Method for	Heo,	1	1	
49	5,858,815	12/11/96.	01/12/99	Fabricating the Same	Han	X	×	
1_1				A Method for Manufacturing an Alumina-	1 .	[
50	5,894,008	10/16/97	04/13/99	Silicon Carbide Nanocomposite	Na	x	x	
الحت	0,004,000	1 10/10/3/	1 07/10/38	Tourous caroline Harrocomposite	JING	<u> </u>	<u> </u>	L

REEL: 011122 FRAME: 0776

Patents Granted Amkor Technology, Inc.

#1	Patent Number	Filed	Issued	Title	Inventor(s)	Amkor	Anam	Other
				Method for Reproducing Printed Circuit Boards				
- 1		1		For Semiconductor Packages Including Poor			1	
- 1	{	1		Quality Printed Circuit Board Units and			1	
İ	1	[Methods for Fabricating Semiconductor	,			
- 1	į	I		Packages Using the Reproduced Printed	Ha, Heo,		1 1	
51	5,897,334	10/15/97		Circuit Boards	Han	×	x	
쒸	3,637,334	10/13/3/	04/2/199	Method of Producing BGA Semcionductor	11011		 	
- {			}	Packages Using Metal Carrier Frame & BGA		1		
52	5,905,633	12/31/96	05/18/00	Produced by Such Method	Shim, Heo	×	×	
22	3,903,033	12/3/190	05/10/33	Method of Forming Chip Bump Chip Scale	Child, Heb	 - ^ -	 ^	
- 1				Semiconductor Package, Such Package and		ì	1 1	
	E 000 047	2/7/07	06/01/99	Chip Bump	Heo	1	x	
53	5,908,317	3/7/97	06/01/99	Semiconductor Chip Scale Package and	rieo	×	^	
- 4	E 04E 460	40.002.006	000000	Method of Producing Such	Heo			
54	5,915,169	12/23/96			Glenn	×	×	
55	5,939,784	9/9/97	08/17/99	Standing Acoustical Wave Package Method of Making An Intgrated Circuit	Panczak.	×	 	-
	F 050 074	F (00 (00	00/07/00		Glenn	Ì	ł	
56	5,950,074	5/26/98	09/07/99	Package	Glenn	}	 	
				Mounting Having An Aperture Cover With		ł		
57	5,949,655	7/17/98	09/07/99	Adhesive Locking Features For Flip Chip	Glenn	 	 	
		l	}		Shim,	ł	1	}
		1	}	Ball Grid Array Semiconductor Package With	Darveaux,	1]	Ì
58	5,953,589	8/20/97	09/14/99	Solder Balls Fused On Printed Circuit Board	Glenn	×	X	
		1	1	Integrated circuit package employing a	l	1	1	1
	Į.	ł	ļ	transparent encapsulant and a method of	ł	}	ł	l
59		9/9/97	10/05/99	making the package	Glenn	X	<u> </u>	
60	5,971,734	9/19/97	10/26/99	Mold for ball grid array semiconductor	YY Moon	X	×	<u> </u>
	([1	Semiconductor Package and Assembly for	YW Heo	1	1	(
61	5,977,624	1/16/98	11/02/99	Fabricating the Same	BJ Han	×	<u> </u>	<u> </u>
	{		1		Glenn,	1	[(
	1		[Panczak	{	İ	1
62	5,981,314	10/31/96	11/09/99	Near Chip Size Integrated Circuit Package	Hollaway	<u> </u>	1	
				Printed circuit board for ball grid array	1	1	1	1
		1	1	semiconductor package and method for	1	į.	1	ĺ
	(1		molding ball grid array semiconductor package	:1	1	1	1
63	5,981,873	5/24/99	11/09/99	using the same	YW Heo	(x	x	Ĺ
				Method of making a molded flex circuit ball	Freyman,			
64	5,985,695	8/28/98	11/16/99	grid array	Darveaux	×		1
		1		Semiconductor Package having light, thin,		1	[
65	5,986,334	10/2/97	11/17/99	simple, and compact structure	SG Lee	X	x	1
66		12/30/98		Method for fabricating an LDD MOS transistor	Park	×	×	
			1	Method of manufacturing ball grid array				
67	6,020,218	1/26/98	02/01/00	semiconductor package	Shim, Ha	×	×	
	1	1	1	Method for marking poor quality printed circuit				1
j		1		board units of printed circuit board strip for	1	1	1]
68	6,021,563	6/25/97	02/08/00	1	Shim, Hed	. x	x	1
اٽ	1 - 5,02 - 1,000	1	1	A microelectronic device package having a	1	1	1	1
1	1	1	1	heat sink structure for increasing the thermal	1	}	1	}
69	6,028,354	10/14/97	02/22/00		Hoffman	×	1	
۳	0,020,004	1 .0.14.31	1	The state of the s	Glenn.	 ^-	+	1
l		1		Method of Making an Integrated Circuit	Panczak	1	1	
70	6,034,429	4/18/97	03/07/00	Package	Hollaway	x	1	
	/ U,UUM, TZS	1 7/10/37	1 00.07700	1, 20,	i			

This INTELLECTUAL PROPERTY SECURITY AGREEMENT (as amended, amended and restated, supplemented or otherwise modified from time to time, the "IP Security Agreement") dated April 28, 2000, is made by the Persons listed on the signature pages hereof (collectively, the "Grantors") in favor of Société Générale ("SG"), as collateral agent (the "Collateral Agent") for the Secured Parties (as defined in the Credit Agreement referred to below).

WHEREAS, Amkor Technology, Inc. (the "Borrower"), a Delaware corporation, has entered into a Credit Agreement dated as of April 28, 2000 (as amended, amended and restated, supplemented or otherwise modified from time to time, the "Credit Agreement"; terms defined therein, unless otherwise defined herein, being used herein as therein defined) among the Borrower, certain Lender Parties party thereto, Salomon Smith Barney Inc. ("SSBI") as Book Manager, SG, as Administrative Agent and as Collateral Agent, SSBI, SG Cowen Securities Corporation ("SG Cowen") and Deutsche Bank Securities Inc., as Arrangers, SSBI and SG as Syndication Agents, and (ii) the Security Agreement dated as of April 28, 2000 (as amended, amended and restated, supplemented or otherwise modified from time to time, the "Security Agreement"; terms defined therein, unless otherwise defined herein, being used herein as therein defined) made by the Grantors from time to time party thereto in favor of the Collateral Agent for the Secured Parties.

WHEREAS, as a condition precedent to the making of Advances and the issuance of Letters of Credit by the Lender Parties under the Credit Agreement from time to time, each Grantor has executed and delivered that certain Security Agreement dated April 28, 2000 made by the Grantors to the Collateral Agent (as amended, amended and restated, supplemented or otherwise modified from time to time, the "Security Agreement").

WHEREAS, under the terms of the Security Agreement, Grantors have granted a security interest in, among other property, certain intellectual property of the Grantors to the Collateral Agent for the ratable benefit of the Secured Parties, and have agreed as a condition thereof to execute this IP Security Agreement covering such intellectual property for recording with the U.S. Patent and Trademark Office, the United States Copyright Office and other governmental authorities.

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, each Grantor agrees as follows:

SECTION 1. <u>Grant of Security</u>. Each Grantor hereby grants to the Collateral Agent for the ratable benefit of the Secured Parties a security interest in and to all of such Grantor's right, title and interest in and to the following (the "Collateral"):

(i) the United States, international, and foreign patents, patent applications and patent licenses set forth in Schedule A hereto (as such Schedule A may be supplemented from time to time by supplements to the Security Agreement and this IP Security Agreement, each such supplement being in substantially the form of Exhibit G to the Security Agreement (an "IP Security Agreement Supplement"), executed and delivered by such Grantor to the Collateral Agent from time to time), together with all reissues, divisions, continuations, continuations-in-part, extensions and reexaminations thereof, and all rights therein provided by international treaties or conventions (the "Patents");

Amkor Intellectual Property Security Agreement NYDOCS03/523330

Amkor Intellectual Property Security Agreement NYDOCS03/523330

- the United States and foreign trademark and service mark registrations, applications, and licenses set forth in Schedule B hereto (as such Schedule B may be supplemented from time to time by IP Security Agreement Supplements executed and delivered by such Grantor to the Collateral Agent from time to time) (the "Trademarks");
- the copyrights, United States and foreign copyright registrations and applications and copyright licenses set forth in Schedule C hereto (as such Schedule C may be supplemented from time to time by IP Security Agreement Supplements executed and delivered by such Grantor to the Collateral Agent from time to time) (the "Copyrights");
- (iv) any and all claims for damages for past, present and future infringement, misappropriation or breach with respect to the Patents, Trademarks and Copyrights, with the right, but not the obligation, to sue for and collect, or otherwise recover, such damages; and
 - any and all proceeds of the foregoing. (v)

Notwithstanding the foregoing, the term "Collateral" does not include any license or contract right to the extent that (i) the granting of a security interest therein would be contrary to applicable law or (ii) such license or contract right is not assignable according to its terms (but only to the extent any such prohibition is enforceable under applicable law, including, without limitation, Section 9-318(4) of the N.Y. Uniform Commercial Code).

Section 2. Security for Obligations. The grant of a security interest in, the Collateral by each Grantor under this IP Security Agreement secures the payment of all Obligations of such Grantor now or hereafter existing under or in respect of the Loan Documents, whether direct or indirect, absolute or contingent, and whether for principal, reimbursement obligations, interest, premiums, penalties, fees, indemnifications, contract causes of action, costs, expenses or otherwise.

SECTION 3. Recordation. Each Grantor authorizes and requests that the Register of Copyrights, the Commissioner of Patents and Trademarks and any other applicable government officer record this IP Security Agreement.

SECTION 4. Execution in Counterparts. This Agreement may be executed in any number of counterparts, each of which when so executed shall be deemed to be an original and all of which taken together shall constitute one and the same agreement.

SECTION 5. Grants, Rights and Remedies. This IP Security Agreement has been entered into in conjunction with the provisions of the Security Agreement. Each Grantor does hereby acknowledge and confirm that the grant of the security interest hereunder to, and the rights and remedies of, the Collateral Agent with respect to the Collateral are more fully set forth in the Security Agreement, the terms and provisions of which are incorporated herein by reference as if fully set forth herein.

SECTION 6. Governing Law. This IP Security Agreement shall be governed by, and construed in accordance with, the laws of the State of New York.

Amkor Intellectual Property Security Agreement
NYDOCS03/523330

IN WITNESS WHEREOF, each Grantor has caused this Agreement to be duly executed and delivered by its officer thereunto duly authorized as of the date first above written.

AMKOR TECHNOLOGY, INC.

Name: Kenneth

Title: Chief Financial Officer

Address for Notices:

Goshen Corporate Park 1345 Enterprise Drive West Chester, PA 19380

GUARDIAN ASSETS, INC.

Rν

Name: Kenneth T. Joyce

Title: Chief Financial Officer

Address for Notices:

Goshen Corporate Park 1345 Enterprise Drive West Chester, PA 19380

Amkor Intellectual Property Security Agreement NYDOCS03/523330

PATENT REEL: 011122 FRAME: 0780

RECORDED: 10/10/2000