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09/20/00

Case Docket No. 001204

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To the Director of Patents and Trademarks.
Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Takeshi OTSUKA, Kuniyuki TANI and Atsushi WADA

Additional name(s) of conveying party(ies) attached? NO

09/665/678

2. Name and address of receiving party(ies):

Name: **SANYO ELECTRIC CO., LTD.**

Street Address: 5-5, Keihanhondori 2-chome,
Moriguchi-shi, Osaka-fu 570-8677, Japan

Additional name(s) & address(es) attached? NO

MRD 9/20/00

3. Nature of conveyance: **Assignment**

Execution Date: **September 6, 2000**

4. Title: **SIGNAL PROCESSING CIRCUIT AND SEMICONDUCTOR
INTEGRATED CIRCUIT**

5. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: **September 6, 2000**

A. Patent Application No.(s) B. Patent No.(s)

Additional numbers attached? NO

6. Name and address of party to whom correspondence concerning document should be mailed:

Name: Armstrong, Westerman, Hattori,
McLeland & Naughton
Suite 1000
1725 K Street, N.W.
Washington, D.C. 20006

7. Total number of applications and patents involved: 1

8. Total fee (37 CFR 3.41). \$ **40.00**

XX Check enclosed

9. Deposit Account No.: 01-2340

10. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.



Date: September 20, 2000

William G. Kratz, Jr.
Reg. No.: 22,631

Total number of pages including a cover sheet: 4

WGK/ll

U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$ 1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter ASSIGNOR) by

SANYO ELECTRIC CO., LTD. of 5-5 Keihanhondori 2-chome, Moriguchi-shi, Osaka-fu
570-8677, Japan

(hereinafter ASSIGNEE), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled

SIGNAL PROCESSING CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT

for which application for Letters Patent of the United States was executed on even date herewith unless otherwise indicated below:

*filed on _____, Serial No. _____.

(Armstrong, Westerman, Hattori, McLeland & Naughton is hereby authorized to insert the series code, serial number and/or filing date hereon, when known)

and all Letters Patent of the United States to be obtained therefor on said application or any continuation, divisional, substitute, reissue or reexamination thereof for the full term or terms for which the same may be granted.

The ASSIGNOR agrees to execute all papers necessary in connection with the application and any continuation, divisional, reissue or reexamination applications thereof and also to execute separate assignments in connection with such applications as the ASSIGNEE may deem necessary or expedient.

The ASSIGNOR agrees to execute all papers necessary in connection with any interference, litigation, or other legal proceeding which may be declared concerning this application or any continuation, divisional, reissue or reexamination thereof or Letters Patent or reissue patent issued thereon and to cooperate with the ASSIGNEE in every way possible in obtaining and producing evidence and proceeding with such interference, litigation, or other legal proceeding.

IN WITNESS WHEREOF, the undersigned inventor(s) has(have) affixed his/her/their signature(s).

<u>Takeshi Otsuka</u> (SIGNATURE)	<u>Takeshi Otsuka</u> (TYPE NAME)	<u>Sep. 6. 2000</u> (DATE)
<u>Kuniyuki Tani</u> (SIGNATURE)	<u>Kuniyuki Tani</u> (TYPE NAME)	<u>Sep. 6. 2000</u> (DATE)
<u>Atsushi Wada</u> (SIGNATURE)	<u>Atsushi Wada</u> (TYPE NAME)	<u>Sep. 6. 2000</u> (DATE)