

ULP

IN THE UNIT

10-25-2000

FFICE

Docket No.: VLSI-3406

10.12.00



101496367

I hereby certify that this transmittal of the below described document is being deposited in the U.S. Patent and Trademark Office, Assignment Branch, North Tower Building, Suite 10C35, Washington, D.C., 20231, on the below date of deposit.

Date of Deposit:	10/06/00	Name of Person Making the Deposit:	JULIE GIBSON	Signature of the Person Making the Deposit:	<i>Julie M. Gibson</i>
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The Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

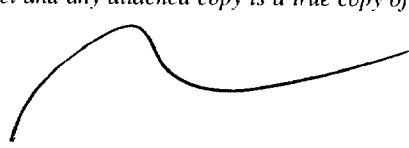
Please record the attached original document(s) or copy(ies) thereof:

Transmittal of an Assignment

- Name of conveying party(ies): TODD MITCHELL, MARK W. HALEY
Serial No.: 09/586,411 Group Art Unit:
Filed: 06/01/00 Examiner:
- Name and Address of Receiving party(ies): PHILIPS ELECTRONICS NORTH AMERICAN CORPORATION
1251 AVENUE OF THE AMERICAS, NEW YORK, NEW YORK 10020-1104
- Nature of Conveyance: Assignment
Execution Date: 09/08/00, 09/08/00
- New Patent Application entitled: USE OF SILICIDE BLOCKING LAYER TO CREATE HIGH VALUED RESISTOR AND DIODE FOR SUB-1V BANDGAP
- Name and address of party to whom correspondence concerning document should be mailed:
Name: John P. Wagner
Address: Wagner, Murabito & Hao LLP
Two North Market Street, Third Floor, San Jose, CA 95113
Phone: (408) 938-9060
- Total Number of applications and patents involved: ONE
- Fee Calculation (for other than a small entity)
Assignment Recordation Fee, per property 1 X \$40.00
Total Fees (37 CFR 3.41) \$40.00
X The amount of \$ 40.00 is enclosed
- At any time during the pendency of this application, please charge any additional fees required or credit any overpayments to Deposit Account 23-0085. A duplicate copy of this transmittal is enclosed.
- Statement and signature:

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Date: 10/6/00


Name John P. Wagner
Reg. No. 35,398

Assignment to PHILIPS Electronics North American Corporation

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we

Mark W. Haley

do hereby sell, assign and transfer unto PHILIPS Electronics North American Corporation (hereinafter called PHILIPS), a Delaware Corporation having its principal place of business at 1251 Avenue of the Americas, New York, New York 10020-1104, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled:

USE OF SILICIDE BLOCKING LAYER TO CREATE HIGH VALUED RESISTOR AND DIODE FOR SUB-1V BANDGAP

..... filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefore; or

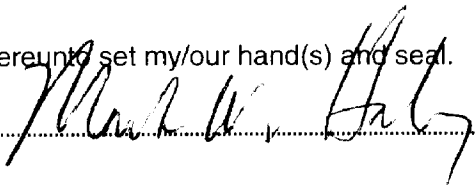
Serial No.: 09/586,411 filed on 06/01/00 and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefore;

Further, I/we have agreed to assign to PHILIPS all inventions (except as otherwise limited by law) which relate to PHILIPS business and which were first conceived or actually reduced to practice during my/our employment by PHILIPS;

And for the above consideration, I/we agree promptly upon request of PHILIPS, its successors or assigns, to execute and deliver without further compensation any power of attorney, assignment, application, whether original, continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to PHILIPS, its successors and assigns, the inventions described in said application and all patent rights therein, in the United States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the expenses which may be incurred by me/us in lending such cooperation and assistance are paid by PHILIPS;

I/we further covenant with PHILIPS, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned;

In witness whereof, I/we hereunto set my/our hand(s) and seal.

Inventor's Signature:  Date: 9/8/2000

Assignment to PHILIPS Electronics North American Corporation

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we

Todd Mitchell

do hereby sell, assign and transfer unto PHILIPS Electronics North American Corporation (hereinafter called PHILIPS), a Delaware Corporation having its principal place of business at 1251 Avenue of the Americas, New York, New York 10020-1104, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled:

USE OF SILICIDE BLOCKING LAYER TO CREATE HIGH VALUED RESISTOR AND DIODE FOR SUB-1V BANDGAP

..... filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefore; or

Serial No.: 09/586,411 filed on 06/01/00 and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefore;

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In witness whereof, I/we hereunto set my/our hand(s) and seal.

Inventor's Signature: Todd Mitchell

Date: 9/8/2000