

11-08-2000

IN



AND TRADEMARK OFFICE

Docket No.: CYP-CD00054.US.P

101509115

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the U.S. Patent and Trademark Office, Assignment Branch, North Tower Building, Suite 10C35, Washington, D.C., 20231, on the below date of deposit.

Date of Deposit:	10/04/00	Name of Person Making the Deposit:	ELIZABETH HENSLEY	Signature of the Person Making the Deposit:	<i>Elizabeth Hensley</i>
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The Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Please record the attached original document(s) or copy(ies) thereof:

10-4-00

Transmittal of an Assignment

916 U.S. PTO  
09/684159  
10/04/00

1. Name of conveying party(ies): James Daniel Merchant, Gordon Carskadon, Brian P. Evans, Jeffrey Scott Hunt, Anup Nayak and Andrew Wright  
Serial No.:  
Filed: 10/04/00  
Group Art Unit:  
Examiner:

2. Name and Address of Receiving party(ies) Cypress Semiconductor Corporation  
3901 N. First Street, San Jose, CA 95134

3. Nature of Conveyance: Assignment  
Execution Date: 09/18/00, 09/18/00, 09/18/00, 09/18/00, 09/19/00 and 09/15/00

4. New Patent Application entitled: METHOD AND SYSTEM FOR IDENTIFYING CONFIGURATION  
CIRCUIT ADDRESSED IN A SCHEMATIC HIERARCHY

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Ronald M. Pomeranke  
Address: Wagner, Murabito & Hao LLP  
Two North Market Street, Third Floor, San Jose, CA 95113  
Phone: (408) 938-9060

09684159

6. Total Number of applications and patents involved: ONE

7. Fee Calculation (for other than a small entity)

Assignment Recordation Fee, per property

1 X \$40.00

**Total Fees** (37 CFR 3.41)

\$40.00

x The amount of \$ 40.00 is enclosed

8. At any time during the pendency of this application, please charge any additional fees required or credit any overpayments to Deposit Account 23-0085. A duplicate copy of this transmittal is enclosed.

9. Statement and signature:

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

*Ronald M. Pomeranke*

Name: Ronald M. Pomeranke  
Reg. No. 43,009

Date: 10/04/00

## Assignment to Cypress Semiconductor Corporation

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we

James Daniel Merchant

do hereby sell, assign, and transfer unto Cypress Semiconductor Corporation (hereinafter called Cypress), a Delaware Corporation, having its principal place of business at 3901 N. First Street, San Jose, California 95134, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled:

METHOD AND SYSTEM FOR IDENTIFYING CONFIGURATION CIRCUIT ADDRESSES IN A SCHEMATIC HIERARCHY

☒ filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor; or

Serial No.: \_\_\_\_\_ filed on \_\_\_\_\_ and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor;

And for the above consideration, I/we agree promptly upon request of Cypress, its successors or assigns, to execute and deliver without further compensation any power of attorney, assignment, application, whether original, continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to Cypress, its successors and assigns, the inventions described in said application and all patent rights therein in the United States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the expenses which may be incurred by me/us in lending such cooperation and assistance are paid by Cypress.

I/we further covenant with Cypress, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned;

In witness whereof, I/we hereunto set my/our hand(s) and seal:

Inventor's Signature: James Daniel Merchant Date: 7/18/00

# Assignment to Cypress Semiconductor Corporation

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we

Brian P. Evans

do hereby sell, assign, and transfer unto Cypress Semiconductor Corporation (hereinafter called Cypress), a Delaware Corporation, having its principal place of business at 3901 N. First Street, San Jose, California 95134, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled:

METHOD AND SYSTEM FOR IDENTIFYING CONFIGURATION CIRCUIT ADDRESSES IN A SCHEMATIC HIERARCHY

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I/we further covenant with Cypress, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned;

In witness whereof, I/we hereunto set my/our hand(s) and seal:

Inventor's Signature: Brian P. Evans Date: 18 Sep 2000

# Assignment to Cypress Semiconductor Corporation

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we

Gordon Carskadon

do hereby sell, assign, and transfer unto Cypress Semiconductor Corporation (hereinafter called Cypress), a Delaware Corporation, having its principal place of business at 3901 N. First Street, San Jose, California 95134, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled:

METHOD AND SYSTEM FOR IDENTIFYING CONFIGURATION CIRCUIT ADDRESSES IN A SCHEMATIC HIERARCHY

☒ filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor; or

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I/we further covenant with Cypress, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned;

In witness whereof, I/we hereunto set my/our hand(s) and seal:

Inventor's Signature: *Gordon Carskadon* Date: 18 SEP MM

# Assignment to Cypress Semiconductor Corporation

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we

Jeffery Scott Hunt

do hereby sell, assign, and transfer unto Cypress Semiconductor Corporation (hereinafter called Cypress), a Delaware Corporation, having its principal place of business at 3901 N. First Street, San Jose, California 95134, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled:

METHOD AND SYSTEM FOR IDENTIFYING CONFIGURATION CIRCUIT ADDRESSES IN A SCHEMATIC HIERARCHY

☒ filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor; or

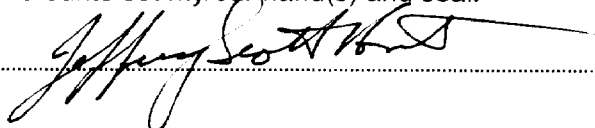
Serial No.: \_\_\_\_\_ filed on \_\_\_\_\_ and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor;

And for the above consideration, I/we agree promptly upon request of Cypress, its successors or assigns, to execute and deliver without further compensation any power of attorney, assignment, application, whether original, continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to Cypress, its successors and assigns, the inventions described in said application and all patent rights therein in the United States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the expenses which may be incurred by me/us in lending such cooperation and assistance are paid by Cypress.

I/we further covenant with Cypress, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned;

In witness whereof, I/we hereunto set my/our hand(s) and seal:

Inventor's Signature: \_\_\_\_\_



Date: \_\_\_\_\_

9/18/00

## Assignment to Cypress Semiconductor Corporation

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we

Anup Nayak

do hereby sell, assign, and transfer unto Cypress Semiconductor Corporation (hereinafter called Cypress), a Delaware Corporation, having its principal place of business at 3901 N. First Street, San Jose, California 95134, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled:

METHOD AND SYSTEM FOR IDENTIFYING CONFIGURATION CIRCUIT ADDRESSES IN A SCHEMATIC HIERARCHY

☒ filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor; or

Serial No.: \_\_\_\_\_ filed on \_\_\_\_\_ and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor;

And for the above consideration, I/we agree promptly upon request of Cypress, its successors or assigns, to execute and deliver without further compensation any power of attorney, assignment, application, whether original, continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to Cypress, its successors and assigns, the inventions described in said application and all patent rights therein, in the United States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the expenses which may be incurred by me/us in lending such cooperation and assistance are paid by Cypress.

I/we further covenant with Cypress, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned;

In witness whereof, I/we hereunto set my/our hand(s) and seal:

Inventor's Signature: Anup Nayak Date: 9/19/2000

# Assignment to Cypress Semiconductor Corporation

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we

Andrew Wright

do hereby sell, assign, and transfer unto Cypress Semiconductor Corporation (hereinafter called Cypress), a Delaware Corporation, having its principal place of business at 3901 N. First Street, San Jose, California 95134, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled:

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I/we further covenant with Cypress, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned;

In witness whereof, I/we hereunto set my/our hand(s) and seal:

Inventor's Signature: *Andrew Wright*

Date: 09/15/2000