

11-24-2000

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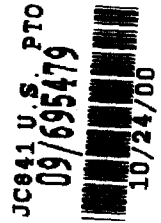


TRADEMARK OFFICE

101526658

Attorney Docket No. 7268-000226

COVER SHEET FOR RECORDAL OF DOCUMENT (PATENT)



Hon. Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. 3.31, enclosed herewith is a document for recordal in this case.
The following information is provided:

(1) **Name Of Party Conveying The Interest:**

Ching-Yuan Wu

(2) **Name And Address Of Party Receiving The Interest:**

Silicon-Based Technology Corp.
1F, No. 23, R&D Rd. 1
Science-Based Industrial Park
Hsinchu, Taiwan, R.O.C.

09/695479

(3) **Description Of The Transaction To Be Recorded:**

☒ Assignment ☐ License
☐ Change of Name ☐ Other _____

(4) **Application(s) and/or Patent(s) Against Which Enclosure Is To Be Recorded:**

☐ Serial Number _____, filed _____
☒ Application being filed concurrently herewith.
☐ Patent Number _____, issued _____

(5) **Name And Address Of The Party To Whom Correspondence Concerning The Request To Record Should Be Mailed:**

HARNESS, DICKEY & PIERCE, P.L.C.
P.O. Box 828
Bloomfield Hills, MI 48303

(6) **Number Of Applications and/or Patents Identified In The Cover Sheet And Total Recordal Fee:**

Number of Applications/Patents: . 1

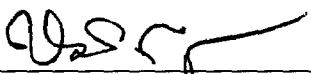
Total Recordal Fee Enclosed: . . . \$40.00

(7) **Date(s) The Document Was Executed:** October 5, 2000

To the best of my knowledge and belief, the foregoing information is true and correct, and if the attached is not an original document, the undersigned verifies that it is a true copy of the original.

If, for some reason, Applicant has not paid a sufficient fee, please charge our Deposit Account No. 08-0750 for any further fees which may be due. A duplicate copy of this document is enclosed.

Respectfully submitted,

By: 
Thomas T. Moga
Reg. No. 34,881
Attorney for Applicant

Date: October 24, 2000

ASSIGNMENT

This assignment agreement is applicable to an invention entitled (Invention Title) METHODS FOR FABRICATING SCALABLE NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE WITH DOUBLE-SIDES ERASE CATHODES

The PATENT RIGHTS referred to in this agreement are:

(check one) ☒ a patent application for this invention, executed by the ASSIGNOR(S) concurrently with this assignment.

☐ U.S. patent application Serial No. _____, filed _____

☐ a U.S. patent application based on PCT International Application

No. _____ filed on (date) _____ (U.S. patent application Serial No. _____, if known).

☐ U.S. patent No. _____, issued _____.

The PATENT RIGHTS also include all divisions reissues, continuations and extensions of the patents and patent applications identified above.

The PATENT RIGHTS assigned under this agreement are:

(check one) ☒ U.S. patent rights only.

☐ Worldwide patent rights. In this case, the assignee shall have the right to claim the benefit of the filing date of any U.S. or foreign patent application for this invention.

The ASSIGNOR(S) referred to in this agreement is (or are) the inventor(s) whose signatures appear on page 2 of this Assignment and any Supplemental Sheet(s).

The ASSIGNEE referred to in this agreement is:

(Name of Assignee) Silicon-Based Technology Corp.

(Address) 1F, NO.23, R&D RD.1, SCIENCE-BASED INDUSTRIAL PARK,

HSINCHU, TAIWAN, R.O.C.

The ASSIGNEE is:

(check one) ☐ An individual.

☐ A Partnership.

☒ A Corporation of TAIWAN, R.O.C. (specify state or country)

☐ (other) _____

The ASSIGNOR(S), in consideration of \$10.00 paid by the ASSIGNEE, and other good and valuable consideration, receipt of which is acknowledged, hereby assign(s) the following rights to the ASSIGNEE, its successors and assigns:

the full and exclusive right to the invention;

the entire right, title and interest in and to the PATENT RIGHTS;

the right to sue and recover for any past infringement; and

the right to claim priority under 35 USC 119, 35 USC 120, or any other applicable provisions, based on any earlier patent applications for this invention.

THIS IS PAGE 2 OF AN ASSIGNMENT FROM THE INVENTOR(S) TO

ASSIGNEE: Silicon-Based Technology Corp.

INVENTION TITLE: METHODS FOR FABRICATING SCALABLE NON-VOLATILE

SEMICONDUCTOR MEMORY DEVICE WITH DOUBLE-SIDES

ERASE CATHODES

As to all U.S. patent applications assigned under this agreement, the ASSIGNOR(S) hereby authorize(s) and request(s) the Commissioner of Patents and Trademark to issue all Letters Patent to the ASSIGNEE as the assignee of the entire right, title and interest, for the sole use and enjoyment of said ASSIGNEE, its successors and assigns.

Further, the ASSIGNOR(S) agree(s) to communicate to said ASSIGNEE, or its representatives, any fact known to the ASSIGNOR(S) with respect to said invention, and testify in any legal proceeding, sign all lawful papers, execute all divisional, continuation, substitute, renewal, reexamination and reissue applications, execute all necessary assignment papers to cause and all Letter Patent to be issued to said ASSIGNEE, make all rightful oaths and generally do everything necessary or desirable to aid said ASSIGNEE, its successors and assigns, to obtain and enforce proper protection for said invention.

The ASSIGNOR(S) authorize(s) the attorneys and agents who have the power of attorney in this application to check any appropriate boxes and to insert the Serial Number and filing date in this document after it has been executed.

Ching-Yuan WU

Name of sole or first inventor

Ching-Yuan Wu

Signature

Oct. 5, 2000

Date

Name of second inventor, if any

Signature

Date

Name of third inventor, if any

Signature

Date

Name of fourth inventor, if any

Signature

Date

Name of fifth inventor, if any

Signature

Date

Name of sixth inventor, if any

Signature

Date

Name of seventh inventor, if any

Signature

Date

Name of eighth inventor, if any

Signature

Date

RECORDED: 10/24/2000

PATENT
REEL: 011259 FRAME: 0882