

11-28-2000

Form-PTO-1595
1-31-92

RECORDATION

Atty. Docket: 2666.54

To the Commissioner of

101529416

attached original documents or copy thereof.

1. Name of conveying party(ies):

MARVELL SEMICONDUCTOR, INC. 10-27-01

Additional name(s) of conveying party(ies) attached?

☐ Yes☒ No

3. Nature of conveyance:

☒ Assignment☐ Merger☐ Security Agreement☐ Change of Name☐ Other

Execution Date: October 26, 2000

2. Name and address of receiving party(ies):

Name: MARVELL TECHNOLOGY GROUP, LTD.

Foreign Address: Clarendon House, 2 Church Street

P.O. Box 1022

Hamilton HM DX, Bermuda

Additional name(s) & address(es) attached? ☐ Yes ☒ No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: October 27, 2000

A. Patent Application Number:

Filing Date:

09/697714

B. Title of Invention:

GATE CAPACITOR STRESS REDUCTION IN
CMOS/BICMOS CIRCUIT

Additional numbers attached?

☐ Yes☒ NoJC927 U.S. PTO
09/697714
10/27/00

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Fitzpatrick, Cella, Harper & Scinto

30 Rockefeller Plaza

New York, New York 10112-3801

Telephone No.: (212) 218-2100

Facsimile No.: (212) 218-2200

6. Number of applications and patents involved:

One

7. Total fee (37 CFR 3.41): . . . \$ 40.00

☒ Enclosed☐ Authorized to be charged to deposit account

8. Deposit account number (for deficiency or excess)

06-1205

(Attach duplicate copy of this page if paying by deposit account):

DO NOT USE THIS SPACE

9. Statement and signature.

*To the best of my knowledge and belief, the foregoing information is true and the attached is the original document or is a true copy of the original document.*Richard P. Bauer, Registration No. 31,588
Name of Person Signing

Signature

October 27, 2000
Date

Total number of pages including cover sheet, attachments, and documents: Two

RPBJAG\vjw

ASSIGNMENT

FOR VALUE RECEIVED,

MARVELL SEMICONDUCTOR, INC., a corporation of California

having a place of business at *645 Almanor Avenue, Sunnyvale, California 94086* (hereinafter called the "Assignor"), which by virtue of an Assignment from *Pantas SUTARDJA* (hereinafter called the "Inventor(s)") is the owner of the entire right, title and interest for all countries in and to certain inventions described below, hereby sells, assigns, transfers and conveys unto

MARVELL TECHNOLOGY GROUP, LTD., a corporation of Bermuda

having a place of business at *Richmond House, 3rd Floor, 12 Parla Ville Road, Hamilton HM DX, Bermuda* its successors, assigns and legal representatives (hereinafter called the "Assignee"),

the entire right, title and interest, for all countries, in and to certain inventions relating to
GATE CAPACITOR STRESS REDUCTION IN CMOS/BICMOS CIRCUIT
(Marvell Ref.: MP0054)

and described in an application for Letters Patent of the United States *filed/executed* by the Inventor(s) on Concurrently herewith, and in and to said application, and all divisions, renewals and continuations thereof, and all Letters Patent of the United States which may be granted, thereon, and all reissues and extensions thereof, and all applications for Letters Patent or other grants of protection of proprietary rights including, but not limited to, inventor's certificate, utility model, utility certificate, patent of importation, registration of patent and industrial design registration which may be filed, and which may be granted, upon said inventions in any countries or regions foreign to the United States, and all reissues, renewals and extensions thereof; and Assignor hereby authorizes and requests the Commissioner for Patents and Trademarks of the United States, and all officials of countries or regions foreign to the United States having authority so to do, to issue all such Letters Patent or other grants of protection upon said inventions to the Assignee or to such nominees as it may designate.

AND Assignor authorizes and empowers the said Assignee or nominees to invoke and claim for any application for such Letters Patent or other grants of protection for said inventions filed by it or them, the benefit of the right of priority provided by the International Convention for the Protection of Industrial Property, as amended, or by a convention which may henceforth be substituted for it, and to invoke and claim such right of priority without further written or oral authorization from Assignor.

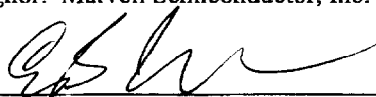
AND Assignor hereby consents that a copy of this Assignment shall be deemed a full and formal equivalent of any assignment, consent to file or like document which may be required in any country or region for any purpose and more particularly in proof of the right of the said Assignee or nominees to claim the aforesaid benefit of the right of priority provided by the International Convention for the Protection of Industrial Property, as amended, or by any convention which may henceforth be substituted for it.

AND Assignor hereby covenants that it has the full right to convey the entire right, title and interest herein assigned and that it has not executed and will not execute any agreement in conflict herewith.

AND Assignor hereby covenants and agrees that it will communicate to said Assignee or nominees all facts known to it pertaining to said inventions, and testify in all legal proceedings, sign all lawful papers, execute all divisional, continuing and reissue applications, make all rightful oaths and declarations and in general perform all lawful acts necessary or proper to aid said Assignee or nominees in obtaining, maintaining and enforcing all lawful patent or other grants of protection of said inventions in any and all countries and regions.

IN TESTIMONY WHEREOF the undersigned who is empowered to act in this capacity on behalf of the Assignor has set his hand and seal this 26 day of Oct, 2000

Assignor: Marvell Semiconductor, Inc.

By:  L.S.
Name: Eric B. Janofsky
Title: General Patent Counsel