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12-01-2000

U.S. DEPARTMENT OF COMMERCE  
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THIS DOCUMENT CONTINUES TO GO

ED WITH A NEW APPLICATION IN WHICH CASE  
R PATENTS

TO THE HONORABLE DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE. PLEASE RECORD THE ATTACHED ORIGINAL DOCUMENTS OR COPY THEREOF.

1. Name of conveying party(ies):

(a) Kwon, Yong-hun

(b)

(c)

Additional name(s) of conveying party(ies) attached?

Yes  No

2. Name and address of receiving party(ies):

Name: Samsung Electronics Co., Ltd.

416, Maetan-dong, Paldal-gu

Suwon-city, Kyungki-do

Republic of Korea

10/06/00  
09/680895  
10/06/00

3. Nature of Conveyance:

Assignment  Merger

Security Agreement  Change of Name

Other \_\_\_\_\_

Execution Date: **October 6, 2000**

Name and address of receiving party(ies):

Name:

Street Address:

City:

Country:

4. Application number(s) or patent number(s): 9/680895

If this document is being filed together with a new application, the execution date of the application is: **October 6, 2000**

A. Patent Application No.(s) -

Title: **Layout Verifying Method For Integrated Circuit Device**

B. Patent No.(s)

N/A

Additional numbers attached?  Yes  No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: David T. Millers

Internal Address: SKJERVEN MORRILL MacPHERSON LLP

Street Address: 25 METRO DRIVE, SUITE 700

City SAN JOSE State CA Zip 95110

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41): \$40.00

Authorized to be charged to Deposit Account 19-2386

Charge Deposit Account 19-2386 for any additional fees required for this conveyance and credit deposit account 19-2386 any amounts overpaid

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8. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

David T. Millers Reg. No. 37,396 David Millers 10-6-00

Name of Person Signing Signature Date

Total number of pages comprising cover sheet: 2

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**ASSIGNMENT**

For good and valuable consideration, receipt of which is hereby acknowledged, I, Kwon, Yong-hun of 103-1806, Injung Prince Apt., Sangha 5-ri, Gusung-myeon, Yongin-city, Kyungki-do, Republic of Korea, hereby sell, assign and transfer to Samsung Electronics Co., Ltd., a Korean corporation, having a place of business at 416, Maetan-dong, Paldal-gu, Suwon-city, Kyungki-do, Republic of Korea, its successors and assigns, the entire right, title and interest throughout the world in my invention in

**LAYOUT VERIFYING METHOD FOR INTEGRATED CIRCUIT DEVICE**

for which I have executed a United States patent application on or about the date of this assignment, and all patent applications and patents of every country for said invention, including divisions, reissues, continuations and extensions thereof, and all rights of priority resulting from the filing of said applications; I authorize the above-named assignee to apply for patents of foreign countries for said invention, and to claim all rights of priority without further authorization from me; I agree to execute all papers useful in connection with said United States and foreign applications, and generally to do everything possible to aid said assignee, its successors, assigns and nominees, at their request and expense, in obtaining and enforcing patents for said invention in all countries; and I request the Commissioner of Patents and Trademarks to issue all patents granted for said invention to the above-named assignee, its successors and assigns.

Executed this 8th day of Oct, 2000.

Yong-hun Kwon  
Yong-hun KWON