## 02-13-2001



FORM PTO-1596 /0/3//00 REC	U.S. DEPARTMENT OF COMMERCE			
	Patent and Trademark Office			
To the Honorable Commissioner of Patents and Trademarks:	Please record the attached original documents or copy thereto:			
1. Name of Conveying Parties:	2. Name and address of receiving party:			
Hideo FUJIWARA, Toshimitsu MASUZAWA and Satoshi	Name: Semiconductor Technology Academic Research Center			
OHTAKE				
	Address: 16-10, Shinbashi 6-chome			
Additional name(s) of conveying party(ies) attached?   Yes	Minato-ku			
3. Nature of Conveyance:	Tokyo 105-0004, Japan			
	•			
☐ Security Agreement ☐ Change of Name				
Other				
Execution Date: October 18, 2000				
·	Additional name(s) & address(es) attached?  Yes			
4. Application number(s) or patent number(s):				
If the document is being filed together with a new application,	the execution date of the application is: October 18, 2000			
A. Patent Application No(s).	B. Patent No(s).			
Additional numbers attached?  Yes				
5. Name and address of party to whom correspondence	6. Total number of applications and patents involved:			
concerning document should be mailed:				
Name: MCDERMOTT, WILL & EMERY	7. Total fee (37 CFR 3.41) \$40.00			
Internal Address:	☐ Enclosed			
	Authorized to be charged to deposit account			
Street Address: 600 13th Street, N.W.	8. Deposit account number:			
Sheet radioss.	500417			
City: Washington State: DC Zip: 20005				
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To the best of me knowledge and belief, the foregoing inf	ormation is true and correct and any attached copy is a true copy			
of the original document.	12 0			
of the original as an	October 31, 2000			
Stephen A. Becker, 26,527	Date			
Name and Registration No. of Person Signing	Signature			
Name and Registration No. of 2 data and a second and segment of the second and second	Total number of pages comprising cover sheet: 1			
CMB No. 0851-0011 (exp. 4/94)				
CMB No. 0851-0011 (CAP: 172.)				

PATENT REEL: 011307 FRAME: 0663

<b>Attorney</b>	Docket	No .	
Accorney	DOGKOL		

## **ASSIGNMENT**

WHEREAS, Hideo FUJIWARA, Toshimitsu MASUZAWA and Satoshi OHTAKE,				
hereinafter called the "Assignor," has invented a new and useful invention entitled INTEGRATED CIRCUIT WITH DESIGN FOR TESTABILITY AND METHOD				
FOR DESIGNING THE SAME				
, for which he has:				
(a) filed an application for United States Letters Patent on); or				
(b) executed an application for United States Letters Patent on October 18, 2000; or				
(c) filed a provisional application on				
Semiconductor Technology Academic				
WHEREAS, Research Center , a corporation organized				
and existing under the laws of Japan, having a place of business at:  16-10, Shinbashi 6-chome, Minato-ku, Tokyo 105-0004 Japan				

hereinafter called the "Assignee," is desirous of acquiring the entire right, title and interest in and to said invention, the application above identified, and in, to and under any Letters Patent which may be obtained to said invention, as hereinafter more fully set forth;

NOW, THEREFORE, TO ALL WHOM IT MAY CONCERN, be it known that for and in consideration of the sum of One Dollar (\$1.00), and other valuable and legally sufficient considerations, the receipt of which by said Assignor from the said Assignee is hereby acknowledged, the said Assignor has sold, assigned and transferred, and by these presents do sell, assign and transfer unto the said Assignee, the entire, right, title and interest for the United States in and to the invention and application hereinabove identified, and any Letters Patent of the United States that may issue for said invention, together with the entire right, title and interest in and to said invention and applications for Letters Patent and Letters Patent therefor, in all countries foreign to the United States, including the full right to claim for any such application all benefits and priority rights under any applicable convention; to have and to hold for the sole and exclusive use and benefit of the said Assignee, its successors and assigns, to the full end of the term or terms for which any and all of said Letters Patent for said inventions may issue.

PATENT REEL: 011307 FRAME: 0664 And the said Assignor does hereby covenant and agree, for himself and his legal representatives, that he will assist the said Assignee in the prosecution of the application herein identified; in the making and prosecution of any other applications for Letters Patent that the said Assignee may elect to make covering the invention herein identified, as hereinabove set forth; in vesting in the said Assignee like exclusive title in and to all such other applications and Letters Patent; and in the prosecution of any interference which may arise involving said invention, or any application or Letters Patent herein contemplated; and that they will execute and deliver to the said Assignee any and all additional papers which may be requested by the said Assignee to fully carry out the terms of this Assignment.

The undersigned hereby grant(s) the attorneys of McDermott, Will & Emery the power to insert on this Assignment any further identification which may be necessary or desirable in order to comply with the rules of the United States Patent and Trademark Office for recordation of this document.

And the Commissioner of Patents and Trademarks is hereby authorized and requested to issue Letters Patent to the said Assignee in accordance with the terms of this Assignment.

IN TESTIMONY WHEREOF, the said Assignor has hereunto set his hands and affixed his seal.

Date: (Seal)	October 18, 2000	Hideo FUJIWARA
Date: (Seal)	October 18, 2000	Johnste hangar Toshimitsu MASUZAWA
Date: (Seal)	October 18, 2000	Satoshi OHTAKE
Date: (Seal)		
Date: (Seal)		
Date: (Seal)		-2-

PATENT REEL: 011307 FRAME: 0665