Form PTO-1595 RECORDATION FOR 12-14-2000

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U.S. Department of Commerce Patents & Trademark Office

Attorney Docket No. 108287-00003 Date: December 1, 2000



To the Assistant Commissioner of Patents Please record the attached original documents or copy thereof 2. Name and address of receiving party(ies): Name of conveying party(ies) Name: FUJITSU LIMITED Mitsuru SASAKI 12-1-00 Address: 1-1, Kamikodanaka 4-chome, Nakahara-ku, Kawasaki-shi, Kanagawa 211-8588 Japan Additional name(s) of conveying party(ies) attached? Additional name(s) & address(es) attached? ⊠ No ☐ Yes ☐ Yes ⊠ No 3. Nature of conveyance: ☐ Merger 09/726607 ☐ Change of Name ☐ Security Agreement Other _ Execution Date: November 20, 2000 4. If this document is being filed together with a new application, the execution date of the application is: November 20, 2000 B. Patent No.(s) A. Patent Application No.(s) 6. Total number of applications and patents involved: 5. Name and address of party to whom correspondence concerning document should be mailed: 7. Total fee (37 CFR 3.41)...... \$ 40.00 Name: Arent Fox Kintner Plotkin & Kahn PLLC Street Address: 1050 Connecticut Avenue, N.W., Suite 600 Included in attached check. Any additional fees are authorized to be charged to Washington, D.C. 20036-5339 deposit account 12/04/2000 JBALINAN 00000097 09726607 8. Deposit account number: 01-2300 (Attach duplicate copy of this page if paying by 40.00 OP 03 FC:581 deposit account) DO NOT USE THIS SPACE 9. Statement and signature. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. December 1, 2000 Charles M. Marmelstein Date Name of Person Signing Reg. No. 25,895 Total number of pages including cover sheet, attachments, and document: 2

ARENT FOX KINTNER PLOTKIN & KAHN, PLLC Nikaido, Marmelstein, Murray & Oram Intellectual Property Group

Docket No:

U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventors (hereinafter ASSIGNOR) by:

FUJITSU LIMITED

1-1, Kamikodanaka 4-chome; Nakahara-ku, Kawasaki-shi, Kanagawa 211-8588 Japan

(hereinafter ASSIGNEE), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled:

PATH TRANSISTOR CIRCUIT AND METHOD FOR DESIGNING THE SAME, A DEVICE AND METHOD FOR OPTIMIZING LOGIC CIRCUITS, AND A COMPUTER PRODUCT

for which application for Letters Patent of the	United States was filed on
and assigned Serial No.	(Arent Fox Kintner Plotkin & Kahn, PLLC
	serial number and/or filing date hereon, when known)
and all Letters Patent of the United States	to be obtained therefor on said application or any
	sue or reexamination thereof for the full term or terms
for which the same may be granted.	

The ASSIGNOR agrees to execute all papers necessary in connection with application and any continuing, divisional, reissue or reexamination applications thereof and also to execute separate assignments in connection with such applications as the ASSIGNEE may deem necessary or expedient.

The ASSIGNOR agrees to execute all papers necessary in connection with any interference, litigation, or other legal proceeding which may be declared concerning this application or any continuation, division, reissue or reexamination thereof or Letters Patent or reissue patent issued thereon and to cooperate with the ASSIGNEE in every way possible in obtaining and producing evidence and proceeding with such interference, litigation, or other legal proceeding.

IN WITNESS WHEREOF, the undersigned inventors have affixed their signatures.

Mitsuru Sasaki	misture Jasaki	Nov. 20, 2000
	(SIGNATURE)	(DATE)
<u></u>	(SIGNATURE)	(DATÉ)
	(SIGNATURE)	(DATE)

NO LEGALIZATION REQUIRED

RECORDED: 12/01/2000

PATENT REEL: 011321 FRAME: 0955