

PATENT RECORD

12-21-2000

Attorney Docket No.
L13.12-0129/00-531

12/4/00

COVER :



101558170

To the Honorable Director of
attached original document

Trademark Office: Please record the

1. Name of conveying party(ies):

- (1) Ghasi R. Agrawal
(2) Jerry K. Tanaka
(3)

Additional name(s) of conveying party(ies)
attached? [] Yes [X] No

2. Name and address of receiving
party(ies):

Name: LSI Logic Corporation

Internal Address: _____

Street Address: 1551 McCarthy Boulevard

City Milpitas State CA ZIP 95035

Additional name(s) & address(es) attached?
[] Yes [X] No

3. Nature of Conveyance:

- [X] Assignment [] Merger
[] Security Agreement
[] Change of Name [] Other _____

Execution Date: December 1, 2000

4A. Application No. (s)

09/129020

If this document is being filed together with a
new application, the execution date(s) of the
application is: December 4, 2000

Additional numbers attached? [] Yes [X] No

4B. Patent No. (s)

Additional numbers attached?
[] Yes [] No

5. Name and address of party to whom corres-
pondence concerning document should be mailed:

Name: Gary Edward Ross

Street Address: LSI Logic Corporation

1551 McCarthy Boulevard

M/S D-106

City Milpitas State CA ZIP 95035

6. Total number of applications and
patents involved: [1]

7. Total fee (37 CFR 3.41):\$ 40.00

8. Method of Payment

- [X] Enclosed
[X] The Director is authorized to
charge payment of any additional
recording fees or credit any
overpayment to deposit account
No. 23-1123.

DO NOT USE THIS SPACE

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any
attached copy is a true copy of the original document.

David D. Brush
Name of Person Signing

Signature

Date

12/4/00

Total number of pages submitted: [2]

JP960 U.S. PTO
09/729020
12/04/00

ASSIGNMENT

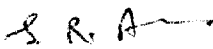
For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, we, the undersigned Ghasi R. Agrawal and Jerry K. Tanaka, do hereby sell, assign, and transfer to **LSI LOGIC CORPORATION**, a corporation of Delaware, having a place of business at 1551 McCarthy Boulevard, Milpitas, California 95035 ("Assignee"), its successors, assigns, and legal representatives, the entire right, title and interest for the United States and all foreign countries, in and to any and all inventions and improvements which are disclosed in the application for United States Letters Patent,

[X] which has been executed by the undersigned concurrently herewith,
[] which was filed on and assigned Application No. ,

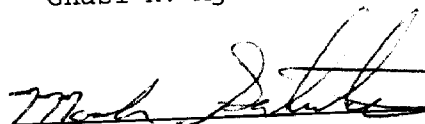
and is entitled: INTEGRATED CIRCUIT MEMORY HAVING COLUMN REDUNDANCY WITH NO TIMING PENALTY and in and to said application and all divisional, continuing, substitute, renewal, reissue, and all other applications for Letters Patent which have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original and reissued patents which have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application; and

Agree that said Assignee may apply for and receive Letters Patent for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will executed all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all said improvements; executed all rightful oaths, assignments, powers of attorney and other papers; communicate to said Assigned, its successors, assigns, and representatives, all facts known to the undersigned relating to said improvements and the history thereof; and generally do everything possible which said Assignee, its successors, assigns or representatives shall consider desirable for aiding in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements and all applications to patents and all patents on said improvements, in said Assignee, its successors, assigns and legal representatives; and

Covenant with said Assignee, its successors, assigns and legal representatives that no assignment, grant mortgage, license, or other agreement affecting the rights and properly herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

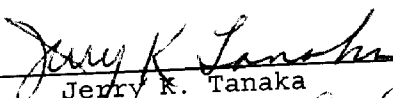
Name: 
Ghasi R. Agrawal

Date 12/01/00

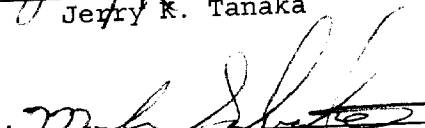
Witness: 

Date 12-01-00

Witness
Printed: Mark Sabatone

Name: 
Jerry K. Tanaka

Date 12/1/00

Witness: 

Date 12-01-00

Witness
Printed: Mark Sabatone