



PATENTS ONLY (ref OG 7/28/92)

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1-23-01

To the Honorable Commissioner of Patents and Trademarks:
Please record the attached original document or copy thereof.

- 1. Name of Party(ies) conveying an interest:
- 2. Name and Address of Party(ies) receiving an interest:

Satoshi IMASU
 Ikuo YOSHIDA
 Tetsuya HAYASHIDA
 Akira YAMAGIWA
 Shinobu TAKEURA



HITACHI, LTD.
 6, Kanda Surugadai 4-chome
 Chiyoda-ku, Tokyo, JAPAN
 and
 HITACHI HOKKAI
 SEMICONDUCTOR, LTD.
 145 Azanakajima, Nanae-cho
 Kameda-gun, Hokkaido, JAPAN

3. Description of the interest conveyed: Assignment

4. Application number(s) or patent number(s).

Additional sheet attached? ___ Yes XXX No

If this document is being filed together with a new application,
the execution date of the application is: _____
DATE

- A. Patent Application No.(s)
- B. Patent No.(s)

09/048,054

5. Name and address of party to whom correspondence concerning document should be mailed:

John R. Mattingly
 Mattingly, Stanger & Malur, P.C.
 104 East Hume Avenue
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6. Number of applications and patents involved: One

7. Amount of fee enclosed or authorized to be charged: \$40.00

8. The Commissioner is hereby authorized to charge Deposit Account No. 50-1417 if no check is attached.

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9. Execution date of attached document: April 3, 1998

10. To the best of my knowledge and belief, I declare under penalty of perjury under the laws of the United States of America that the foregoing information is true and correct and that any attached copy is a true copy of the original document.

John R. Mattingly
Name of Person Signing

John R. Mattingly
Signature

January 23, 2001
Date

Total number of pages being submitted: 2

ASSIGNMENT

For good and valuable consideration, the sufficiency and receipt of which is hereby acknowledged, as a below named inventor, I hereby sell and assign to Hitachi, Ltd. & Hitachi Hokkai Semiconductor, Ltd of Chiyoda-ku, Tokyo, Japan & Nanae-cho, Hokkaido, Japan its successors and assigns or other legal representatives all my rights, title and interest, in and for the United States of America, in and to

PROCESS FOR MOUNTING ELECTRONIC DEVICE AND SEMICONDUCTOR DEVICE,
U.S. Serial No. _____ filled on March 26, 1998

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed by me even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said Hitachi, Ltd. & Hitachi Hokkai Semiconductor, Ltd., its successors, assigns or other legal representatives, to the full end of the term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said _____ Hitachi, Ltd. & Hitachi Hokkai Semiconductor, Ltd.

(Full Name)

(Signature)

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Witness(es)

- All) _____ 2) Eiji Yamaguchi 4) Akira Yamagiwa
- 1) Takashi Kikuchi 3) TSUTOMU NAKAMARU 5) TSUKIO FUNAKI