	TTODNEY DOCKET NO: M-8866
	U.S. DEPARTMENT OF COMMER
RECUP	PATENT AND TRADEMARK C
TO THE HONORABLE DIRECTOR OF THE UNITED STATES PATER ORIGINAL DOCUMENTS OR COPY THEREOF.	619723
Name of conveying parties:	2. Name and address of receiving party:
	621. 621
(a) Sau Ching Wong	Name: Multi Level Memory Technology
	Street Address: 20480 Pacifica Drive, Suite C
Additional names of conveying parties attached?	Country: US
B. Nature of Conveyance:	Name and address of receiving party(ies):
Assignment Merger	Name:
Security Agreement Change of Name	Street Address:
Other	City:
Execution Date: December 1, 2001	Country:
	Additional name(s) & address(es) attached?
04/766272	Yes No
Application number(s) or patent number(s):	
If this document is being filed together with a new application,	the execution date of the application is: <u>January 18, 2001</u>
A. Patent Application No.(s) -	B. Patent No.(s)
Title: Data Coding For Multi-Bit-Per-Cell Memories Having /ariable Numbers of Bits Per Memory Cell	
Additional nu	Imbers attached? Yes No
Name and address of party to whom correspondence concerning document should be mailed:	6. Total number of applications and patents involved: <u>1</u>
lame: David T. Millers	
nternal Address: <u>SKJERVEN_MORRILL_MacPHERSON_LLP</u>	
Street Address: 25 METRO DRIVE, SUITE 700	7. Total fee (37 CFR 3.41): \$40.00
City <u>SAN JOSE</u> State <u>CA</u> Zip <u>95110</u>	Authorized to be charged to Deposit Account 19-2386
	Charge Deposit Account 19-2386 for any additional fees
$\wedge$	required for this conveyance and credit deposit account 19-2386 any amounts overpaid
2/2001 AAHMED1 00000065 192386 09766272	
C:581 (40.00 CH	E THIS SPACE
Statement and signature.	
To the best of my knowledge and belief, the foregoing informa original document.	ation is true and correct and any attached copy is a true copy of the
	AAI AA 1 100 mm
David Millers 37,396	Mullers 1-18-01

PATENT REEL: 011524 FRAME: 0435

## ASSIGNMENT

For good and valuable consideration, receipt of which is hereby acknowledged, we

Sau Ching Wong of 30 Sugar Hill Drive, Hillsborough CA 94010 and Kimberley Johnsen of 11521 Roosevelt Way, Seattle WA 98125

hereby sell, assign and transfer to Multi Level Memory Technology, a California corporation, having a place of business at 20480 Pacifica Drive, Suite C, Cupertino CA 95014-3015, its successors and assigns, the entire right, title and interest throughout the world in our invention in:

Data Coding For Multi-Bit-Per-Cell Memories Having Variable Numbers of Bits Per Memory Cell

for which we have executed a United States patent application on or about the date of this assignment, and all patent applications and patents of every country for said invention, including divisions, reissues, continuations and extensions thereof, and all rights of priority resulting from the filing of said applications; we authorize the above-named assignee to apply for patents of foreign countries for said invention, and to claim all rights of priority without further authorization from us; we agree to execute all papers useful in connection with said United States and foreign applications, and generally to do everything possible to aid said assignee, their successors, assigns and nominees, at their request and expense, in obtaining and enforcing patents for said invention in all countries; and we request the Commissioner of Patents and Trademarks to issue all patents granted for said invention to the above-named assignee, its successors and assigns.

Executed this <u>/O</u> day of	January, 2001.
	San Ching Wing
	Sau Ching Wong

Executed this \_\_\_\_\_ day of \_\_\_\_\_, 2001.

Kimberley Johnsen

708211 vl

PATENT REEL: 011524 FRAME: 0436

Attorney Docket No.: M-8866 US

## ASSIGNMENT

For good and valuable consideration, receipt of which is hereby acknowledged, we

Sau Ching Wong of 30 Sugar Hill Drive, Hillsborough CA 94010 and Kimberley Johnsen of 11521 Roosevelt Way, Seattle WA 98125

hereby sell, assign and transfer to Multi Level Memory Technology, a California corporation, having a place of business at 20480 Pacifica Drive, Suite C, Cupertino CA 95014-3015, its successors and assigns, the entire right, title and interest throughout the world in our invention in:

Data Coding For Multi-Bit-Per-Cell Memories Having Variable Numbers of Bits Per Memory Cell

for which we have executed a United States patent application on or about the date of this assignment, and all patent applications and patents of every country for said invention, including divisions, reissues, continuations and extensions thereof, and all rights of priority resulting from the filing of said applications; we authorize the above-named assignee to apply for patents of foreign countries for said invention, and to claim all rights of priority without further authorization from us; we agree to execute all papers useful in connection with said United States and foreign applications, and generally to do everything possible to aid said assignee, their successors, assigns and nominees, at their request and expense, in obtaining and enforcing patents for said invention in all countries; and we request the Commissioner of Patents and Trademarks to issue all patents granted for said invention to the above-named assignee, its successors and assigns.

Executed this \_\_\_\_\_ day of \_\_\_\_\_, 2001.

Sau Ching Wong

Executed this 12th day of Jonnary , 2001.

708211 v1

**RECORDED: 01/18/2001**