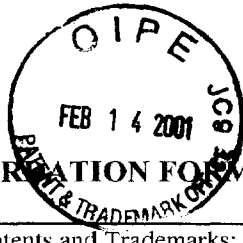


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1) Thomas SKOTNICKI
2) Malgorzata JURCZAK

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2. Name and address of receiving party:

STMicroelectronics S.A.
7, Avenue Gallieni
94250 Gentilly, France

Additional name(s) & address(es) attached?

☐ Yes ☒ No3. Nature of conveyance: XX Assignment Execution Date: 1-2) December 27, 2000

4. Application or patent number(s):

If this document is being filed together with a new application, the execution date of the application is

A. Patent Application No.(s): 09/680,035

B. Patent No.(s):

5. Name and address of party to whom correspondence concerning document should be mailed:

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For good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, each undersigned inventor has sold and assigned, and by these presents hereby sells and assigns, unto **STMicroelectronics S.A.**

name and
address of
assignee

**7, Avenue Galliéni
94250 Gentilly, France**

(hereinafter ASSIGNEE) all right, title and interest for the United States, its territories and possessions in and to this invention relating to

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GATE-ALL-AROUND SEMICONDUCTOR DEVICE AND PROCESS FOR FABRICATING THE SAME

as set forth in this United States Patent Application

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☐ executed concurrently herewith

☐ executed on _____

✓ Serial No. 09/680,035 Filed October 5, 2000

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NAME AND SIGNATURE OF INVENTOR(S)

Name: Thomas SKOTNICKI Signature: *Thomas Skotnicki*

Date: 27/12/00

Name: Malgorzata JURCZAK Signature: *M. Jurczak*

Date: 27/12/2000

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